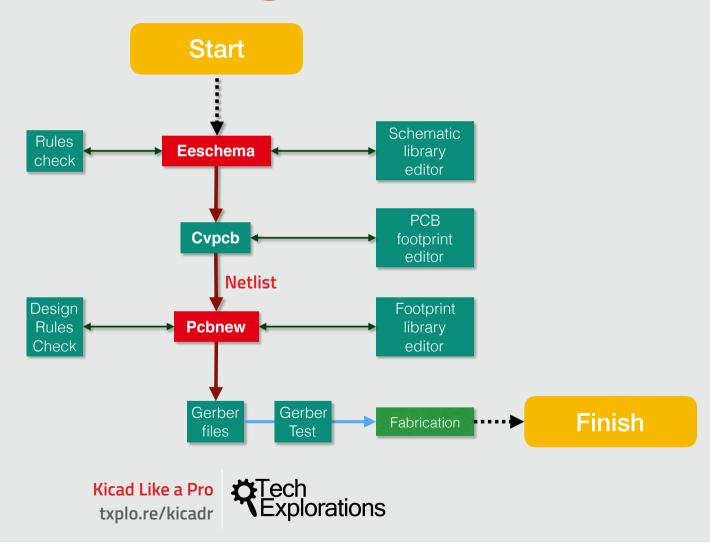
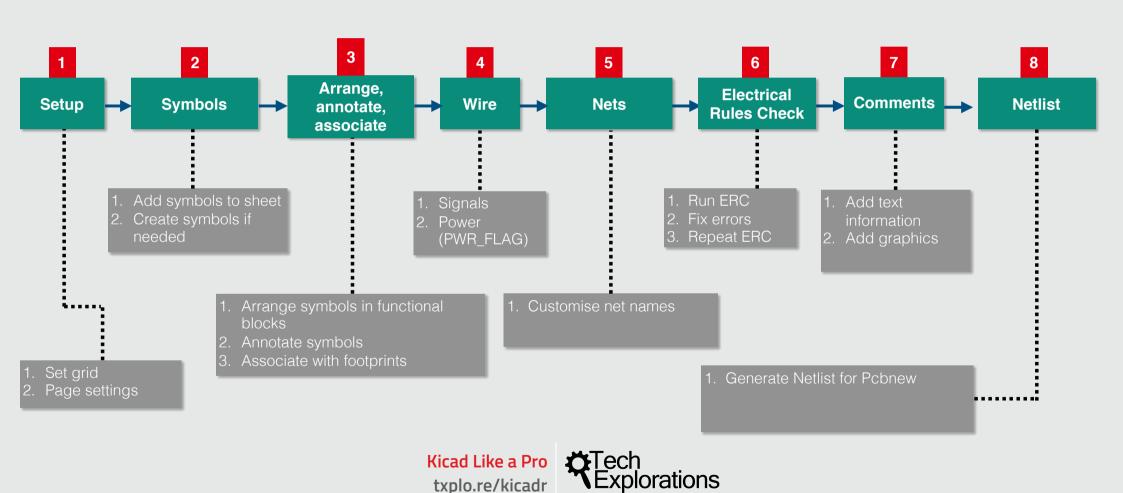
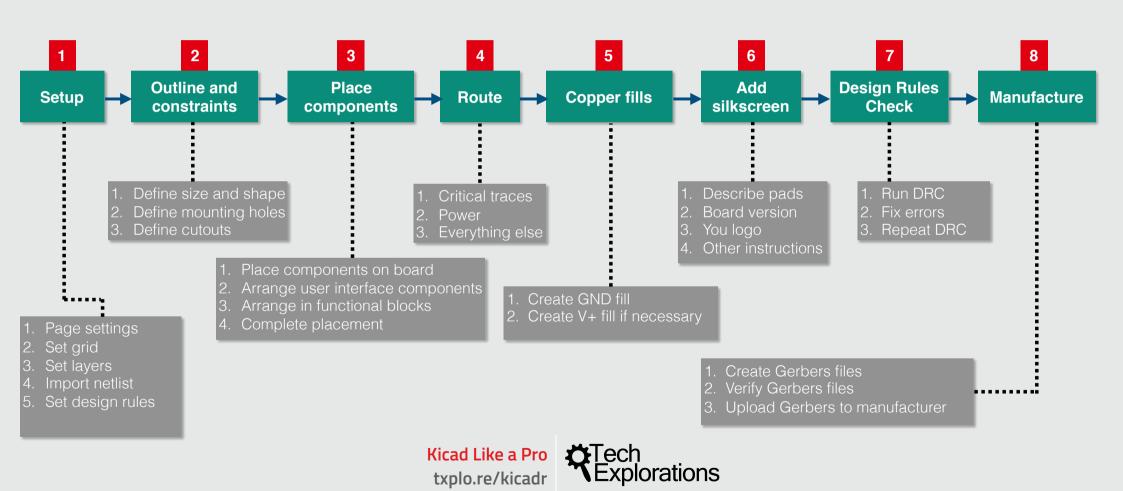
The Kicad design workflow



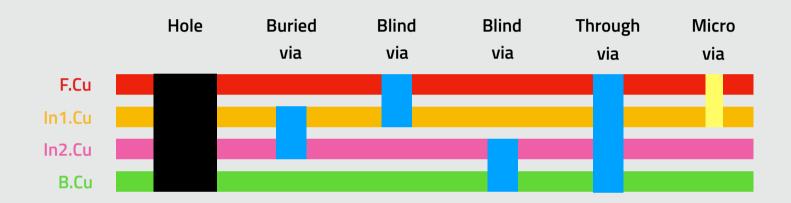
The schematic design process



The PCB layout process



Vias



Holes & pads

