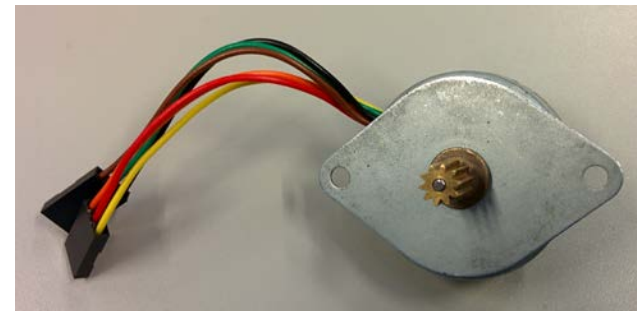
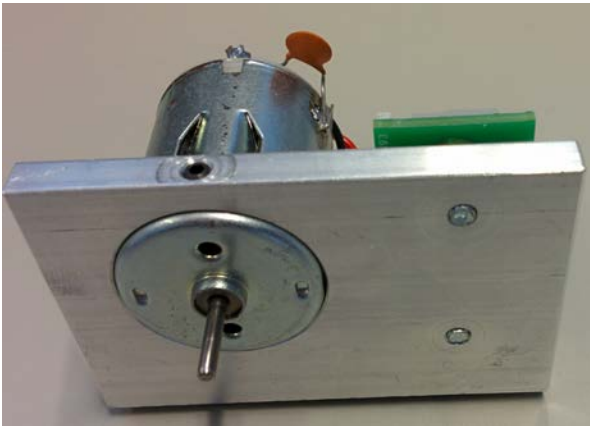
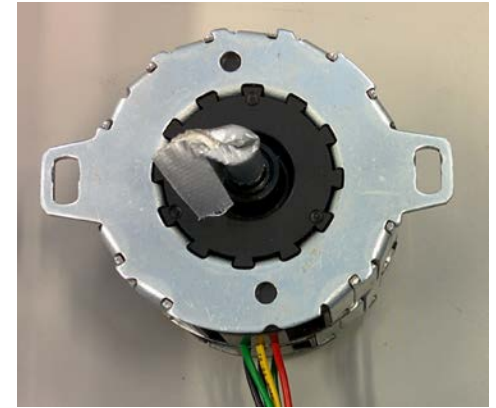


# Motor control Lab experiment

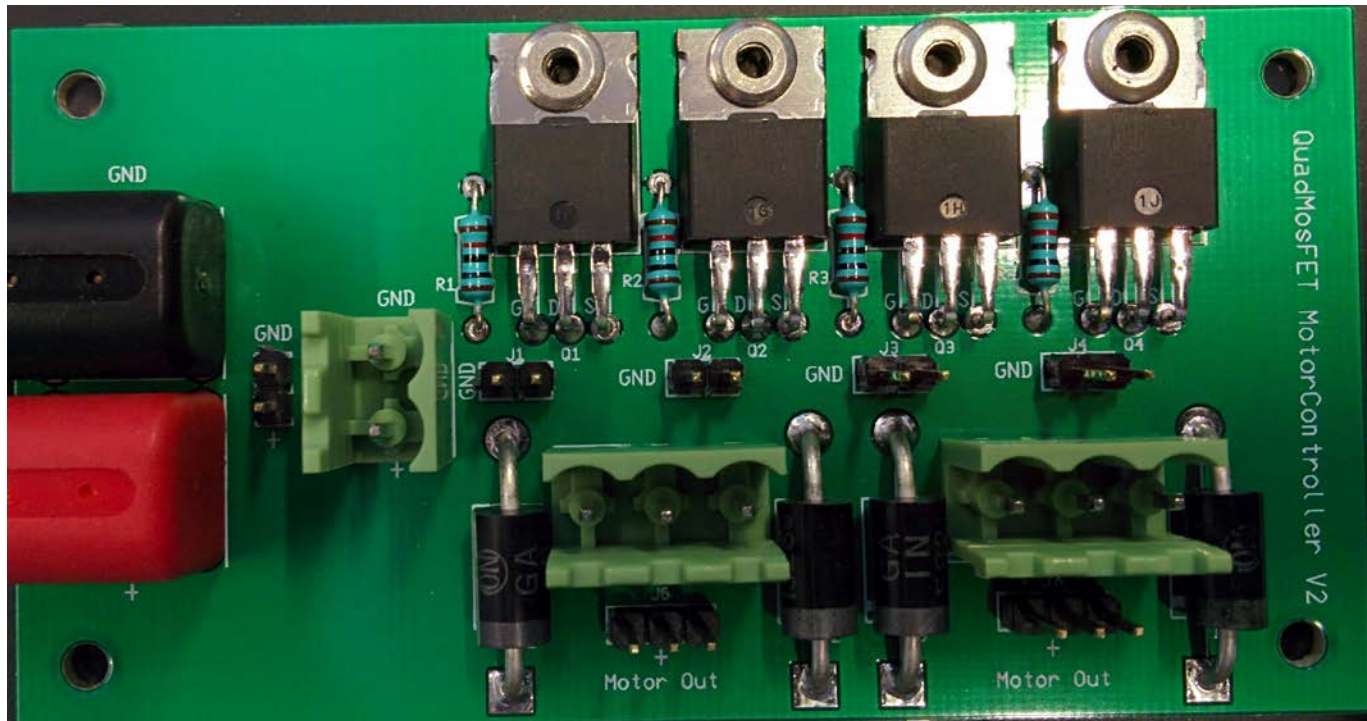


# 3 experiments

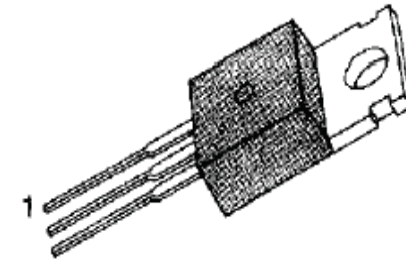
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- DC motor speed control with PWM.
- DC motor speed control with PWM and direction control with an H-Bridge.
- Stepper motor control.
- (+optional: Servo motor)

# MOSFET PCB



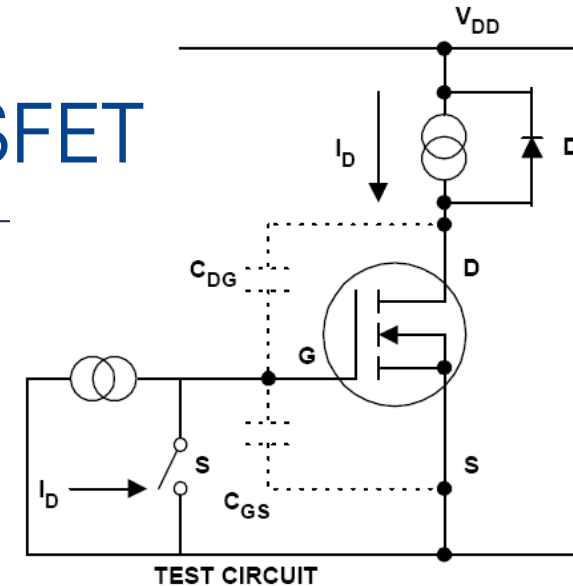
# IRLZ24 N-channel MOSFET



1. Gate 2. Drain 3. Source

Characteristic	Symbol	IRLZ24	IRLZ20	Unit
Drain-Source Voltage (1)	$V_{DS}$	60	50	Vdc
Drain-Gate Voltage ( $R_{GS}=1M\Omega$ )(1)	$V_{DGR}$	60	50	Vdc
Gate-Source Voltage	$V_{GS}$	$\pm 15$		Adc
Continuous Drain Current $T_c=25^\circ\text{C}$	$I_D$	14.0		Adc
Continuous Drain Current $T_c=100^\circ\text{C}$	$I_D$	9.8		Adc
Drain Current - Pulsed (3)	$I_{DM}$	56		Adc
Total Power Dissipation @ $T_c=25^\circ\text{C}$	$P_D$	50		Watts
Derate Above $25^\circ\text{C}$		0.33		W/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{STG}$	-55 to +175		$^\circ\text{C}$
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	$T_L$	300		$^\circ\text{C}$

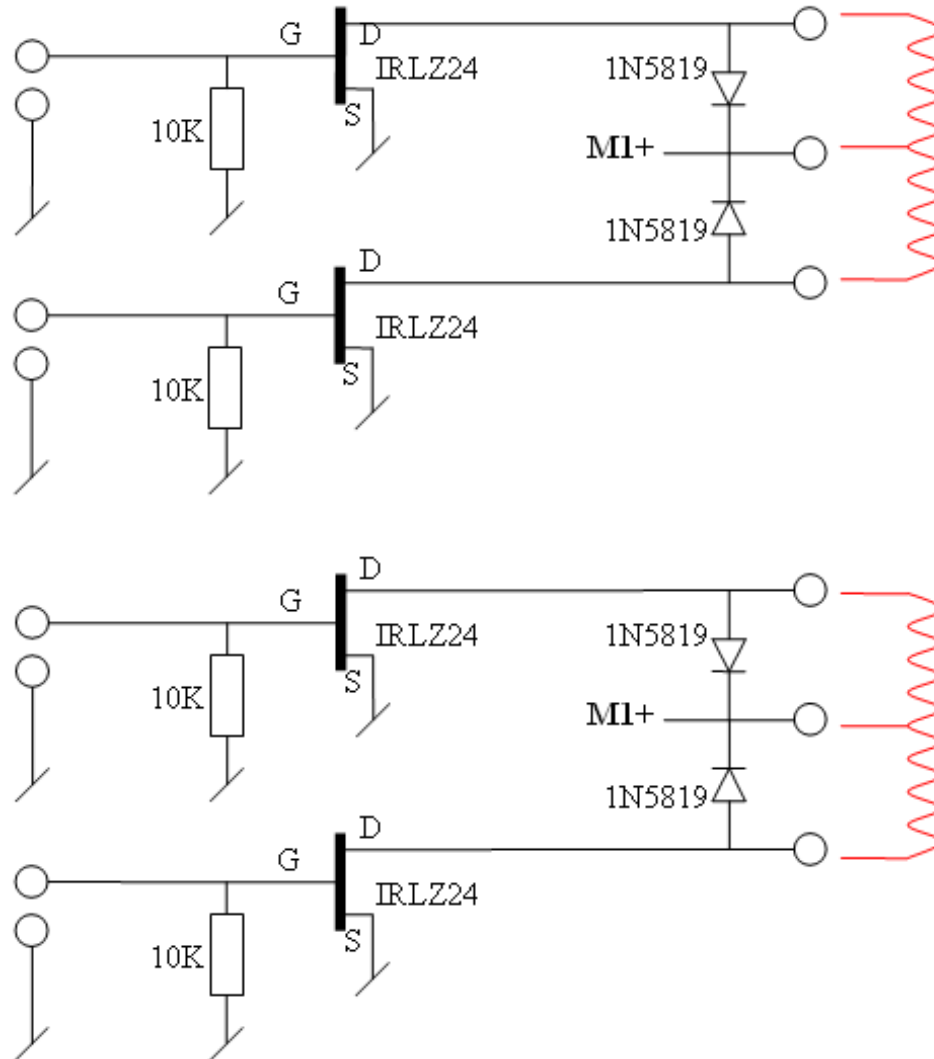
# IRLZ24 N-channel MOSFET



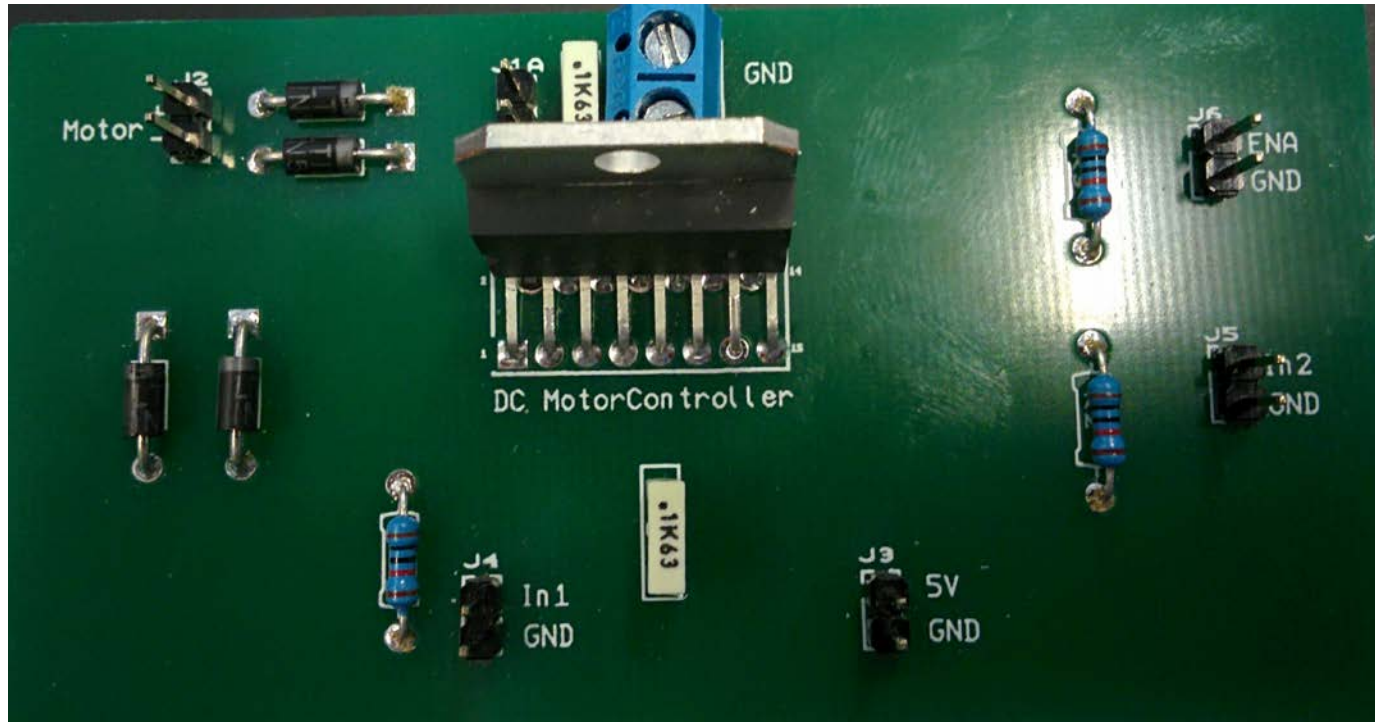
Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage					
	IRLZ24	60	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
	IRLZ20	50	-	-	V	
V <sub>GS(th)</sub>	Gate Threshold Voltage	1.0	-	2.0	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =1mA
I <sub>GSS</sub>	Gate-Source Leakage Forward	-	-	100	nA	V <sub>GS</sub> =15V
I <sub>GSS</sub>	Gate-Source Leakage Reverse	-	-	-100	nA	V <sub>GS</sub> =-15V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	-	-	250	μA	V <sub>DS</sub> =Max. Rating, V <sub>GS</sub> =0V
		-	-	1000	μA	V <sub>DS</sub> =0.8 Max. Rating, V <sub>GS</sub> =0V, T <sub>C</sub> =125°C
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance(2)	-	-	0.15	Ω	V <sub>GS</sub> =5.0V, I <sub>D</sub> =7A
g <sub>fs</sub>	Forward Transconductance (2)	2.0	-	-	Ω	V <sub>DS</sub> ≥15V, I <sub>D</sub> =7A

- 
- There are two revisions of the MOSFET PCB. On the new revision of the PCB, the IRLZ24 is replaced by IRLZ44.
  - For IRLZ44, the gate-threshold voltage can be up to 3V.
  - So the PSoC shall use 5V logic, i.e. no modifications on the PSoC and set output to strong drive (the default).

# MOS FET driver (LAB)

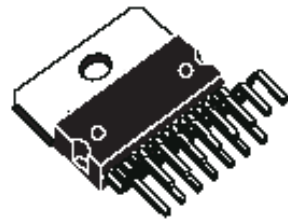


# L298 PCB

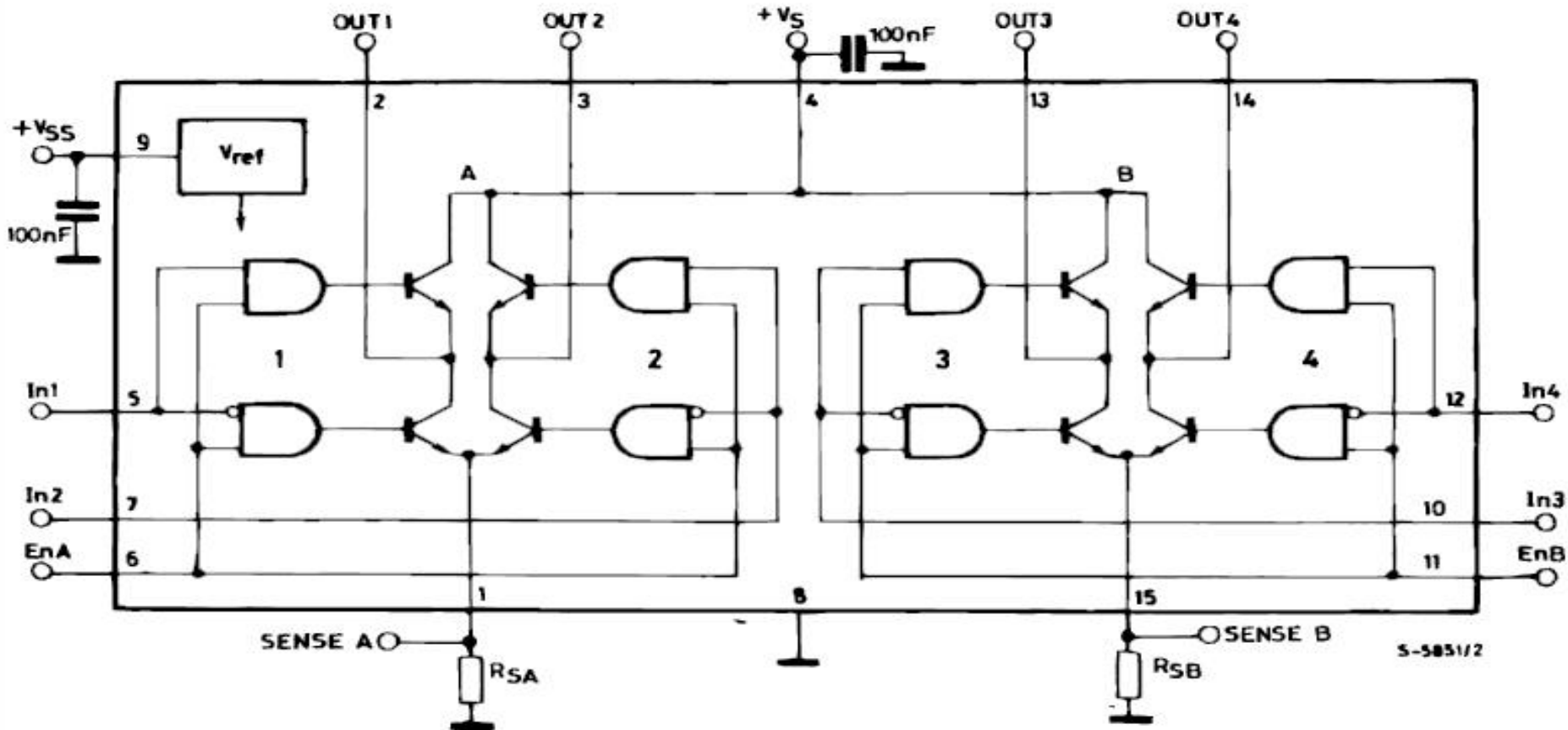




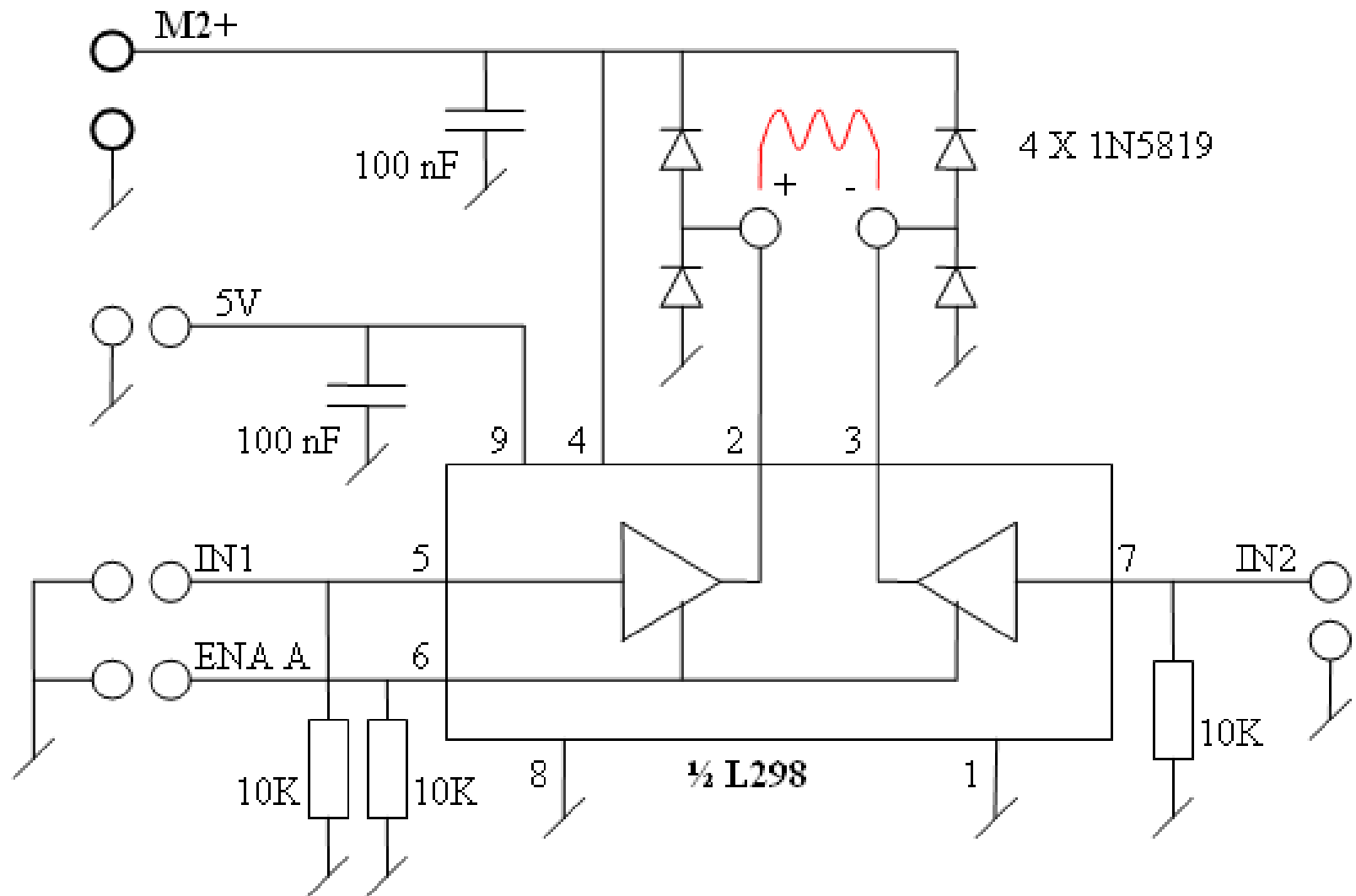
# L298: Dual Full Bridge Driver



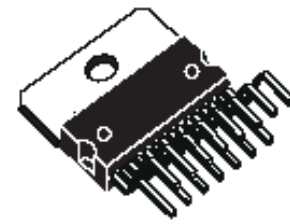
Multiwatt15



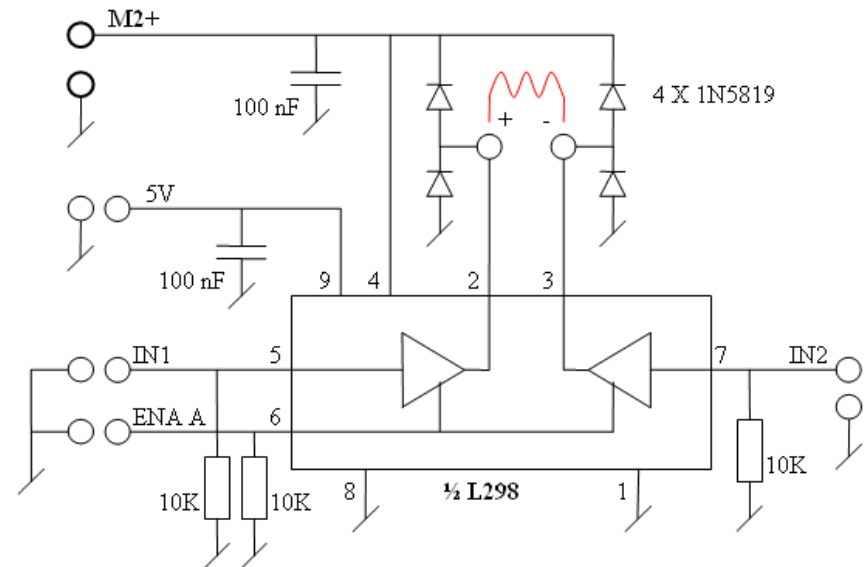
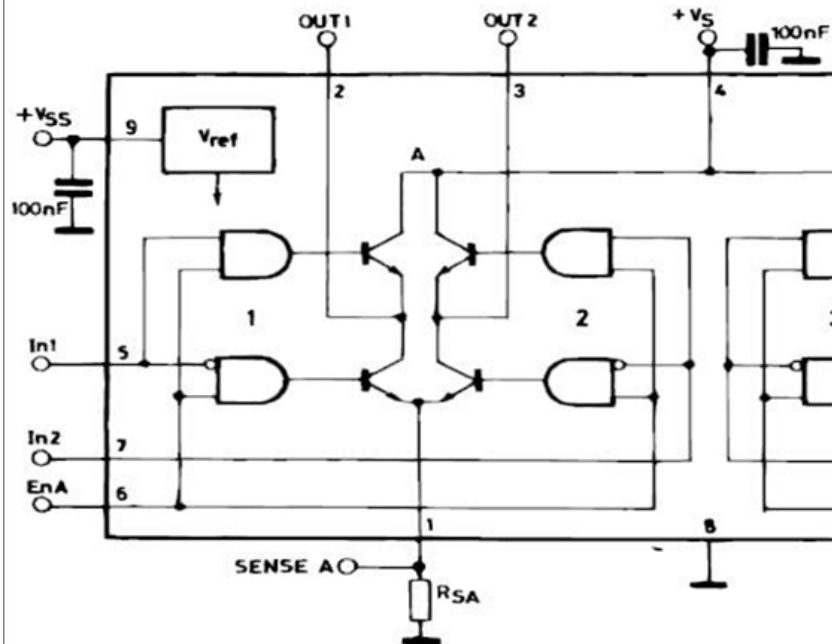
# Lab H-Bridge



# L298: Dual Full Bridge Driver



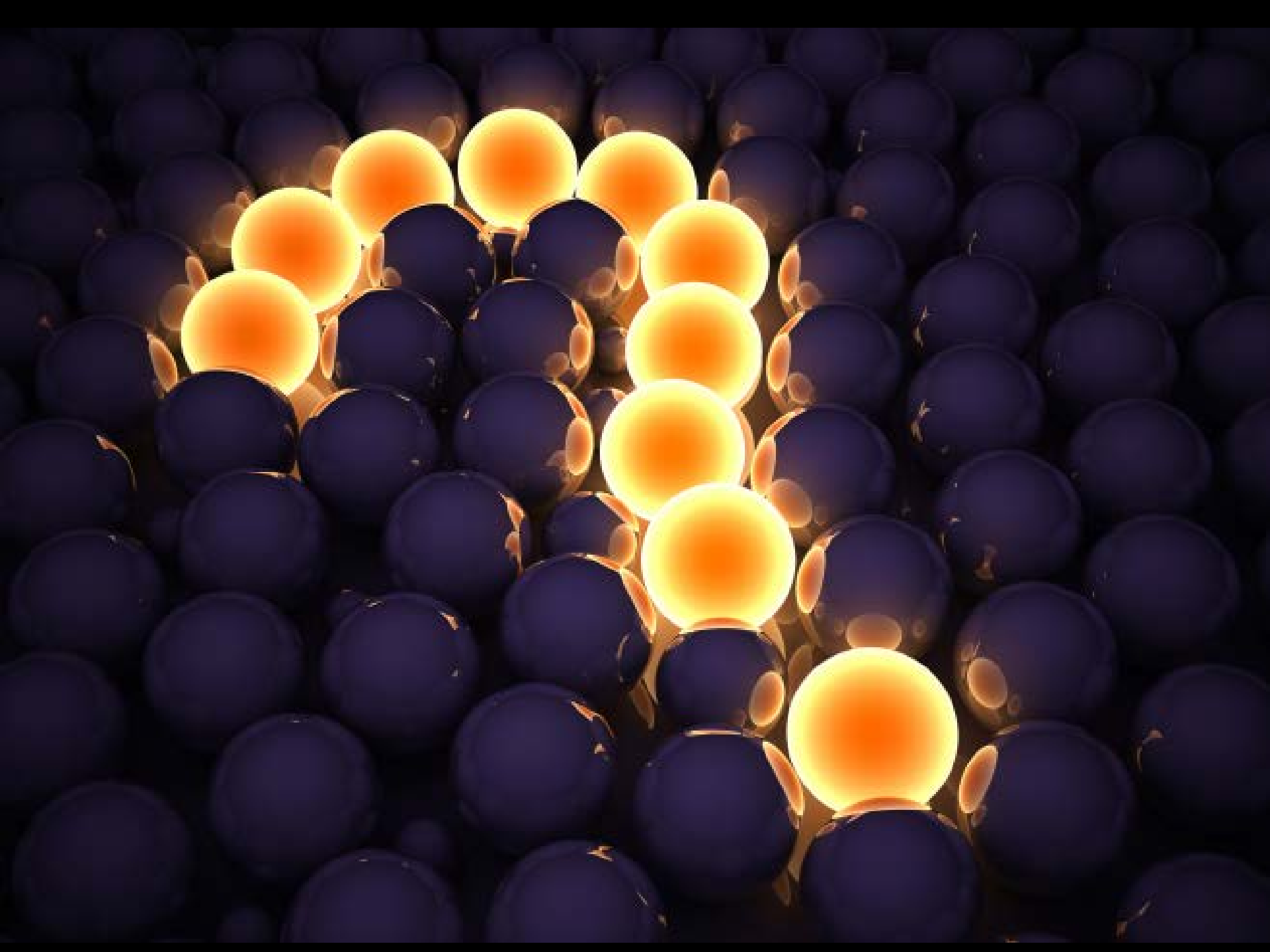
Multiwatt15



# PSoC project on blackboard

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- You can use the provided PSoC project on blackboard as a starting point.
- It has a UART and a PWM component.



# Image resources

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- Question mark: <https://wall.alphacoders.com/big.php?i=437563>