Lab Code	Lab Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ITL403	Microprocessor Lab	--	02	--	--	01	--	01

Lab Code	Lab Name	Examination Scheme						
		Theory Marks				Term Work	Pract. /Oral	Total
		Internal assessment			End Sem. Exam			
		Test1	Test 2	Avg.				
ITL403	Microprocessor Lab	--	--	--	--	25	25	50

Lab Objectives:

Sr. No.	Lab Objectives
The Lab experiments aims:	
1	Learn assembling and disassembling of PC
2	Design, simulate and implement different digital circuits
3	Get hands on experience with Assembly Language Programming.
4	Study interfacing of peripheral devices with 8086 microprocessor.
5	Realize techniques for faster execution of instructions and improve speed of operation and performance of microprocessors.
6	Write and debug programs in TASM/MASM/hardware kits

Lab Outcomes:

Sr. No.	Lab Outcomes	Cognitive levels of attainment as per Bloom's Taxonomy
On successful completion, of course, learner/student will be able to:		
1	Demonstrate various components and peripheral of computer system	L2
2	Analyze and design combinational circuits	L4, L6
3	Build a program on a microprocessor using arithmetic & logical instruction set of 8086.	L3
4	Develop the assembly level programming using 8086 loop instruction set	L6
5	Write programs based on string and procedure for 8086 microprocessor.	L1
6	Design interfacing of peripheral devices with 8086 microprocessor.	L6

Prerequisite: Logic Design, Programming Languages(C, C++)

Hardware & Software Requirements:

NOTE: Programs can be executed on assembler or hardware boards.

Hardware Requirement:	**Software requirement:**
➢ Motherboard, RAM, Processor, Connectors, Cables, SMPS, HDD, Monitor, Graphics card (optional), and Cabinet. ➢ 8086 microprocessor experiment kits with specified interfacing study boards	➢ Microsoft Macro Assembler (TASM)/Turbo Assembler (TASM) ➢ Virtual simulator lab. ➢ Proteus design suite

DETAILED SYLLABUS:

Sr. No.	**Module**	**Detailed Content**	**Hours**	**LO Mapping**
I	PC Assembly	Study of PC Motherboard Technology (South Bridge and North Bridge), Internal Components and Connections used in computer system.	**02**	LO1
II	Implementation of combinational circuits	1. Verify the truth table of various logic gates (basic and universal gates) 2. Realize Half adder and Full adder 3. Implementation of MUX and DeMUX	**06**	LO2
III	Arithmetic and logical operations in 8086 Assembly language programming	1. Program for 16 bit BCD addition 2. Program to evaluate given logical expression. 3. Convert two digit Packed BCD to Unpacked BCD. (any two)	**05**	LO3
IV	Loop operations in 8086 Assembly language programming	1. Program to move set of numbers from one memory block to another. 2. Program to count number of 1's and 0's in a given 8 bit number 3. Program to find even and odd numbers from a given list 4. Program to search for a given number (any three)	**06**	LO4
V	String &Procedure in 8086 Assembly language programming	1. Check whether a given string is a palindrome or not. 2. Compute the factorial of a positive integer 'n' using procedure. OR Generate the first 'n' Fibonacci numbers.	**04**	LO5
VI	Interfacing with 8086 microprocessor	1. Interfacing Seven Segment Display 2. Interfacing keyboard matrix 3. Interfacing DAC (any one)	**03**	LO6

Text Books:

1. Scott Mueller, ”Upgrading and repairing PCs”, Pearson,
2. R. P. Jain, “Modern Digital Electronics”, Tata McGraw Hill.
3. John Uffenbeck, “8086/8088 family: Design Programming and Interfacing:”Pearson Education

Reference Books:

1. M. Morris Mano, “Digital Logic and computer Design”, PHI
2. K Bhurchandi, “Advanced Microprocessors & Peripherals”, Tata McGraw-Hill Education

Term Work: Term Work shall consist of at least 10 to 12 practical’s based on the above list. Also Term workJournal must include at least 2 assignments.

Term Work Marks: 25 Marks (Total marks) = 15 Marks (Experiment) + 5 Marks (Assignments) + 5 Marks(Attendance)

Practical & Oral Exam: An Oral & Practical exam will be held based on the above syllabus.