

Frequency response is a measurable concept that can be used to experimentally and analytically describe a circuit. In this experiment, you become familiar with the frequency response of first- and second-order circuits.

MANDATORY EXPERIMENTS

Experiment 1

Build the circuit shown in Fig. 1 on a breadboard. Instead of using a physical switch, connect or disconnect the supply wire.

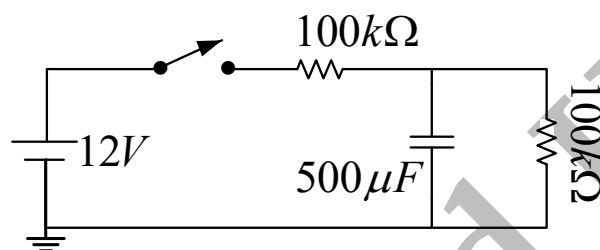


Figure 1: An RC circuit.

(a) Connect and disconnect the supply wire to see the capacitor voltage response on the oscilloscope screen. Discuss the results.

(b) Measure the time constant of the circuit and compare it to its analytical counterpart.

(c) Decrease the supply voltage to 5 V and redo the previous parts.

Experiment 2

Build the circuit shown in Fig. 2 on a breadboard.

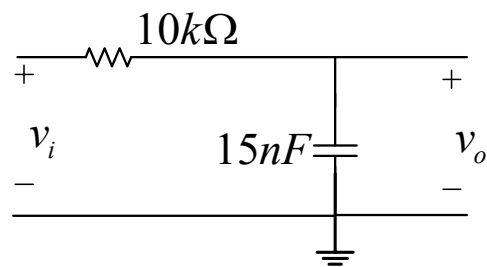


Figure 2: A lowpass RC circuit.

(a) Apply a 1-V 300-Hz square wave to the input. See the input and output voltages simultaneously on the oscilloscope screen. Interpret the observations. Obtain the time constant of the circuit.

(b) Apply a 1-V sine wave to the input and change its frequency to measure the frequency response of the circuit using interpolation. See the input and output voltages simultaneously on the oscilloscope and discuss the filtering behavior of the circuit.

Experiment 3

Build the circuit shown in Fig. 3 on a breadboard.

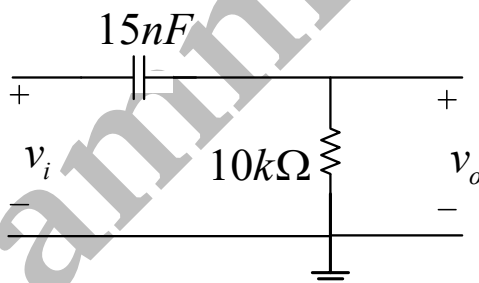


Figure 3: A highpass RC circuit.

(a) Apply a 1-V 300-Hz square wave to the input. See the input and output voltages simultaneously on the oscilloscope screen. Interpret the observations. Obtain the time constant of the circuit.

(b) Apply a 1-V sine wave to the input and change its frequency to measure the frequency response of the circuit using interpolation. See the input and output voltages simultaneously on the oscilloscope and discuss the filtering behavior of the circuit.

Experiment 4

Build the circuit shown in Fig. 4 on a breadboard.

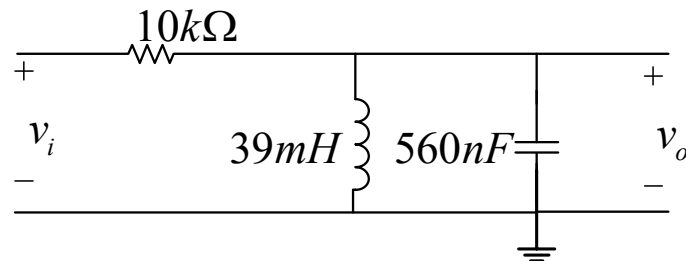


Figure 4: A bandpass RLC circuit.

(a) Apply a 1-V sine wave to the input and change its frequency from 100 Hz to 10 kHz to measure the frequency response of the circuit using interpolation. See the input and output voltages simultaneously on the oscilloscope and discuss the filtering behavior of the circuit.

(b) Apply a 1-V square wave to the input and sweep its frequency from 100 Hz to 10 kHz. See the input and output voltages simultaneously on an oscilloscope screen. Interpret the observations.

BONUS EXPERIMENTS

Experiment 5

The circuit shown in Fig. 5 is called Sallen active lowpass filter, where the triangle abstracts an op-amp amplification circuit with the gain K .

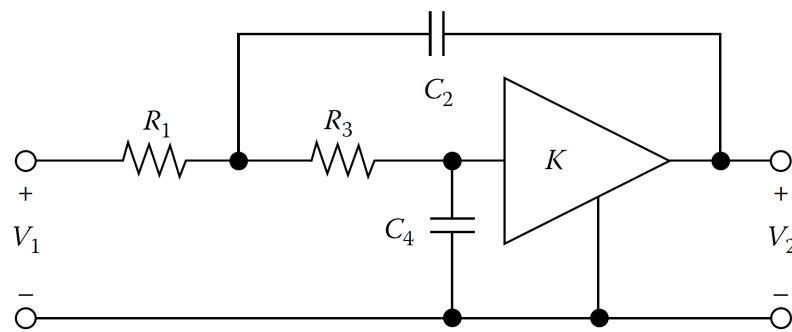


Figure 5: Sallen active lowpass filter.

(a) Calculate the frequency response of the active filter circuit.

(b) How can the amplifier part be implemented using op-amps?

(c) What are the advantages of the amplification part? Does it have any impact on the filtering response?

(d) What are the advantages and disadvantages of such an active filter?

Experiment 6

The circuit shown in Fig. 6 is called biquad active filter. The triangles denote two amplifiers with the gains -1 and 2 . The amplifiers may be implemented using inverting and non-inverting op-amp circuits. The admittances Y_1 , Y_2 , Y_3 and Y_4 can be replaced by series or parallel RC circuits. A sample customized configuration is shown in Fig. 7. Depending on the configuration, the circuit provides various filtering responses.

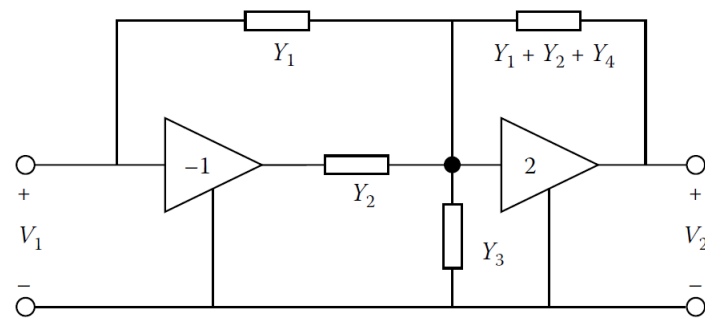


Figure 6: Biquad active filter.

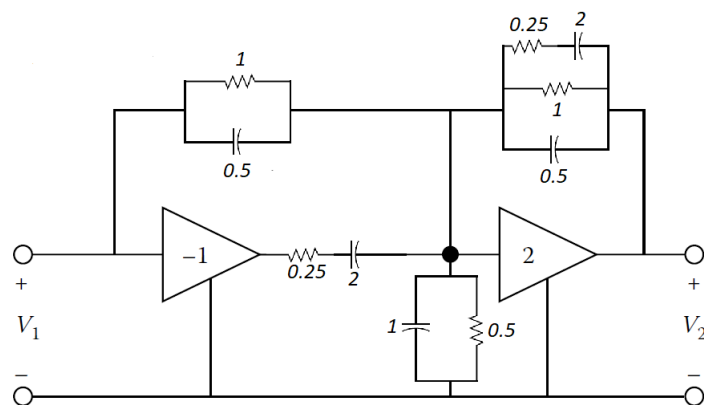


Figure 7: A sample customized realization of the biquad active filter.

(a) Simulate the circuit in PSpice and investigate the filtering response of the circuit for various configurations of Y_1 , Y_2 , Y_3 and Y_4 . Especially, demonstrate how the biquad filter can have lowpass, highpass, bandpass, and bandstop frequency responses.

(b) What is an all-pass filter and how can it be implemented using a biquad?

Experiment 7

Return your work report by filling the \LaTeX template of the manual. Include useful and high-quality images to make the report more readable and understandable.