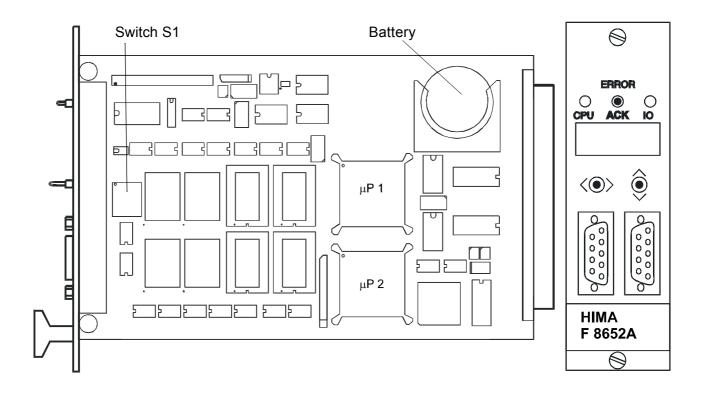


F 8652A

F 8652A: Central module

use in the PES H41q-MS, HS, HRS, safety-related, requirement class RC 1...6



Central module with two clock-synchronised operating microprocessors.

Microprocessor (2x)

Type INTEL 386EX, 32 bits clock frequency 25 MHz

Memory per microprocessor

operating system Flash-EPROM 1 MByte user program Flash-EPROM 512 kByte data store SRAM 256 kByte

Interfaces 2 serial interfaces RS 485

Diagnostic display 4-digit matrix display with requestable

information

Error switch off safety-related watchdog

with output 24 V DC, up to 500 mA,

short-circuit-proof

Construction 2 PCBs in European standard

1 PCB for the circuits of the

diagnostic display

Space requirements 8 TE

Operating data 5 V DC: 2000 mA

Note: Before withdrawing a central module its fixing screws must be loosened completely and freely movable. The module must be withdrawn and inserted uninterruptedly to prevent triggering any faulty signals in the system!

Setting of the bus station no. via switches S1-1/2/3/4/5:

Station no.	1 2 3 4 5 Stat	tion no.	1 2 3 4 5	Station no.	1 2 3 4 5	Station no.	1 2 3 4 5
0	not permitted	8		16		24	
1		9		17		25	
2		10		18		26	
3		11		19		27	
4		12		20		28	
5		13		21		29	
6		14		22		30	
7		15		23		31	

Setting of transm. rate with switch S1-8:

1 2 3 4 5 6 7 8	1 2 3 4 5 6 7 8
□□□□□□□□□ S1-8 ON = 9600 bps	□□□□□□□ S1-8 OFF = 57600 bps

Pin allocation of the interface channels RS 485

Pin	RS 485	Signal	Meaning
1	-	-	not used
2	-	RP	5 V, decoupled by diodes
3	A/A	RxD/TxD-A	Receive/Transmit Data A
4	-	CNTR-A	Control signal A
5	C/C	DGND	Data Ground
6	-	VP	5 V, positive pole of power supply
7	-	-	not used
8	B/B	RxD/TxD-B	Receive/Transmit Data B
9	-	CNTR-B	Control signal B

Diagnostic display of the central module

- 4-digit alphanumerical display,
- 2 LEDs for the common display of errors (CPU for the central modules, IO for the testable I/O-modules,
- Pushbutton ACK resets the error indication. In failure stop ACK will react like the switch-on of the system.

For further information to the diagnostic display refer to the documentation "Functions of the Operational system BS 41q/51q".

Notes for start-up and maintenance

- Lifetime of the buffer battery (*without* voltage feeding): 1000 days at T_A = 25 °C 200 days at T_A = 60 °C
- It is recommended to change the buffer battery (CPU in operation) at the latest after 6 years, or with display BATI within three weeks (Lithium battery, e. g. type CR 2477N, HIMA part no. 44 0000018)
- Check the bus station no. and transmission rate at switch S1 for correct settings

Important: When upgrading an F 8652 to an F 8652A module the fan concept is also to be changed!