

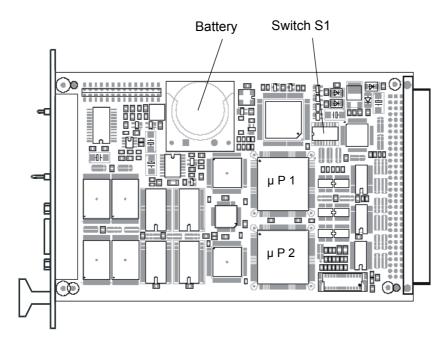
## F 8650X





# F 8650X: Central module

Use in the PES H51q-MS, -HS, -HRS, Safety-related, applicable up to SIL 3 according to IEC 61508



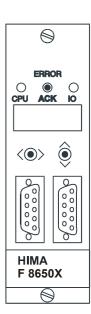


Figure 1: View

Central module with two clock-synchronized microprocessors

Microprocessors INTEL 386EX. 32 bits Clock frequency 25 MHz

Memory per microprocessor

Flash-EPROM 1 MB Operating System User program Flash-EPROM 1 MB \*

Data SRAM 1 MB \*

\* Degree of utilization depending on operating system version

Interfaces Two serial interfaces RS 485 with electric isolation Diagnostic display Four digit matrix display with selectable information

Safety-related watchdog with output 24 V, Shutdown on fault

loadable up to 500 mA, short-circuit proof

Construction Two European standard PCBs,

one PCB for the diagnostic display

Space requirement 8 SU Operating data 5 V / 2 A

## Setting of the bus station no. via switches S1-1/2/3/4/5/6/7:

Position switch no. 6 7			
On ☐ ☐ Off ■ ☐ Switch no.	Switch no.	Switch no.	Switch no.
Station no. 1 2 3 4 5	Station no. 1 2 3 4 5	Station no. 1 2 3 4 5	Station no. 1 2 3 4 5
0 On  not admiss	OH B B B D B	16 On	24 On
1 On	9 Off	17 On	25 On
2 Off	10 On	18 Off	26 On
3 Off	11 On	19 Off	27 On
4 On	12 On	20 On Off	28 On
5 On	13 Off	21 On	29 On
6 On	14 On	22 On	30 On
7 On	15 On	23 On	31 On
Position switch no. 6 7			
On Off	Cusitale ma	Considering to the	Switch no.
Switch no. Station no. 1 2 3 4 5	Switch no. Station no. 1 2 3 4 5	Switch no. Station no. 1 2 3 4 5	Station no. 1 2 3 4 5
32 On	40 On	48 On	56 On
33 Off	41 On	49 On	57 On
34 Off	42 Off	50 On Off	58 On
35 Off	43 Off	51 On	59 On
36 On	44 On	52 On	60 On Gr
37 On	45 Off	53 On	61 On III
38 On	46 Off	54 On	62 On Off
39 On	47 On	55 On	63 Off
Position switch no. 6 7			
On Off	Switch no.	Curitala na	Curitala ma
Switch no. Station no. 1 2 3 4 5	Station no. 1 2 3 4 5	Switch no. Station no. 1 2 3 4 5	Switch no. Station no. 1 2 3 4 5
64 On	72 On	80 On	88 On
65 Off	73 On	81 On	89 Off
66 Off	74 On	82 Off	90 On
67 On	75 Of	83 On	91 On
68 On	76 On	84 On	92 On
69 On	77 On	85 Off	93 On
70 On	78 On	86 Off B B B B	94 On
71 On	79 On	87 Off	95 On
Position switch no. 6 7	Legend:		
Switch no.	Positions white sw	ritch:	
Station no. 1 2 3 4 5 96 Off 1 1 1 1 1 1			
97 On	On Bit is set	on ☐ Bit is not set	
98 On	White switch in position OFF	White switch in position ON	
Off ■ 🗆 ■ ■			
99 On		·	

### Setting of the transmission rate with switch S1-8:

	12345678			12345678	
Off On		S1-8 ON = 9600 bps	Off On		S1-8 OFF = 57600 bps

Pin	RS 485	Signal	Meaning
1	-	-	not used
2	-	RP	5 V, decoupled by diodes
3	A/A'	RxD/TxD-A	Receive/Transmit Data A
4	-	CNTR-A	Control signal A
5	C/C'	DGND	Data Ground
6	-	VP	5 V, positive pole of power supply
7	-	-	not used
8	B/B'	RxD/TxD-B	Receive/Transmit Data B
9	-	CNTR-B	Control signal B

Table 1: Pin assignment of the interface RS 485, 9-pole

For the serial interface only the bus station no. 1-31 can be set.

Within an Ethernet network the bus station no. can be set from 1 to 99. Therefore the switches S1-6/7 must be set in addition to the switches S1-1/2/3/4/5.

The number of the communication partners within a network is still limited to 64.

This enhanced setting of the bus station no. is only possible from operating system BS41q/51q V7.0-8 (05.31) of the central module.

#### Applications with the communication module F 8627X:

- connection of the central module to a PADT (ELOP II TCP)
- connection to other communication partners within an Ethernet network (safeethernet, Modbus TCP)

The communication runs from the central module via the backplane bus to the communication module F 8627X and from the Ethernet ports of the F 8627X into the Ethernet network and vice versa.

#### Special features of the central module:

- Self-education: from operating system BS41g/51g V7.0-8 (05.31)
- ELOP II TCP: from operating system BS41q/51q V7.0-8 (05.31)

Further informations about the bus station no., ELOP II TCP, loading of operating systems and application programs (self-education) et al. corresponding to the central module you will find in the data sheet of the F8627X as well as the operating system manual of H41q/H51q and the safety manual of H41q/H51q.



Before removing a central module its fixing screws must be completely loosened and freely movable. Remove the module from the bus board by pushing the ejection lever (front label) top down and quickly removing in an upward motion to ensure that faulty signals are not triggered within the system!

To attach the module, place it on the terminal block and press it inwards as far as it will go. This action should be performed quickly to ensure that faulty signals are not triggered within the system!

#### Function of the ejection lever with front label

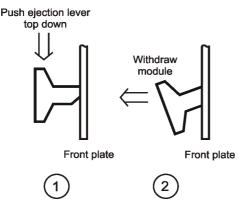


Figure 2: Function of the ejection lever

#### Diagnostic display of the central module

- Four digit alphanumerical display,
- two LEDs for the general display of errors (CPU for the central modules, IO for the testable input/output modules),
- two toggle switches to request detailed error information,
- push-button ACK resets the error indication;
   in failure stop ACK behaves like restarting the system.

For further information on the diagnostic display and lists of error codes, refer to the documentation "Functions of the operational system BS 41q/51q" (also on ELOP II CD).

#### Notes for start-up and maintenance

- Lifetime of the buffer battery (without voltage feeding):
   1000 days at T<sub>A</sub> = 25 °C
   200 days at T<sub>A</sub> = 60 °C
- It is recommended to change the buffer battery (CPU in operation) at the latest after 6 years, or with display BATI within three months
   (Lithium battery, e.g. type CR 2477N, HIMA part no. 44 0000018)
- Check the bus station no. and transmission rate at switch S1 for correct settings
- The F 8650X can be used to replace the previous moduls: F 8650, F 8650A and F 8650E!