# **HIMatrix**

# **Safety-Related Controller**

# F10 PCI 03 Manual





HIMA Paul Hildebrandt GmbH Industrial Automation

Rev. 2.00 HI 800 483 E

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2.00	Revised: Figure 3 Added: SIL 4 certified according to EN 50126, EN 50128 and EN 50129	Х	Х

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F10 PCI 03 1 Introduction

#### 1 Introduction

This manual describes the technical characteristics of the device and its use. It provides information on how to install, start up and configure the module in SILworX.

#### 1.1 Structure and Use of this Manual

The content of this manual is part of the hardware description of the HIMatrix programmable electronic system.

This manual is organized in the following main chapters:

- Introduction
- Safety
- Product Description
- Start-up
- Operation
- Maintenance
- Decommissioning
- Transport
- Disposal

Additionally, the following documents must be taken into account:

Name	Content	Document number
HIMatrix System Manual Compact Systems	Hardware description of the HIMatrix compact systems	HI 800 141 E
HIMatrix Safety Manual	Safety functions of the HIMatrix system	HI 800 023 E
HIMatrix Safety Manual for Railway Applications	Safety functions of the HIMatrix system using the HIMatrix in railway applications	HI 800 437 E
SILworX Communication Manual	Description of the communication protocols, ComUserTask and their configuration in SILworX	HI 801 101 E
SILworX Online Help	Instructions on how to use SILworX	-
SILworX First Steps	Introduction to SILworX using the HIMax system as an example	HI 801 103 E

Table 1: Additional Relevant Documents

The latest manuals can be downloaded from the HIMA website at www.hima.com. The revision index on the footer can be used to compare the current version of existing manuals with the Internet edition.

#### 1.2 Target Audience

This document addresses system planners, configuration engineers, programmers of automation devices and personnel authorized to implement, operate and maintain the modules and systems. Specialized knowledge of safety-related automation systems is required.

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1 Introduction F10 PCI 03

#### 1.3 Formatting Conventions

To ensure improved readability and comprehensibility, the following fonts are used in this document:

**Bold** To highlight important parts.

Names of buttons, menu functions and tabs that can be clicked and used

in the programming tool.

Italics For parameters and system variables

Courier Literal user inputs

RUN Operating state are designated by capitals

Chapter 1.2.3 Cross references are hyperlinks even though they are not particularly

marked. When the cursor hovers over a hyperlink, it changes its shape.

Click the hyperlink to jump to the corresponding position.

Safety notes and operating tips are particularly marked.

#### 1.3.1 Safety Notes

The safety notes are represented as described below.

These notes must absolutely be observed to reduce the risk to a minimum. The content is structured as follows:

- Signal word: warning, caution, notice
- Type and source of risk
- Consequences arising from non-observance
- Risk prevention

#### **A** SIGNAL WORD



Type and source of risk!

Consequences arising from non-observance

**Risk prevention** 

The signal words have the following meanings:

- Warning indicates hazardous situation which, if not avoided, could result in death or serious injury.
- Caution indicates hazardous situation which, if not avoided, could result in minor or modest injury.
- Notice indicates a hazardous situation which, if not avoided, could result in property damage.

#### **NOTE**



Type and source of damage!

Damage prevention

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F10 PCI 03 1 Introduction

# 1.3.2 Operating Tips Additional information is structured as presented in the following example: The text corresponding to the additional information is located here. Useful tips and tricks appear as follows:

TIP

The tip text is located here.

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2 Safety F10 PCI 03

### 2 Safety

All safety information, notes and instructions specified in this document must be strictly observed. The product may only be used if all guidelines and safety instructions are adhered to.

This product is operated with SELV or PELV. No imminent risk results from the product itself. The use in Ex-Zone is permitted if additional measures are taken.

#### 2.1 Intended Use

HIMatrix components are designed for assembling safety-related controller systems.

When using the components in the HIMatrix system, comply with the following general requirements.

#### 2.1.1 Environmental Requirements

Requirement type	Range of values
Protection class	Protection class III in accordance with IEC/EN 61131-2
Ambient temperature	0+60 °C
Storage temperature	-40+85 °C
Pollution	Pollution degree II in accordance with IEC/EN 61131-2
Altitude	< 2000 m
Housing	Standard: IP20
Supply voltage	24 VDC

Table 2: Environmental Requirements

Exposing the HIMatrix system to environmental conditions other than those specified in this manual can cause the HIMatrix system to malfunction.

#### 2.1.2 ESD Protective Measures

Only personnel with knowledge of ESD protective measures may modify or extend the system or replace devices.

#### NOTE



Device damage due to electrostatic discharge!

- When performing the work, make sure that the workspace is free of static, and wear an ESD wrist strap.
- If not used, ensure that the device is protected from electrostatic discharge, e.g., by storing it in its packaging.

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F10 PCI 03 2 Safety

#### 2.2 Residual Risk

No imminent risk results from a HIMatrix system itself.

Residual risk may result from:

- Faults related to engineering
- Faults related to the user program
- Faults related to the wiring

#### 2.3 Safety Precautions

Observe all local safety requirements and use the protective equipment required on site.

#### 2.4 Emergency Information

A HIMatrix system is a part of the safety equipment of a site. If a device or a module fails, the system enters the safe state.

In case of emergency, no action that may prevent the HIMatrix systems from operating safely is permitted.

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3 Product Description F10 PCI 03

#### 3 Product Description

The safety-related **F10 PCI 03** controller is a printed circuit board system for mounting in a PC. The controller is designed as a PC card for the PCI slot.

The configuration is performed using SILworX, see Chapter 4.3. Communication to the PADT occurs via the PCI interface of the PC.

The device is suitable for sequence of events recording (SOE), see Chapter 4.2. The device supports multitasking and reload. For more details, refer to the system manual compact systems (HI 800 141 E).

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A licence is required to use the events recording, the multitasking and the reload features.

The controller must be internally supplied via the PC or by an external 24 V power source. The terminal for the external power supply is located on the slot bracket.

The controller is composed of the safety-related processor system and the communication system. It is neither equipped with I/O connections nor with fieldbus interfaces.

The controller is connected to the field zone via remote I/Os. To this end, the device is equipped with 4 Ethernet interfaces.

The device is TÜV-certified for safety-related applications up to SIL 3 (IEC 61508, IEC 61511 and IEC 62061), Cat. 4 and PL e (EN ISO 13849-1) and SIL 4 (EN 50126, EN 50128 and EN 50129).

Further safety standards, application standards and test standards are specified in the certificates available on the HIMA website.

#### 3.1 Safety Function

The safety function of the controller includes the following points:

- Processing of the user program:
   If faults occur: Stop the user program and reset the variables to the initial values
- Safe communication between HIMA controllers (HIMax, HIMatrix, and remote I/O modules)
  using the safety-related safeethernet protocol. Data is transferred over the Ethernet
  interfaces of the controller.

The safety function is performed in accordance with SIL 3.

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#### 3.2 Equipment, Scope of Delivery

The following table specifies the available controller:

Designation	Description
F10 PCI 03	Printed circuit board with 4 Ethernet interfaces,
SILworX	for PCI slot
	Operating temperature: 0+60 °C,
	for SILworX programming tool

Table 3: Available Controller

#### 3.2.1 IP Address and System ID (SRS)

A transparent label is delivered with the device to allow one to note the IP addresses of the CPU and the COM and the system ID (SRS for system rack slot) after a change.

Default value for IP address of the CPU: 192.168.0.99
Default value for IP address of the COM: 192.168.0.100
Default value for SRS: 60 000.0.0

The label must be affixed such that the ventilation slots in the housing are not obstructed.

Refer to the *SILworX* First Steps manual for more information on how to modify the IP address and the system ID.

#### 3.3 Type Label

The type plate contains the following details:

- Product name
- Bar code (2D code)
- Part no.
- Production year

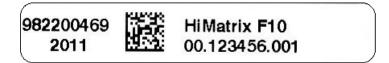


Figure 1: Type Label

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#### 3.4 Structure

This chapter describes the layout and function of the controller, and its connection for communication.

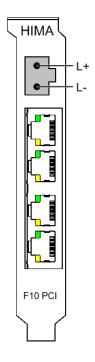


Figure 2: Front View

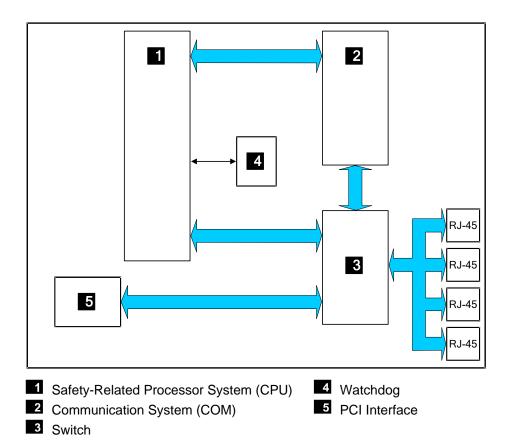


Figure 3: Block Diagram

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#### 3.4.1 LED Indicators

The light-emitting diodes (LEDs) indicate the operating state of the controller. The LEDs are classified as follows:

- Operating voltage LED
- Communication LED

When the supply voltage is switched on, a LED test is performed and all LEDs are briefly lit.

#### **Definition of Blinking Frequencies**

The following table defines the blinking frequencies of the LEDs:

Name	Blinking frequencies
Blinking1	Long (approx. 600 ms) on, long (approx. 600 ms) off
Blinking-x	Ethernet communication: Blinking synchronously with data transfer

Table 4: Blinking Frequencies of LEDs

#### 3.4.1.1 Operating Voltage LED

The operating voltage LED does not depend on the CPU operating system in use.

LED	Color	Status	Description
24 VDC	Green	On	24 VDC operating voltage present
		Off	No operating voltage

Table 5: Operating Voltage LED

#### 3.4.1.2 Communication LEDs

All RJ-45 connectors are provided with a small green and a yellow LEDs. The LEDs signal the following states:

LED	Status	Description	
Green	On	Full duplex operation	
	Blinking1	IP address conflict, all communication LEDs are blinking	
	Blinking-x	Collision	
	Off	Half duplex operation, no collision	
<b>Yellow</b>	On	Connection available	
	Blinking1	IP address conflict, all communication LEDs are blinking	
	Blinking-x	Interface activity	
	Off	No connection available	

Table 6: Ethernet Indicators

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#### 3.4.2 Communication

The controller communicates with remote I/Os via safe**ethernet**. Characteristics and configuration of safe**ethernet** are described in the SILworX communication manual (HI 801 101 E).

#### 3.4.2.1 Connections for Ethernet Communication

Property	Description
Port	4 x RJ-45
Transfer standard	10BASE-T/100BASE-Tx, half and full duplex
Auto negotiation	Yes
Auto crossover	Yes
IP address	Freely configurable <sup>1)</sup>
Subnet mask	Freely configurable <sup>1)</sup>
Supported protocols	<ul> <li>Safety-related: safeethernet, PROFIsafe</li> <li>Standard protocols: Programming and debugging tool (PADT), OPC, Modbus TCP, TCP SR, SNTP, ComUserTask, PROFINET</li> </ul>
1) The general rules for assigning IP address and subnet masks must be adhered to.	

Table 7: Ethernet Interfaces Properties

The four RJ-45 connectors with integrated LEDs are located in the slot bracket of the PC card. Refer to Chapter 3.4.1.2 for a description of the LEDs' function.

The connection parameters are read based on the MAC address (media access control address) defined during manufacturing.

The MAC addresses are specified on a label located on the housing of the RJ-45 connectors. CPU and COM have their own MAC addresses, the third MAC address belongs to the PCI interface.

CPU: MAC-ADR1 00.E0.A1.00.06.C0 COM: MAC-ADR2 00.E0.A1.00.06.C1 PC: MAC-ADR8 00.E0.A1.00.06.C7

Figure 4: Sample MAC Address Label

The COM MAC address corresponds to the CPU MAC address, except for the last byte which is increased by 1. The PCI interface MAC address corresponds to the CPU MAC address, except for the last byte which is increased by 7.

The controller is equipped with an integrated switch for Ethernet communication. For further information on the integrated switch and safe**ethernet**, refer to Chapter *Communication* of the system manual for compact systems (HI 800 141 E).

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#### 3.4.2.2 Network Ports Used for Ethernet Communication

UDP ports	Use
123	SNTP (time synchronization between PES and remote I/O, PES and external devices)
502	Modbus salve (can be modified by the user)
6010	safeethernet and OPC
6005 / 6012	If TCS_DIRECT was not selected in the HH network
8000	Programming and operation with SILworX
8004	Configuration of the remote I/O using the PES (SILworX)
34 964	PROFINET endpoint mapper (required for establishing the connection)
49 152	PROFINET RPC server
49 153	PROFINET RPC client

Table 8: Network Ports (UDP Ports) in Use

TCP ports	Use
502	Modbus salve (can be modified by the user)
XXX	TCP SR assigned by the user

Table 9: Network Ports (TCP Ports) in Use

The ComUserTask can use any port if it is not already used by another protocol.

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3 Product Description F10 PCI 03

#### 3.4.3 Reset Key

The controller is equipped with a reset key. The key is only required if the user name or password for administrator access is not known. If only the IP address set for the controller does not match the PADT (PC), the connection can be established with a Route add entry on the PC.

The reset key is located on the upper side of the printed circuit board and can only be accessed if the PC case is open.

The reset is only effective if the controller is rebooted (switched off and on) while the key is simultaneously engaged for at least 20 s. Engaging the key during operation has no effect.

#### **A** CAUTION



Fieldbus communication may be disturbed!

Prior to switching on the controller with the reset key engaged, all device fieldbus connectors must be unplugged to ensure that the fieldbus communication among other stations is not disturbed.

The fieldbus plugs may only be plugged in again when the controller is in the RUN or STOP state.

Properties and behavior of the controller after a reboot with engaged reset key:

- Connection parameters (IP address and system ID) are set to the default values.
- All accounts are deactivated except for the administrator default account with empty password.
- Loading a user program or operating system with default connection parameters is inhibited!
   The loading procedure is only allowed after the connection parameters and the account have been configured on the controller and the controller has been rebooted.

After a new reboot without the reset key engaged, the connection parameters (IP address and system ID) and accounts become effective.

- Those configured by the user.
- Those valid prior to rebooting with the reset key engaged, if no changes were performed.

#### 3.4.4 Hardware Clock

In case of loss of operating voltage, the power provided by an integrated gold capacitor is sufficient to buffer the hardware clock for approximately one week.

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#### 3.5 Product Data

General		
Total program and data memory for all user programs	5 MB less 64 kBytes for CRCs	
Response time	≥ 6 ms	
Ethernet interfaces	4 x RJ-45, 10BASE-T/100BASE-Tx (with 100 Mbit/s) with integrated switch	
Operating voltage	24 VDC, -15+20 %, r <sub>PP</sub> ≤ 15 %,	
	from a power supply unit with safe insulation	
	in accordance with IEC 61131-2	
Current input	0.7 A	
Fuse (external)	10 A time-lag (T) for line protection	
Buffer for date/time	Gold capacitor	
Operating temperature	0+60 °C	
Storage temperature	-40+85 °C	
Type of protection	IP20	
Max. dimensions	Width: 168 mm	
	Depth: 108 mm	
Weight	approx. 190 g	

Table 10: Product Data

#### 3.6 Certified HIMatrix F10 PCI 03

HIMatrix F10		
CE	EMC	
TÜV	IEC 61508 1-7:2010 up to SIL 3	
	IEC 61511:2004	
	EN ISO 13849-1:2008	
	IEC 62061:2005	
	EN 50156-1:2004	
	EN 298:2003	
	EN 230:2005	
TÜV CENELEC	Railway applications	
	EN 50126: 1999 up to SIL 4	
	EN 50128: 2001 up to SIL 4	
	EN 50129: 2003 up to SIL 4	

Table 11: Certificates

Further safety standards and application standards are specified in the certificate. The certificate and the EC type test certificate are available on the HIMA website at www.hima.com.

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4 Start-up F10 PCI 03

#### 4 Start-up

To start up the controller, it must be mounted, connected and configured in SILworX.

#### 4.1 Installation and Mounting

The controller is mounted on a PCI slot. Observe the mounting manual of the PC manufacturer.

#### 4.2 Sequence of Events Recording (SOE)

The global variables of the controller can be monitored using sequence of events recording. Global variables to be monitored are configured using SILworX, see the online help and the SILworX communication manual (HI 801 101 E). Up to 4000 events can be configured.

An event is composed of:

Entry data	Description	
Event ID	The event ID is assigned by the PADT.	
Timestamp	Date (e.g., 21/11/2008)	
	Time (e.g., 9:31:57.531)	
Event state	Alarm/Normal (Boolean event)	
	LL, L, N, H, HH (scalar event)	
Event quality	Quality good/	
	Quality bad, see www.opcfoundation.org	

Table 12: Event Description

Events are recorded within the cycle of the user program. The processor system uses global variables to create the events and stores them in its non-volatile event buffer.

The event buffer includes 1000 events. If the event buffer is full, an overflow system event entry is created. Thereafter, events are no longer recorded until existing events have been read and space is once again available in the event buffer.

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F10 PCI 03 4 Start-up

#### 4.3 Configuration with SILworX

In the Hardware Editor, the controller is represented like a base plate equipped with the following modules:

- Processor module (CPU)
- Communication module (COM)

Double-click the module to open the Detail View with the corresponding tabs.

#### 4.3.1 Processor Module

The following tables present the parameters for the processor module (CPU) in the same order as given in the Hardware Editor. The Content of the registers Module and Routings of the processor module and the communication module is identical.

#### 4.3.1.1 Tab: **Module**

The **Module** tab contains the following parameters:

Parameter	Description		
Name	Module name		
Activate Max. µP Budget for HH Protocol	<ul> <li>Activated: Use CPU load limit from the Max. µP Budget for HH         Protocol [%] field.</li> <li>Deactivated: Do not use the CPU Load limit for safeethernet.</li> <li>Default setting: Deactivated</li> </ul>		
Max. μP Budget for HH Protocol [%]	Maximum CPU load of module that can be used for processing the safe <b>ethernet</b> protocols.		
	The maximum load must be distributed among all the implemented protocols that use this communication module.		
IP address	IP address of the Ethernet interface. Default value: 192.168.0.99		
Subnet mask	32 bit address mask to split up the IP address in network and host address. Default value: 255.255.252.0		
Standard Interface	Activated: the interface is used as standard interface for the system login.  Default setting: Deactivated		
Default Gateway	IP address of the default gateway. Default value: 0.0.0.0		

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Parameter	Description
ARP Aging Time [s]	A processor or COM module stores the MAC addresses of the communication partners in a MAC/IP address assignment table (ARP cache).
	<ul> <li>If in a period of 1x2x ARP Aging Time</li> <li> messages of the communication are received, the MAC address remains stored in the ARP cache.</li> <li> no messages of the communication partner are received, the MAC address is erased from the ARP cache.</li> </ul>
	The typical value for the <i>ARP Aging Time</i> in a local network ranges from 5300 s.  The user cannot read the contents of the ARP cache.
	Range of values: 13600 s Default value: 60 s
	If routers or gateways are used, the user must adjust (increase) the <i>ARP Aging Time</i> due to the additional time required for two-way transmission.
	If the ARP Aging Time is too low, the processor or the COM module deletes the MAC address of the communication partner from the ARP cache and the communication is either delayed or breaks down entirely. For an efficient performance, the ARP aging time value must be less than the receive timeout set for the protocols in use.
MAC Learning	MAC Learning and ARP Aging Time are used to set how quick the Ethernet switch should learn the MAC address.
	<ul> <li>The following settings are possible:</li> <li>Conservative (recommended):         If the ARP cache already contains MAC addresses of communication partners, these are locked and cannot be replaced by other MAC addresses for at least one ARP Aging Time and a maximum of two ARP Aging Time periods. This ensures that data packets cannot be intentionally or unintentionally forwarded to external network subscribers (ARP spoofing).     </li> <li>Tolerant:</li> </ul>
	When a message is received, the IP address contained in the message is compared to the data in the ARP cache and the MAC address stored in the ARP cache is immediately overwritten with the MAC address from the message. The <i>Tolerant</i> setting must be used if the availability of communication is more important than the authorized access to the controller.  Default setting: Conservative
IP Forwarding	Allow a processor module to operate as router and to forward data packets to other network nodes.  Default setting: Deactivated

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F10 PCI 03 4 Start-up

Parameter	Description
ICMP Mode	The Internet Control Message Protocol (ICMP) allows the higher protocol layers to detect error states on the network layer and optimize the transmission of data packets.  Message types of Internet Control Message Protocol (ICMP) supported by the processor module:  No ICMP Responses All the ICMP commands are deactivated. This ensures a high degree of safety against potential sabotage that might occur over the network.  Echo Response If Echo Response is activated, the node responds to a ping command. It is thus possible to determine if a node can be reached. Safety is still high.  Host Unreachable Not important for the user. Only used for testing at the manufacturer's facility.  All Implemented ICMP Responses All ICMP commands are activated. This allows a more detailed diagnosis of network malfunctions.  Default setting: Echo Response

Table 13: CPU and COM Configuration Parameters, **Module** Tab

## 4.3.1.2 Tab: Routings

The **Routings** tab contains the routing table. This table is empty if the module is new. A maximum of 8 routing entries are possible.

Parameter	Description	
Name	Denomination of the routing settings	
IP address	Target IP address of the communication partner (with direct host routing) or network address (with subnet routing).  Range of values: 0.0.0.0 255.255.255.255  Default value: 0.0.0.0	
Subnet mask	Define the target address range for a routing entry. 255.255.255.255 (with direct host routing) or subnet mask of the addressed subnet. Range of values: 0.0.0.0 255.255.255.255 Default value: 255.255.255.255	
Gateway	IP address of the gateway to the addressed network. Range of values: 0.0.0.0 255.255.255.255 Default value: 0.0.0.1	

Table 14: Routing Parameters of the CPU and COM

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4 Start-up F10 PCI 03

#### 4.3.1.3 Tab: Ethernet Switch

The **Ethernet Switch** tab contains the following parameters:

Parameter	Description		
Name	Name of the port (Eth1Eth4, PC), per port, only one configuration may exist.		
Speed [Mbit/s]	10: Data rate 10 Mbit/s 100: Data rate 100 Mbit/s Autoneg: Automatic baud rate setting Default value: Autoneg		
Flow Control	Full duplex: Simultaneous communication in both directions Half duplex: Communication in one direction Autoneg: Automatic communication control Default value: Autoneg		
Autoneg also with fixed values	The Advertising function (forwarding the speed and flow control properties) is also performed if the parameters Speed and Flow Control have fixed values.  This allows other devices with ports set to Autoneg to recognize the HIMax port settings.  Default setting: Activated		
Limit	Limit the inbound multicast and/or broadcast packets.  Off: No limitation  Broadcast: Limit broadcast packets (128 kbit/s)  Multicast and Broadcast: Limit multicast and broadcast packets (1024 kbit/s)  Default value: Broadcast		

Table 15: Ethernet Switch Parameters

#### 4.3.1.4 Tab: **VLAN** (Port-Based VLAN)

For configuring the use of port-based VLAN.

 $\label{eq:local_local_local_local} 1 \qquad \text{Should VLAN be supported, port-based VLAN should be off to enable each port to communicate with the other switch ports.}$ 

For each port on one switch, the user can define which other ports of the switch received Ethernet frames may be sent to, refer to Figure 3.

The table in the VLAN tab contains entries through which the connection between two ports can be set as active or inactive.

	Eth1	Eth2	Eth3	Eth4	COM	CPU
Eth1						
Eth2	Active					
Eth3	Active	Active				
Eth4	Active	Active	Active			
COM	Active	Active	Active	Active		
CPU	Active	Active	Active	Active	Active	
PC	Active	Active	Active	Active	Active	Active

Table 16: VLAN Tab

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F10 PCI 03 4 Start-up

#### 4.3.1.5 Tab: **LLDP**

With LLDP (Link Layer Discovery Protocol), information such as MAC address, device name, port number is sent per multicast in periodic intervals via the own device and is received from the neighboring devices.

LLDP uses the following values depending on whether PROFINET is configured on the communication module:

PROFINET on the COM module	ChassisID	TTL (Time to Live)
Used	Device name	20 s
Not used	MAC address	120 s

Table 17: Values for LLDP

The processor and communication modules support LLDP on the Eth1, Eth2, Eth3, Eth4 and PC ports

The following parameters define how a given port should work:

Off LLDP is disabled on this port.

Send LLDP sends LLDP Ethernet frames, received

LLDP Ethernet frames are deleted without being

processed.

Receive LLDP sends no LLDP Ethernet frames, but

received LLDP Ethernet frames are processed.

Send/Receive LLDP sends and processes received LLDP

Ethernet frames.

Default setting: Send/Receive

#### 4.3.1.6 Tab: Mirroring

Mirroring is used to configure whether the module should duplicate Ethernet packets on a given port such that they can be read from a device connected to that port, e.g., for test purposes.

The following parameters define how a given port should work:

Off This port does not participate to the mirroring process.

Egress: Outgoing data of this port are duplicated.

Ingress/Egress: Incoming and outgoing data of this port are duplicated.

Dest Port: This port is used to send duplicated data.

Default setting: OFF

#### 4.3.2 Communication Module

The communication module contains the **Module** tab and the **Routings** tab. Their content is identical to those of the processor module, see Table 13 and Table 14.

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5 Operation F10 PCI 03

# 5 Operation

The controller F10 PCI 03 is ready for operation.

No specific monitoring is required for the controller.

# 5.1 Handling

Handling of the controller during operation is not required.

## 5.2 Diagnosis

The device diagnostic history can also be read using SILworX.

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F10 PCI 03 6 Maintenance

#### 6 Maintenance

No maintenance measures are required during normal operation.

If a failure occurs, the defective module or device must be replaced with a module or device of the same type or with a replacement model approved by HIMA.

Only the manufacturer is authorized to repair the device or module.

#### 6.1 Faults

If the test harnesses detect safety-critical faults, the module enters the STOP\_INVALID state and will remain in this state. This means that the input signals are no longer processed by the device and the outputs switch to the de-energized, safe state. The evaluation of diagnostics provides information on the fault cause.

#### 6.2 Maintenance Measures

The following measures are required for the device:

- Loading the operating system, if a new version is required
- Executing the proof test

#### 6.2.1 Loading the Operating System

HIMA is continuously improving the operating system of the devices. HIMA recommends to use system downtimes to load a current version of the operating system into the devices.

Refer to the release list to check the consequences of the new operation system version on the system!

The operating system is loaded using the programming tool.

Prior to loading the operating system, the device must be in STOP (displayed in the programming tool). Otherwise, stop the device.

For more information, refer to the programming tool documentation.

#### 6.2.2 Proof Test

HIMatrix devices and modules must be subjected to a proof test in intervals of 10 years. For more information, refer to the safety manual (HI 800 023 E).

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7 Decommissioning F10 PCI 03

# 7 Decommissioning

Remove the supply voltage to decommission the device. Afterwards pull out the Ethernet cables.

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F10 PCI 03 8 Transport

# 8 Transport

To avoid mechanical damage, HIMatrix components must be transported in packaging.

Always store HIMatrix components in their original product packaging. This packaging also provides protection against electrostatic discharge. Note that the product packaging alone is not suitable for transport.

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9 Disposal F10 PCI 03

# 9 Disposal

Industrial customers are responsible for correctly disposing of decommissioned HIMatrix hardware. Upon request, a disposal agreement can be arranged with HIMA.

All materials must be disposed of in an ecologically sound manner.





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F10 PCI 03 Appendix

# **Appendix**

# **Glossary**

Term	Description
ARP	Address resolution protocol: Network protocol for assigning the network addresses to hardware addresses
Al	Analog input
AO	Analog output
COM	Communication module
CRC	Cyclic redundancy check
DI	Digital input
DO	Digital output
ELOP II Factory	Programming tool for HIMatrix systems
EMC	Electromagnetic compatibility
EN	European norm
ESD	Electrostatic discharge
FB	Fieldbus
FBD	Function block diagrams
FTT	Fault tolerance time
ICMP	Internet control message protocol: Network protocol for status or error messages
IEC	International electrotechnical commission
MAC address	Media access control address: Hardware address of one network connection
PADT	Programming and debugging tool (in accordance with IEC 61131-3), PC with SILworX or ELOP II Factory
PE	Protective earth
PELV	Protective extra low voltage
PES	Programmable electronic system
R	Read: The system variable or signal provides value, e.g., to the user program
Rack ID	Base plate identification (number)
Interference-free	Supposing that two input circuits are connected to the same source (e.g., a transmitter). An input circuit is termed <i>interference-free</i> if it does not distort the signals of the other input circuit.
R/W	Read/Write (column title for system variable/signal type)
SELV	Safety extra low voltage
SFF	Safe failure fraction, portion of faults that can be safely controlled
SIL	Safety integrity level (in accordance with IEC 61508)
SILworX	Programming tool for HIMatrix systems
SNTP	Simple network time protocol (RFC 1769)
SRS	System.rack.slot addressing of a module
SW	Software
TMO	Timeout
W	Write: System variable/signal is provided with value, e.g., from the user program
r <sub>PP</sub>	Peak-to-peak value of a total AC component
Watchdog (WD)	Time monitoring for modules or programs. If the watchdog time is exceeded, the module or program enters the ERROR STOP state.
WDT	Watchdog time

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Figure 4: Sample MAC Address Label

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