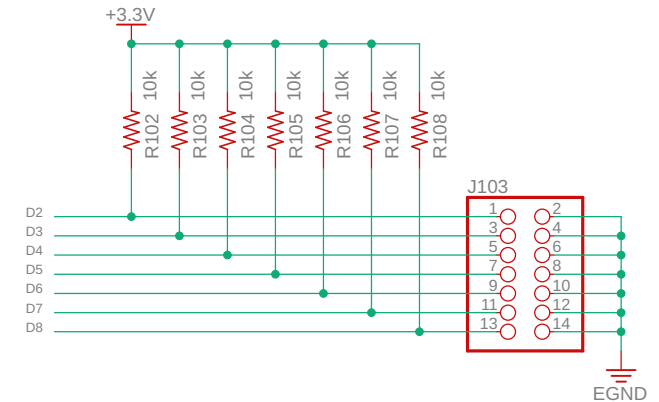
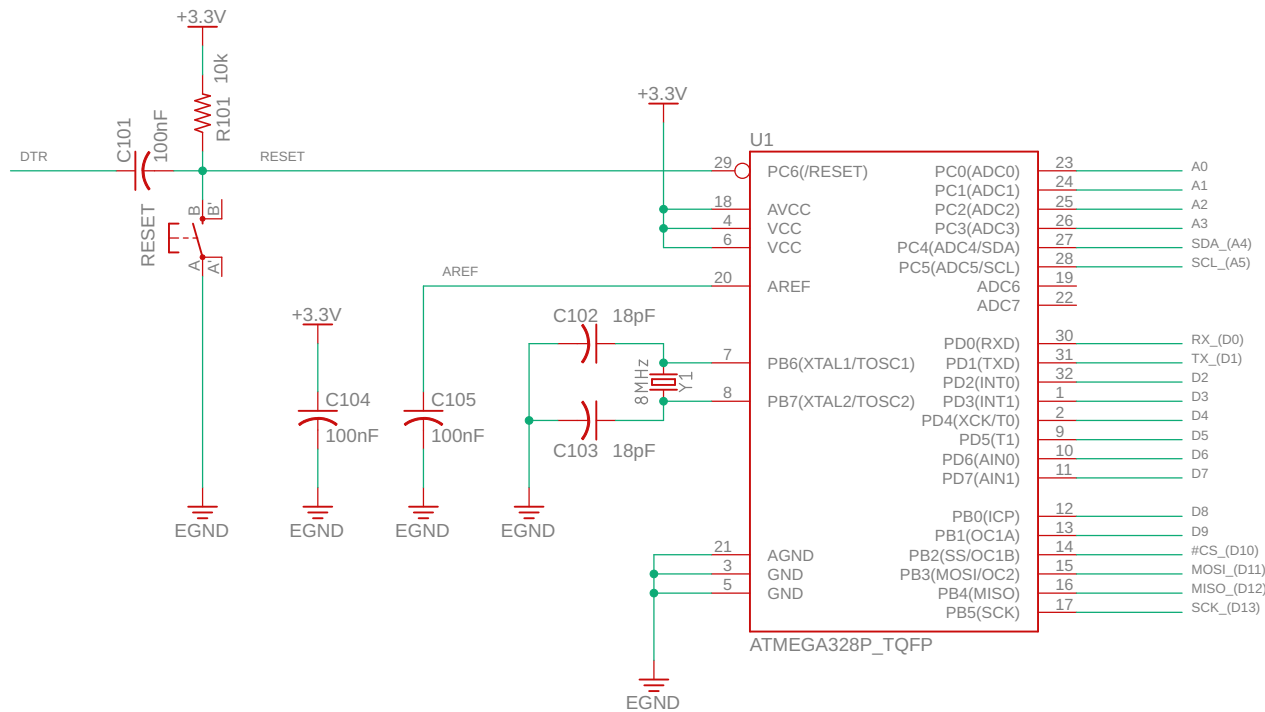
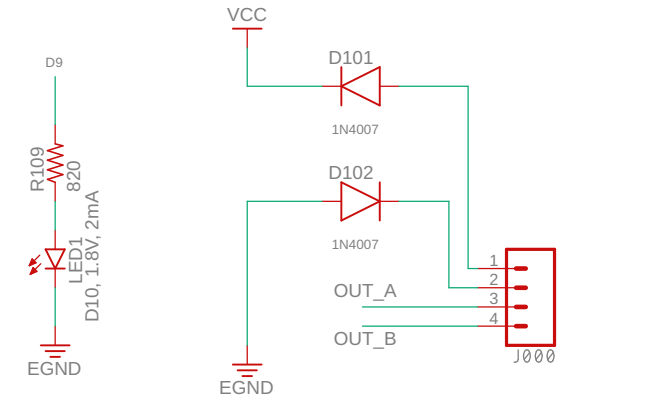
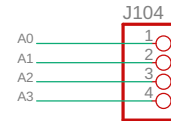
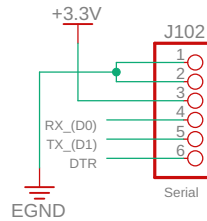
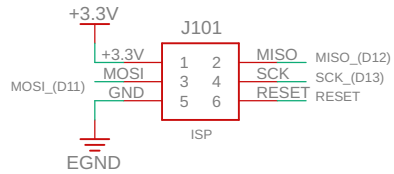


# MCU ATmega328P



The left diagram shows the L78M12ABDT-TR (U6) regulator. Its input (pin 1) is connected to a 12V supply and has a 330nF capacitor (C601) to ground (EGND). Its ground (pin 3) is connected to ground (EGND) and has a 100nF capacitor (C602) to ground (EGND). Its output (pin 4) is connected to the VCC supply.

The right diagram shows the MIC5205-3.3 (U7) regulator. Its VIN is connected to a 12V supply and has a 10uF capacitor (C701) to ground (EGND). Its EN pin is connected to ground (EGND). Its GND pin is connected to ground (EGND). Its VOUT is connected to the +3.3V supply and has a 10uF capacitor (C702) to ground (EGND). Its BYP pin is connected to ground (EGND) and has a 100nF capacitor (C703) to ground (EGND).

I2C Address:  
 HB1750: 0100011 - 0  
 BME280: 1110110 - 0

I2C Address:  
HB1750: 0100011 - 0x23  
BME280: 1110110 - 0x76

The schematic shows the MCP4922-E\_SL DAC circuit. The DAC is connected to a +3.3V supply and ground (EGND). The input signals are #CS\_(D10), SCK\_(D13), and MOSI\_(D11). The DAC outputs are DAC\_A and DAC\_B. The DAC is connected to a 100nF capacitor (C201) and a 10uF capacitor (C202) to ground.