MCU ATmega328P 1N4007 MOSI_(D11) D102 EGND 1N4007 EGND OUT B +<u>3.3</u>V C101 100nF RESET PC0(ADC0) PC1(ADC1) PC2(ADC2) PC3(ADC3) PC4(ADC4/SDA) PC6(/RESET) AVCC VCC VCC RESET PC5(ADC5/SCL) AREF ADC6 ADC7 AREF +3.3V C102 18pF PD0(RXD) PD1(TXD) PB6(XTAL1/TOSC1) PD1(IXD) PD2(INT0)) PD3(INT1) PD4(XCK/T0) PD5(T1) PD6(AIN0) PD7(AIN1) C104 C105 100nF 100nF C103 18pF EGND EGND PB0(ICP) PB1(OC1A) EGND AGND GND PB2(SS/OC1B) PB3(MOSI/OC2) PB4(MISO) PB5(SCK) ATMEGA328P_TQFP







