## MCU ATmega328P VCC-ISO +3.3V U5 +3.3V J101 VIN VOUT MISO MISO (D12) SCK SCK (D13) RESET RESET ΕN + C501 C502 C503 MOSI GND BYP MOSI (D11) 100nF 10uF 10uF RX\_(D0) MIC5205-3.3 TX\_(D1) ISP DTR GND-ISO GND-ISO GND-ISO GND-ISO GND-ISO GND-ISO GND-ISO W R101 U1 DTR RESET PC0(ADC0) PC1(ADC1) PC2(ADC2) PC3(ADC3) PC4(ADC4/SDA) PC5(ADC5/SCL) ADC6 PC6(/RESET) RESET -A2 AVCC АЗ VCC SDA\_(A4) VCC 28 SCL\_(A5) 19 22 AREF ADC7 10k +3.3V C102 18pF PD0(RXD) PD1(TXD) RX\_(D0) 31 TX\_(D1) PB6(XTAL1/TOSC1) D2 PD2(INT0) PD3(INT1) C104 C105 $\overline{\circ}$ PB7(XTAL2/TOSC2) PD4(XCK/T0) PD5(T1) D4 100nF 100nF C103 18pF 10 PD6(AIN0) PD7(AIN1) D3 D6 11 D7 D4 D5 PB0(ICP) PB1(OC1A) PB2(SS/OC1B) D8 D6 GND-ISO GND-ISO D9 14 #CS\_(D10) AGND 15 MOSI\_(D11) GND PB3(MOSI/OC2) MISO\_(D12) PB4(MISO) PB5(SCK) GND SCK\_(D13) ATMEGA328P\_TQFP GND-ISO





