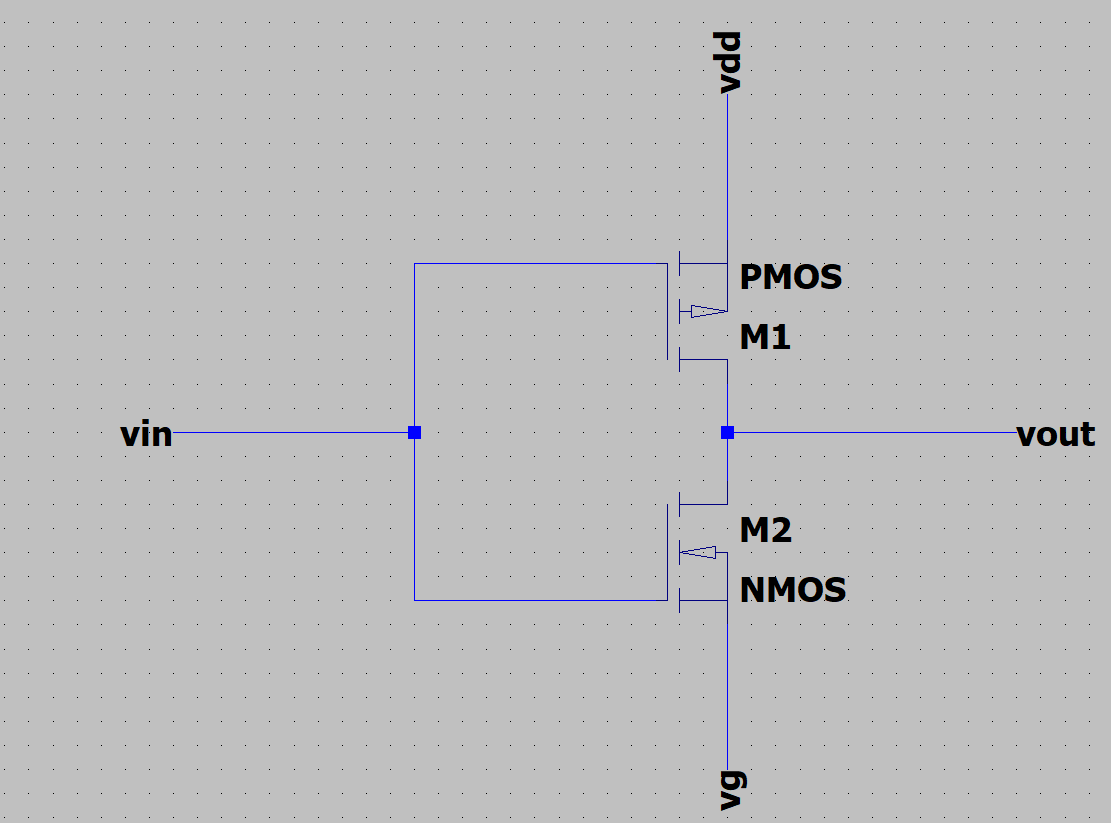
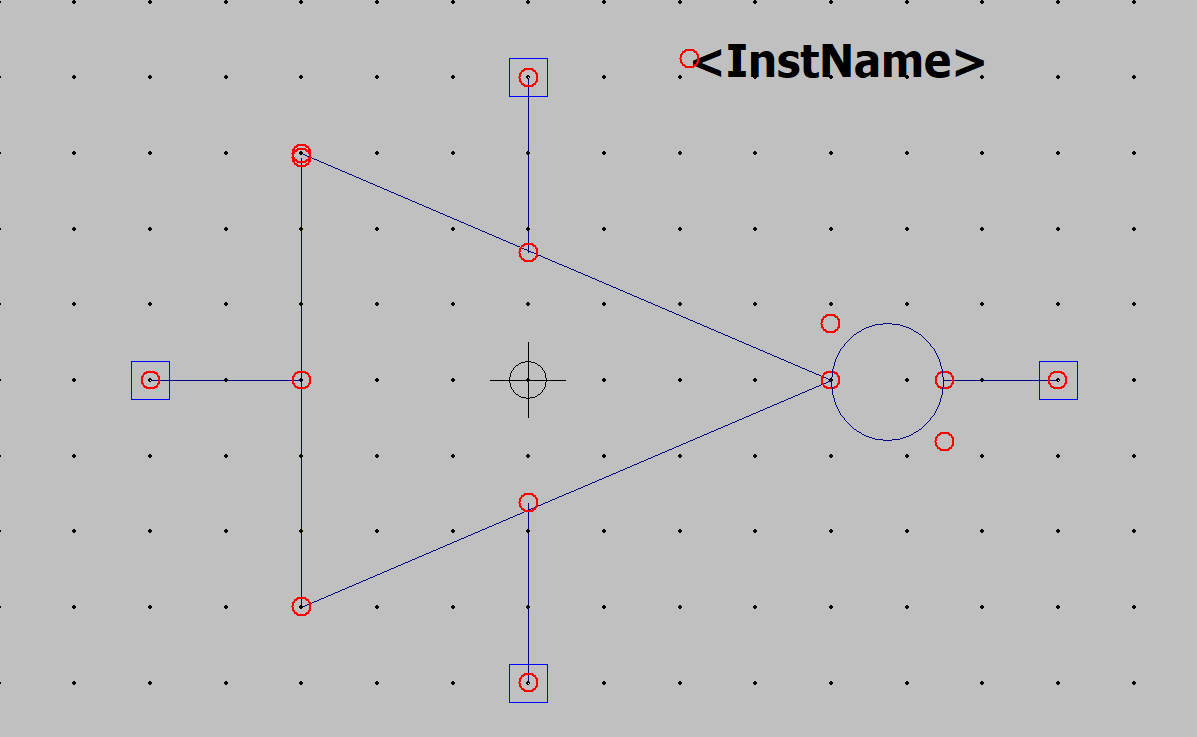
Circuit Diagram:

NOT:

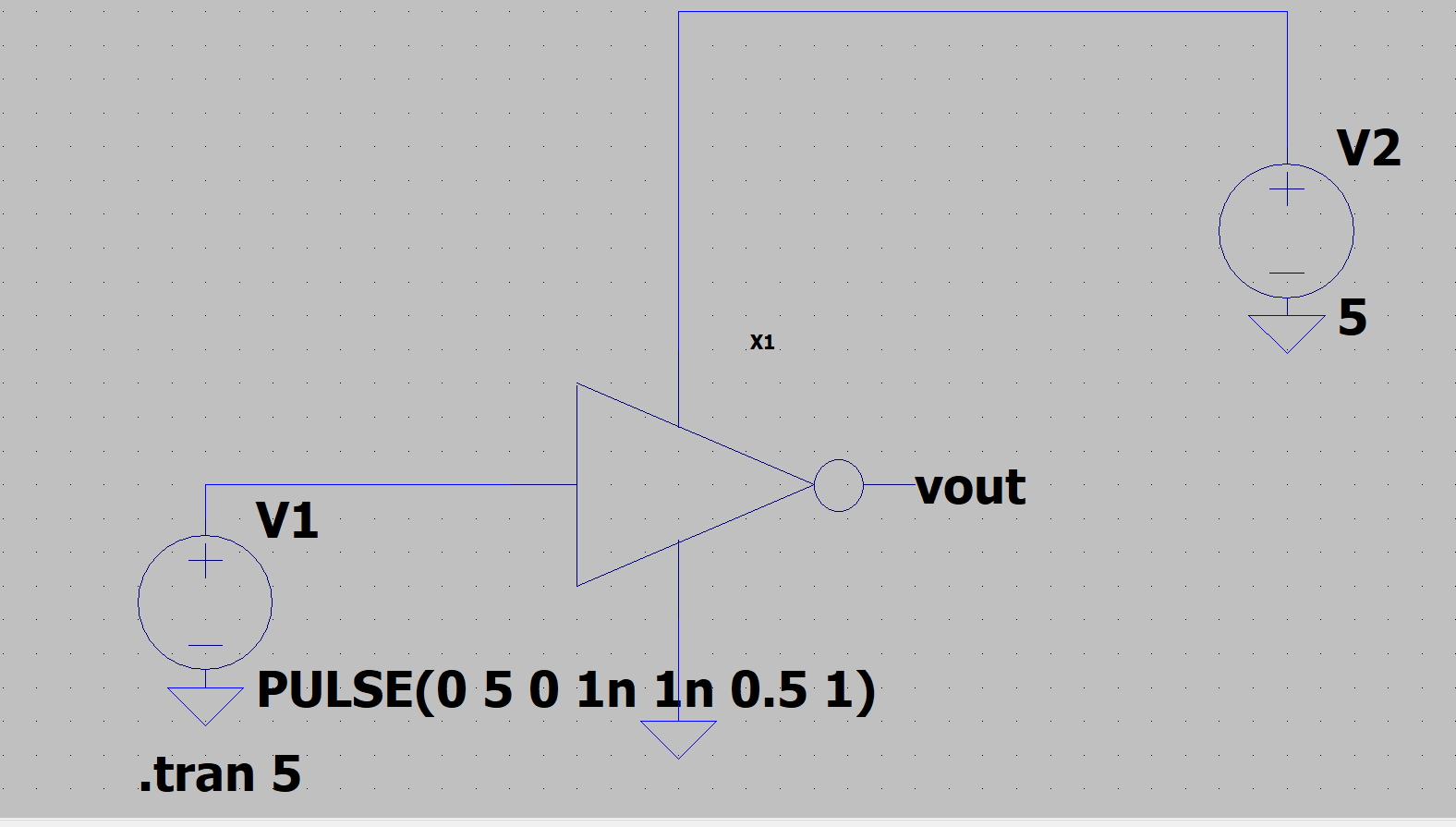
Schematic



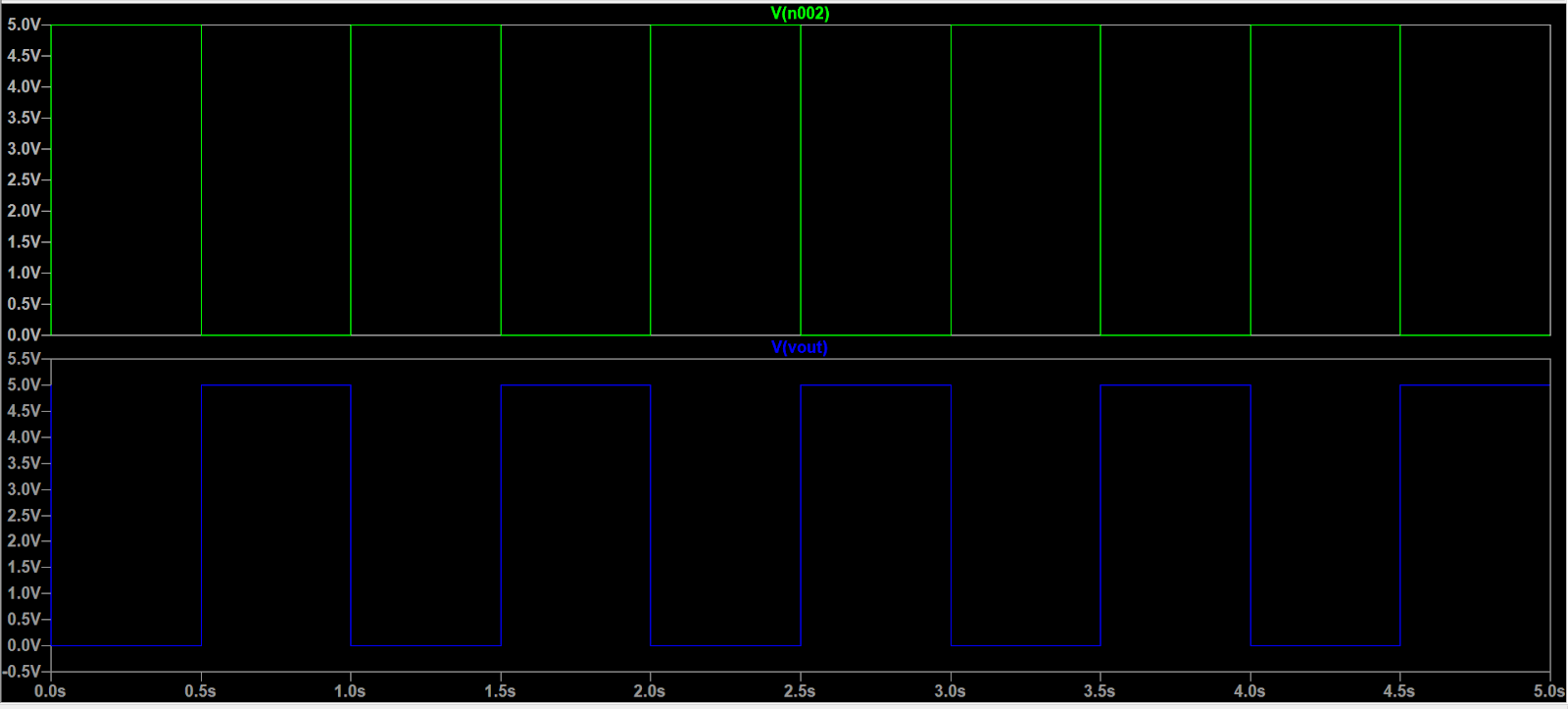
Symbol:



Testbench schematic:

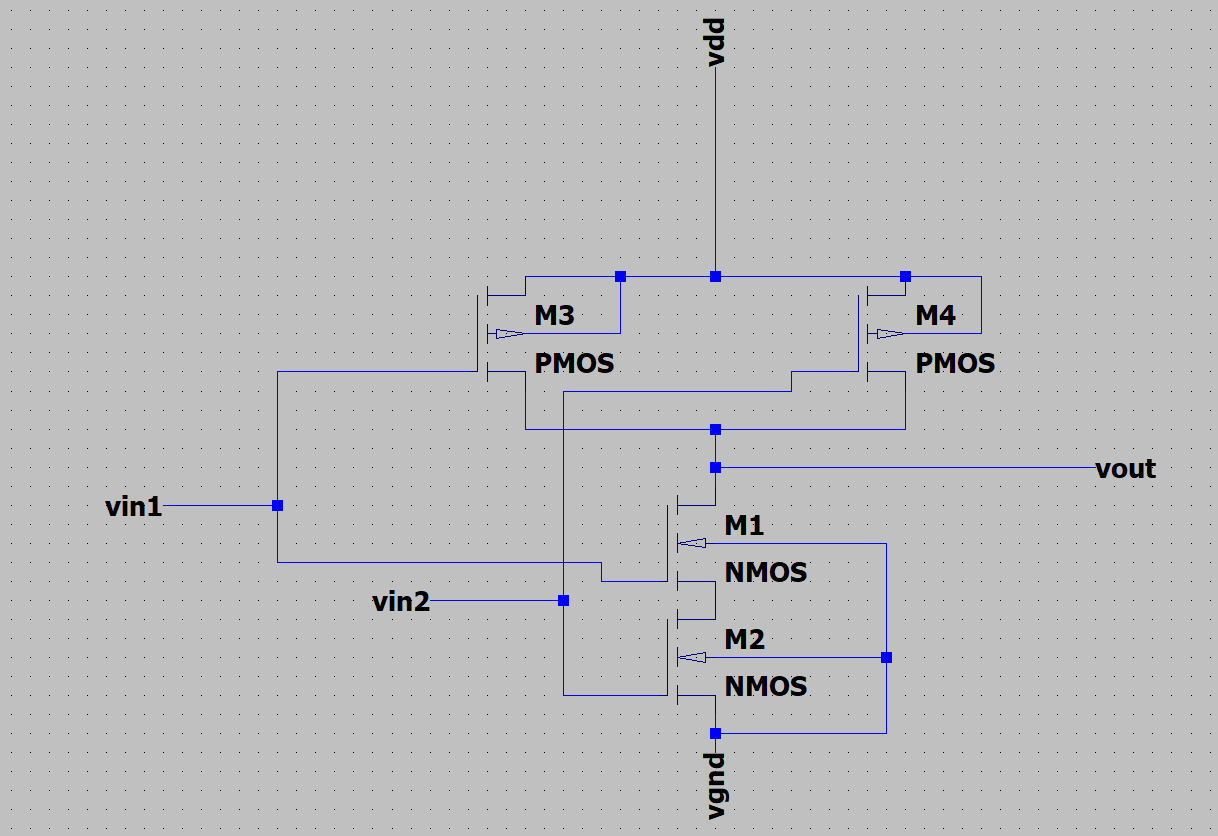


Output:

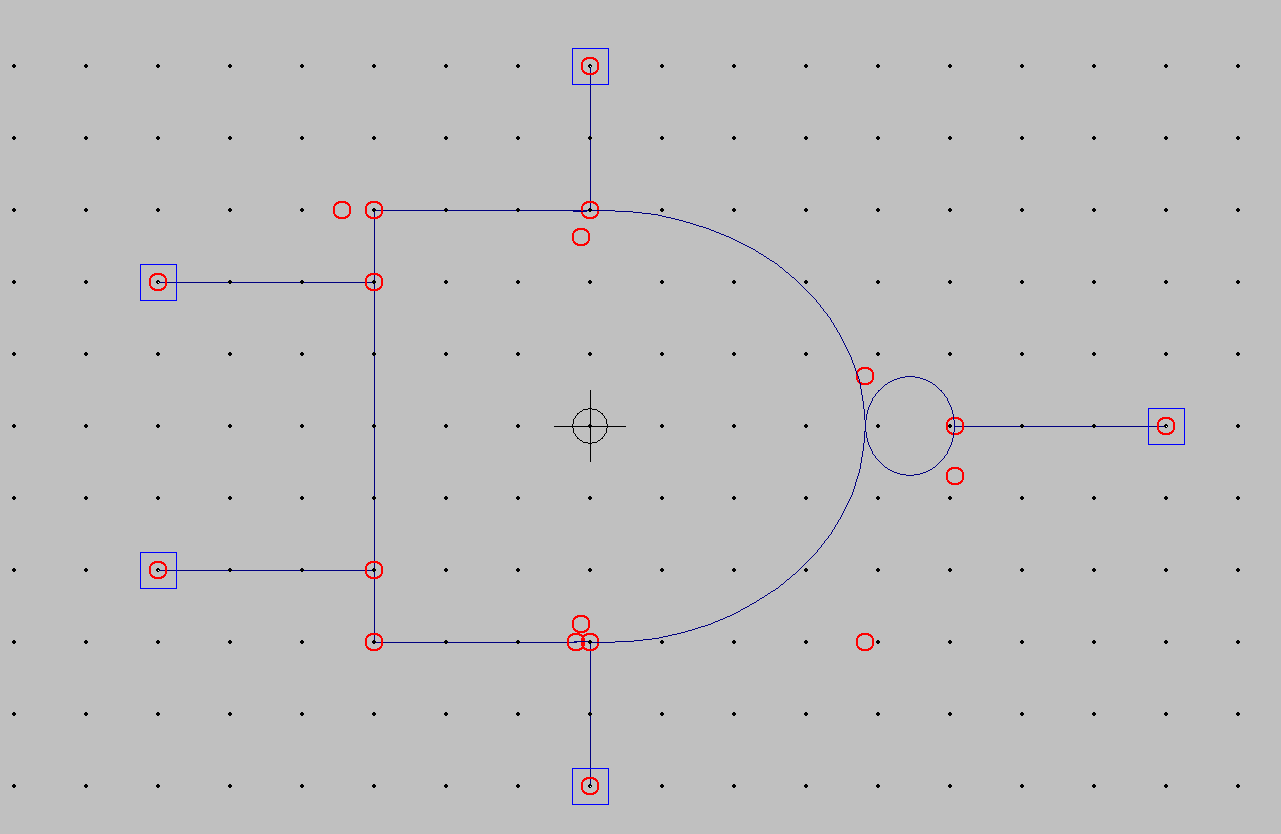


NAND:

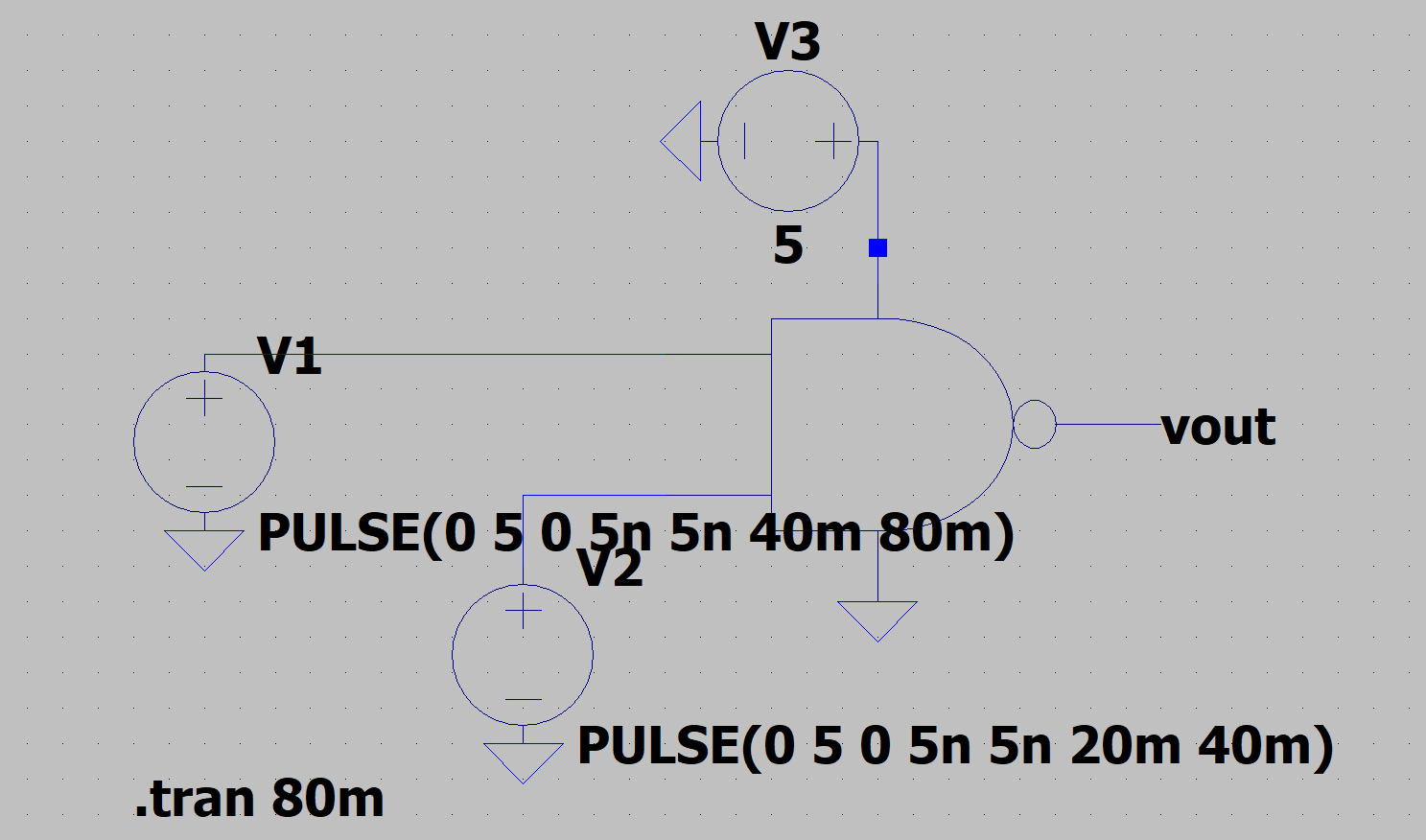
Schematic



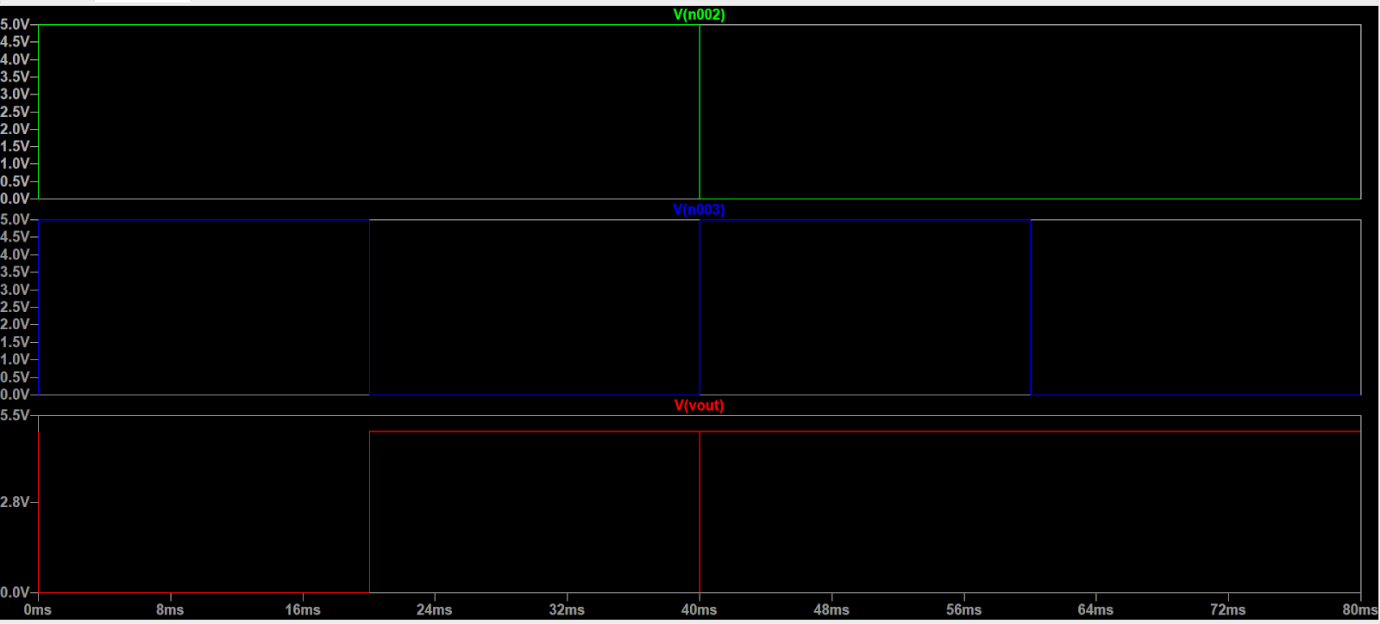
Symbol:



Testbench schematic:

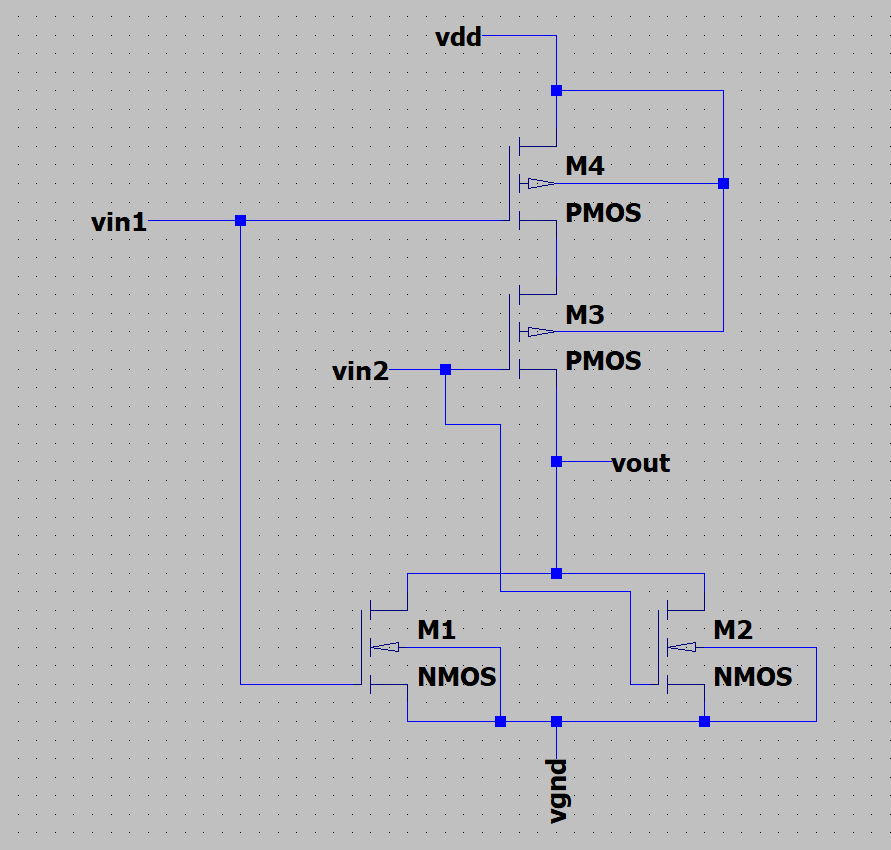


Output:

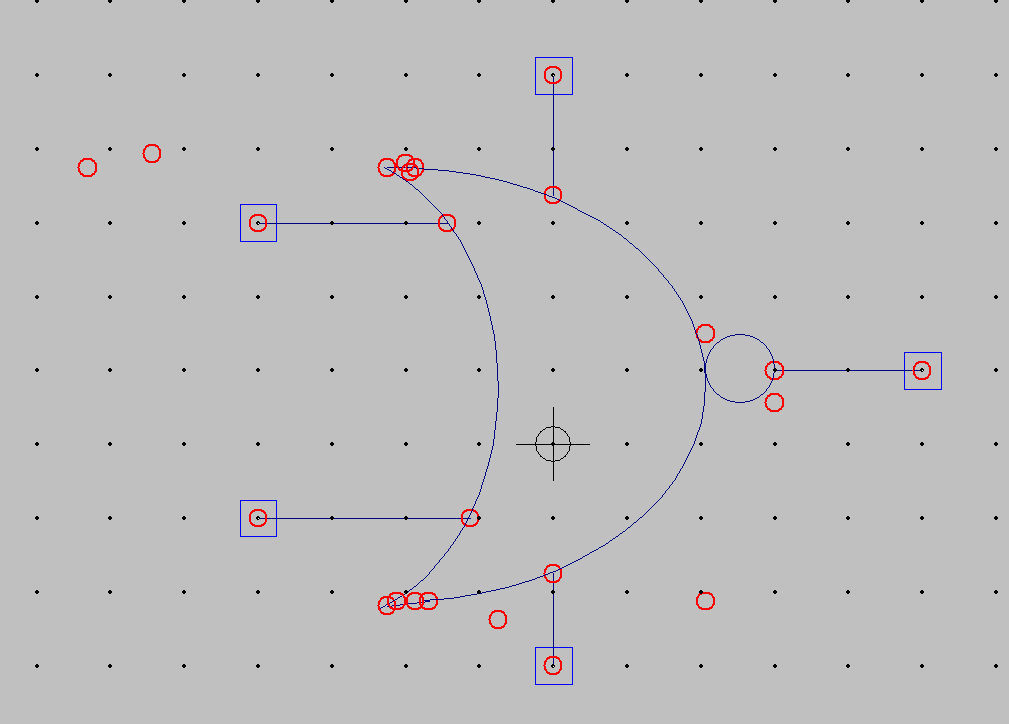


NOR:

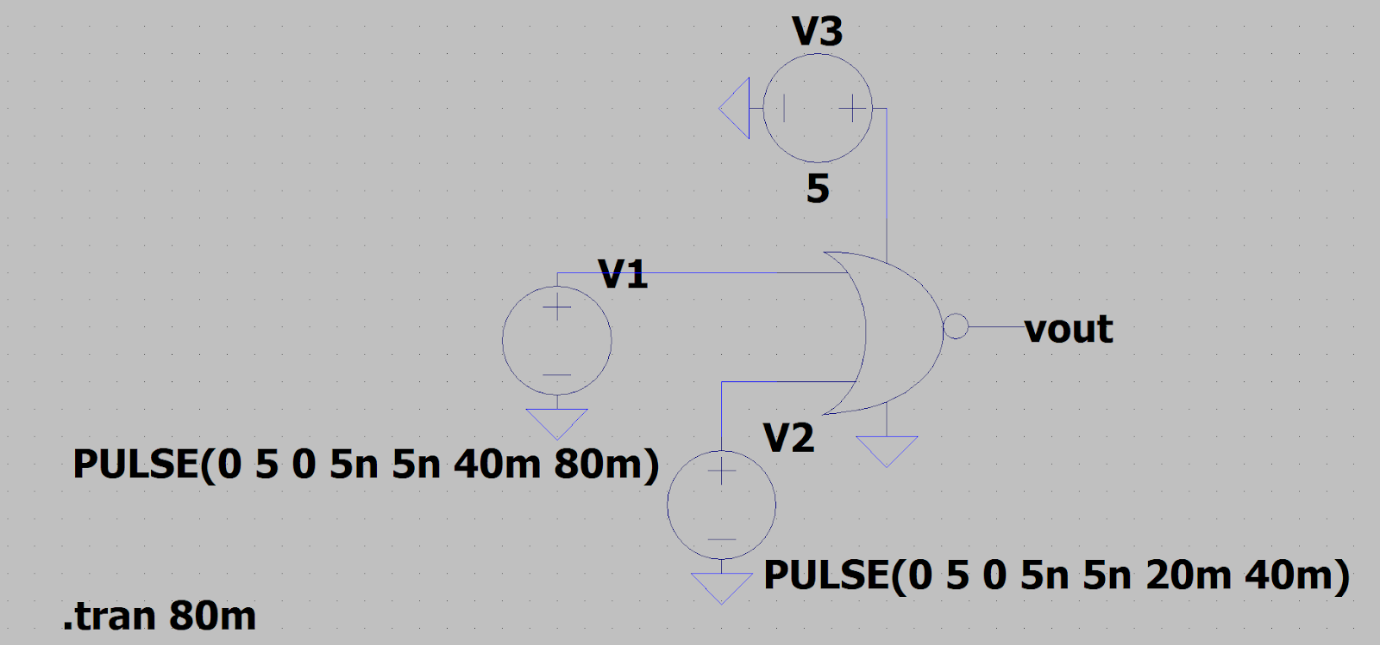
Schematic



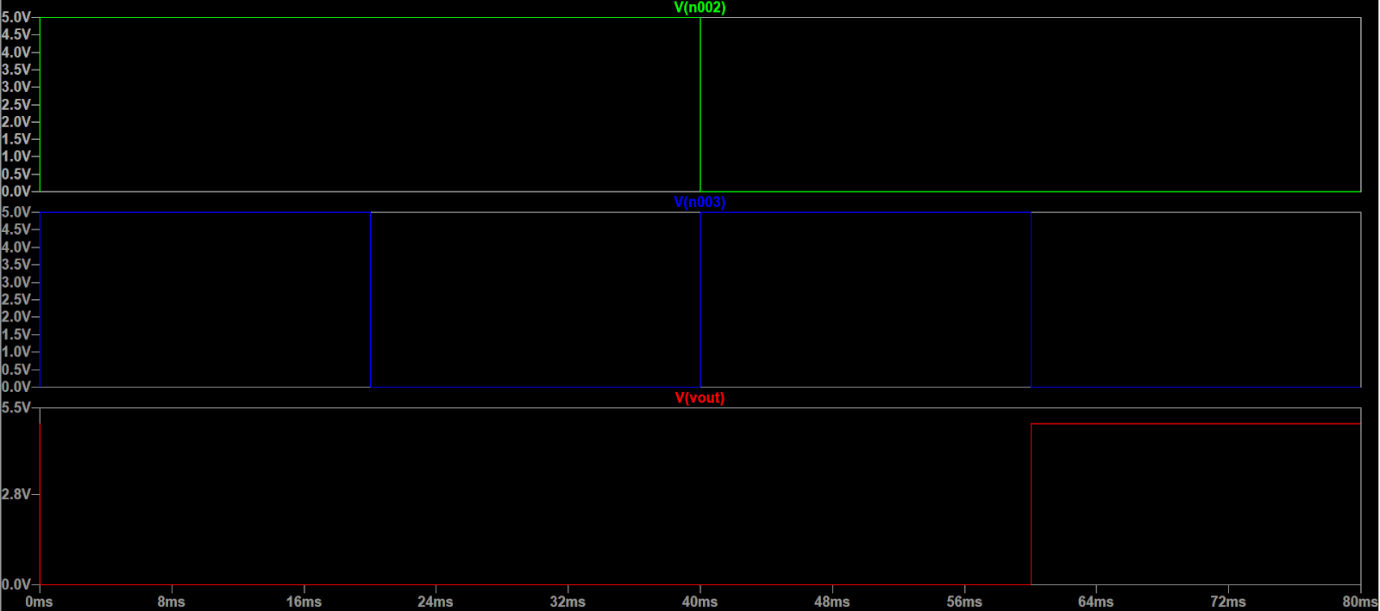
Symbol:



Testbench schematic:

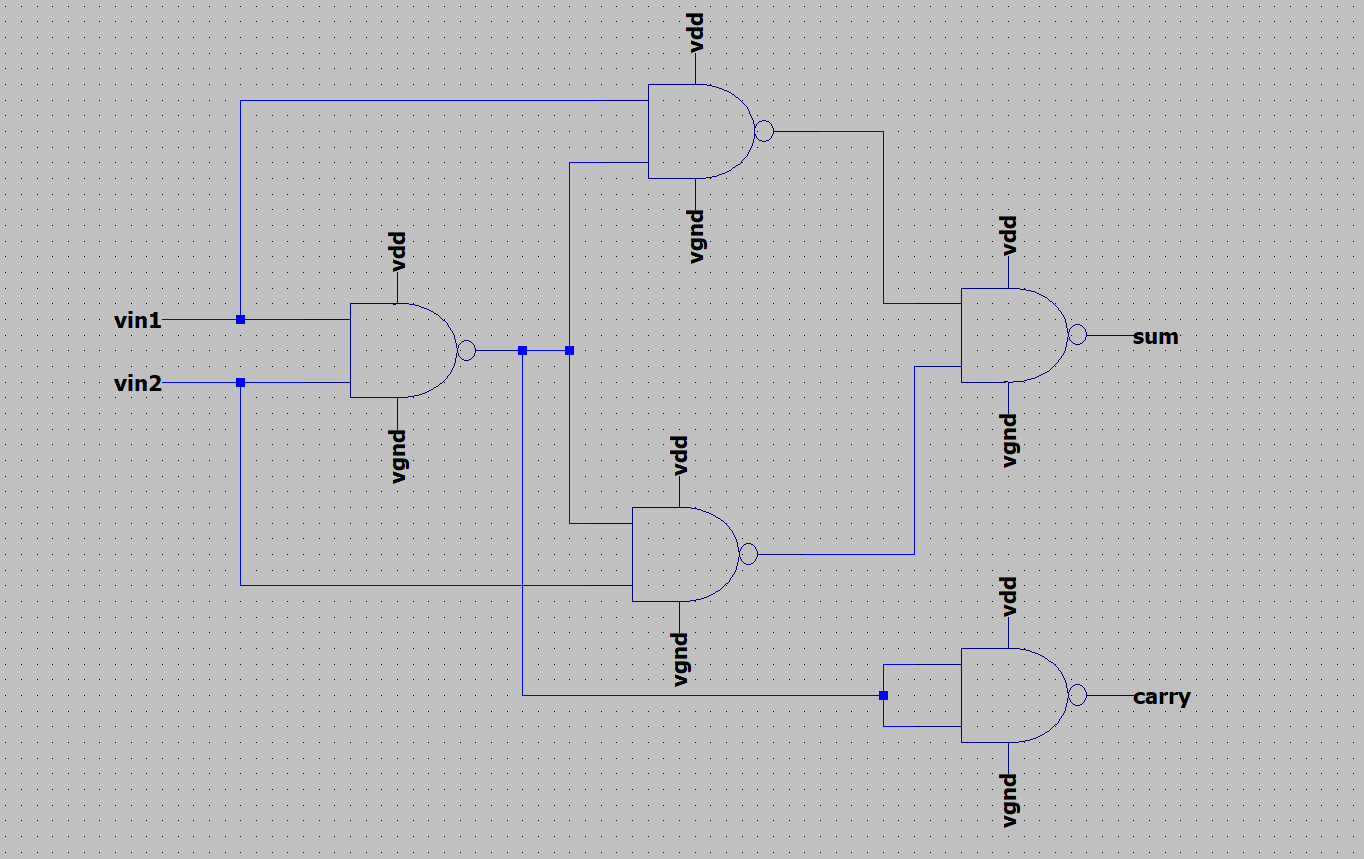


Output:

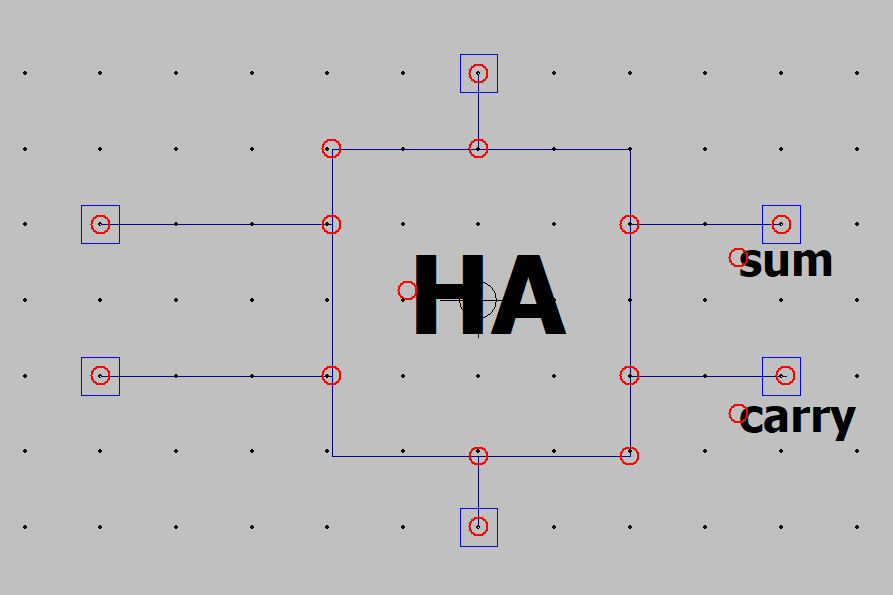


Half adder:

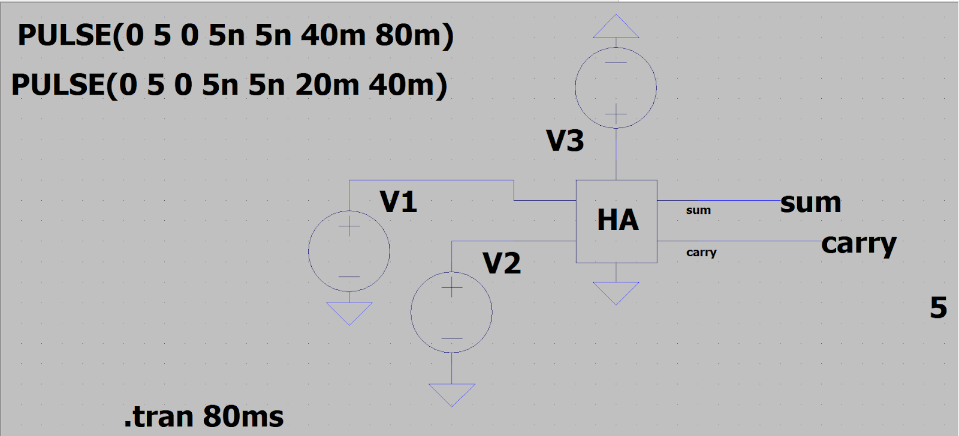
Schematic



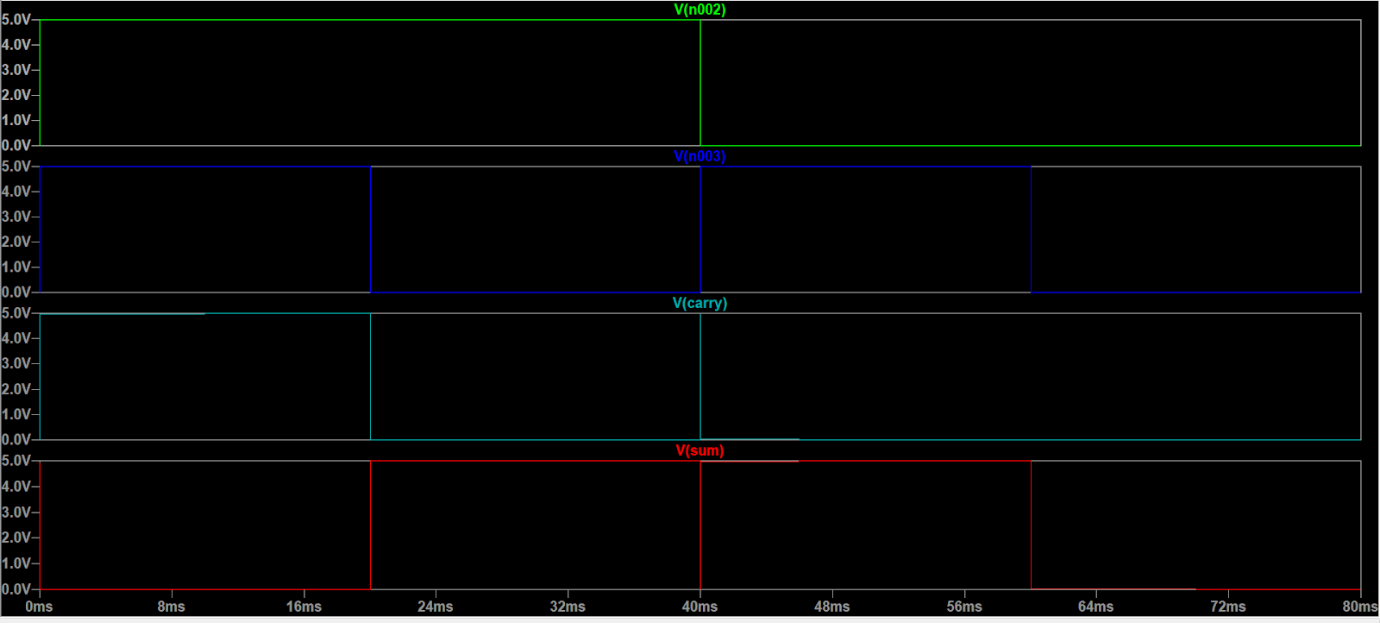
Symbol:



Testbench schematic:

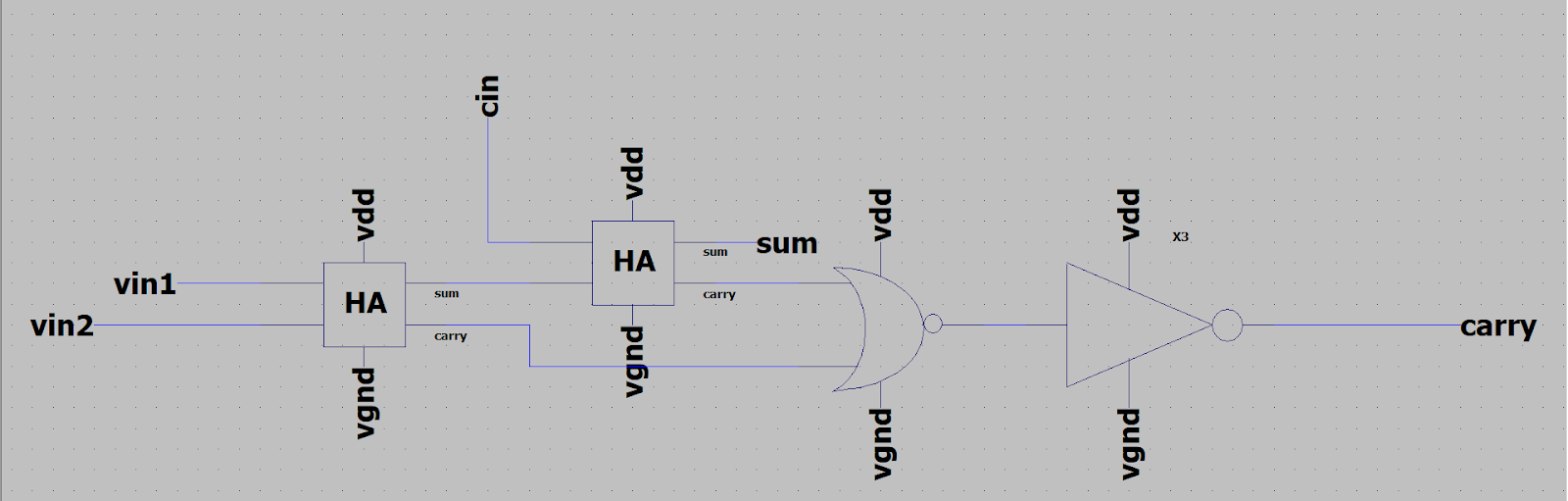


Output:

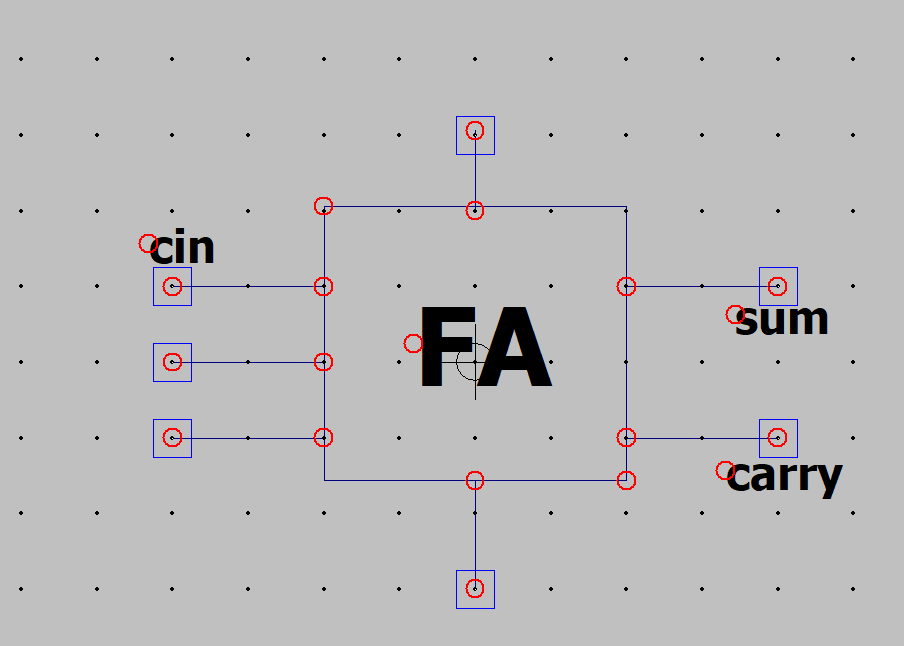


Full adder:

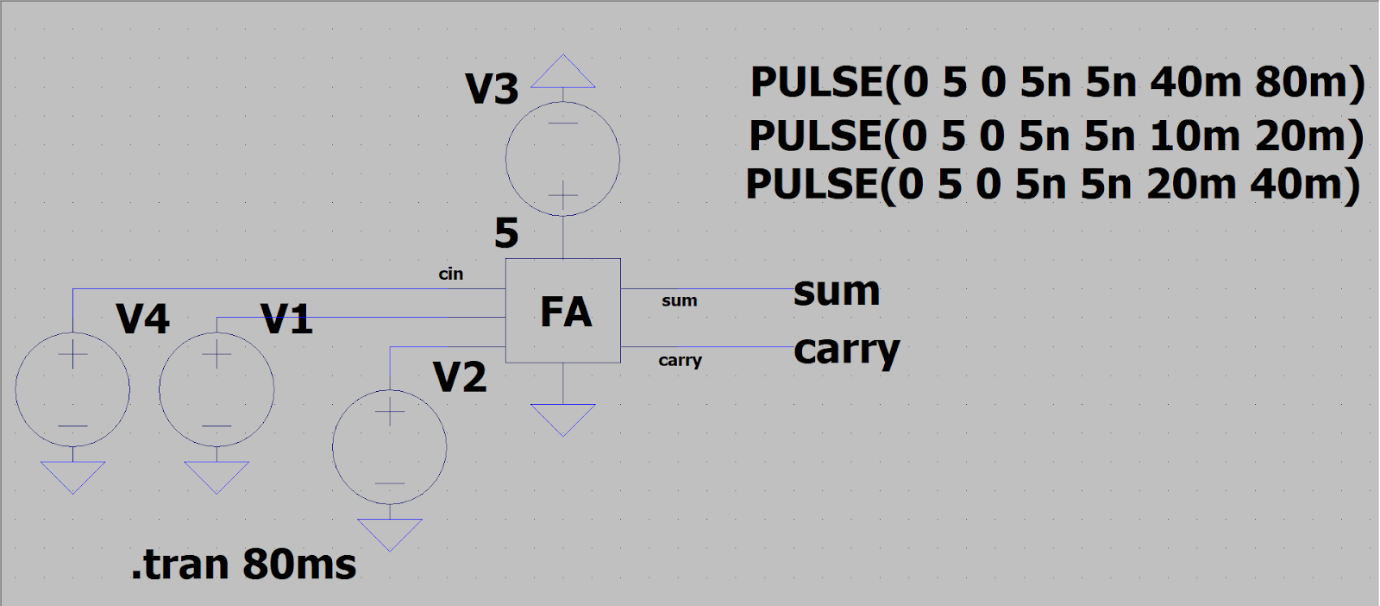
Schematic:



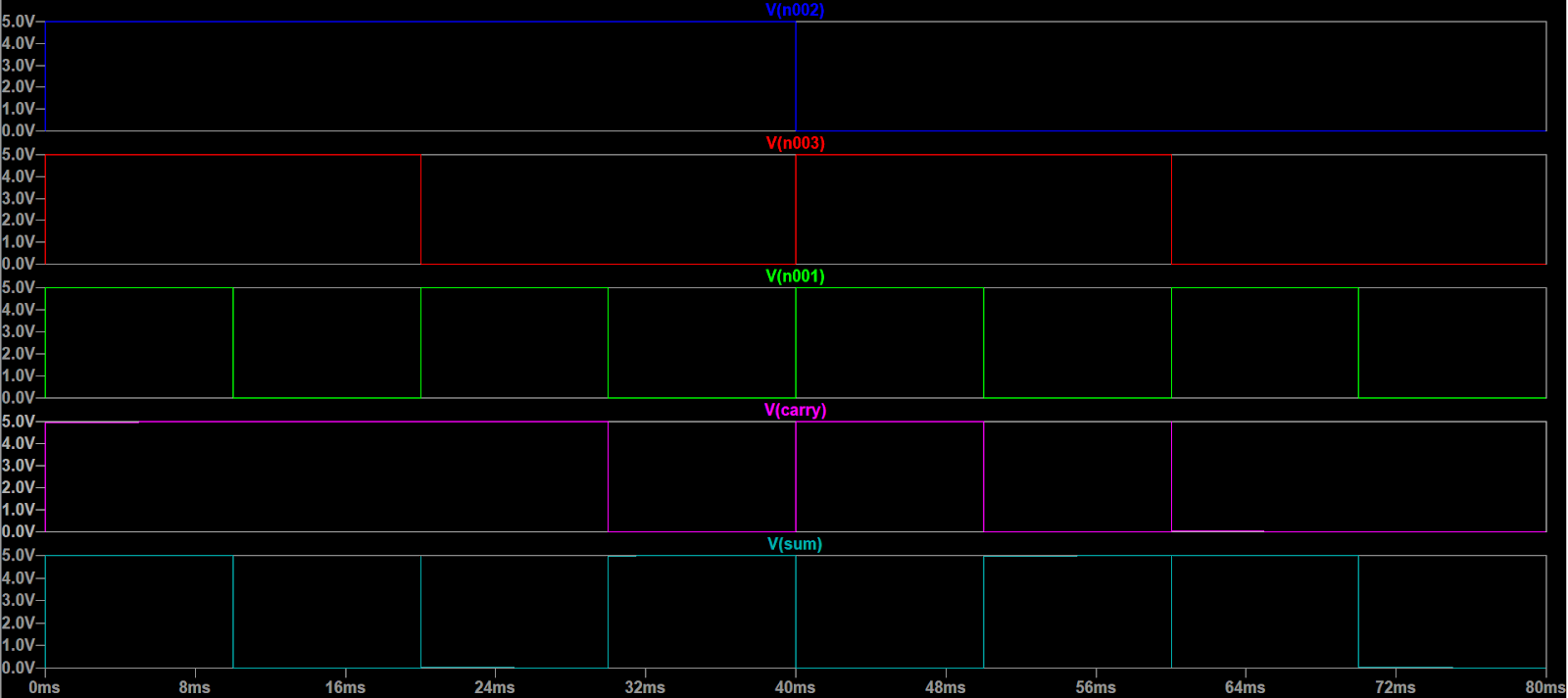
Symbol:



Testbench schematic:

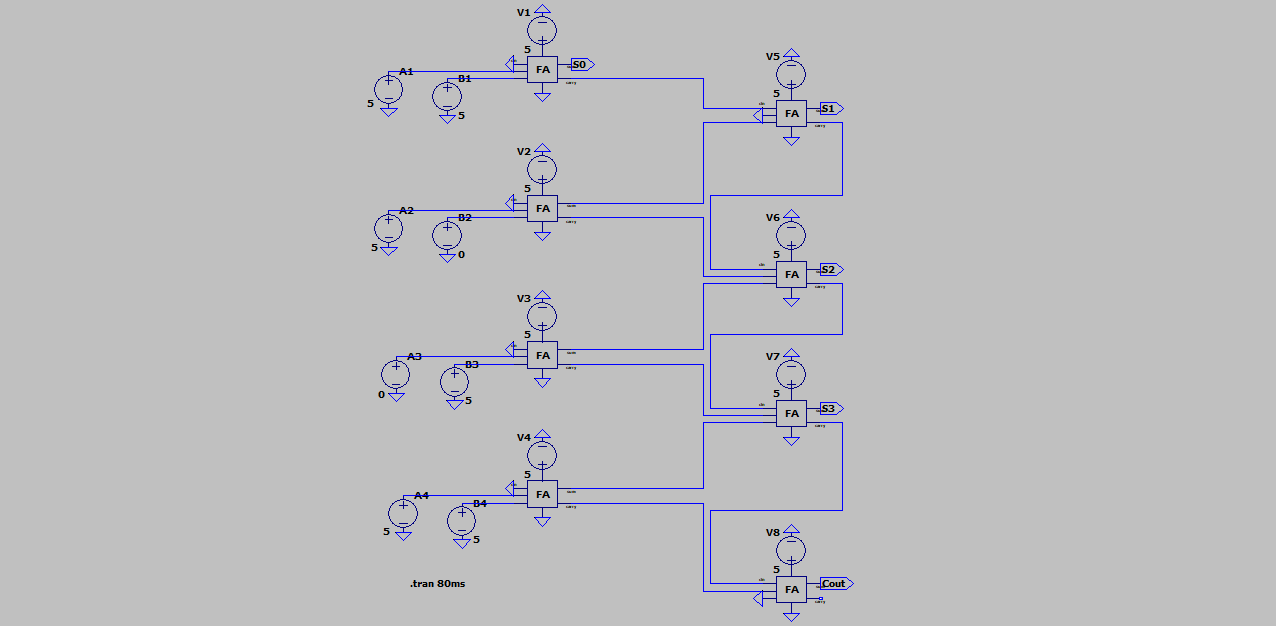


Output:



Carry Save 4-bit Adder

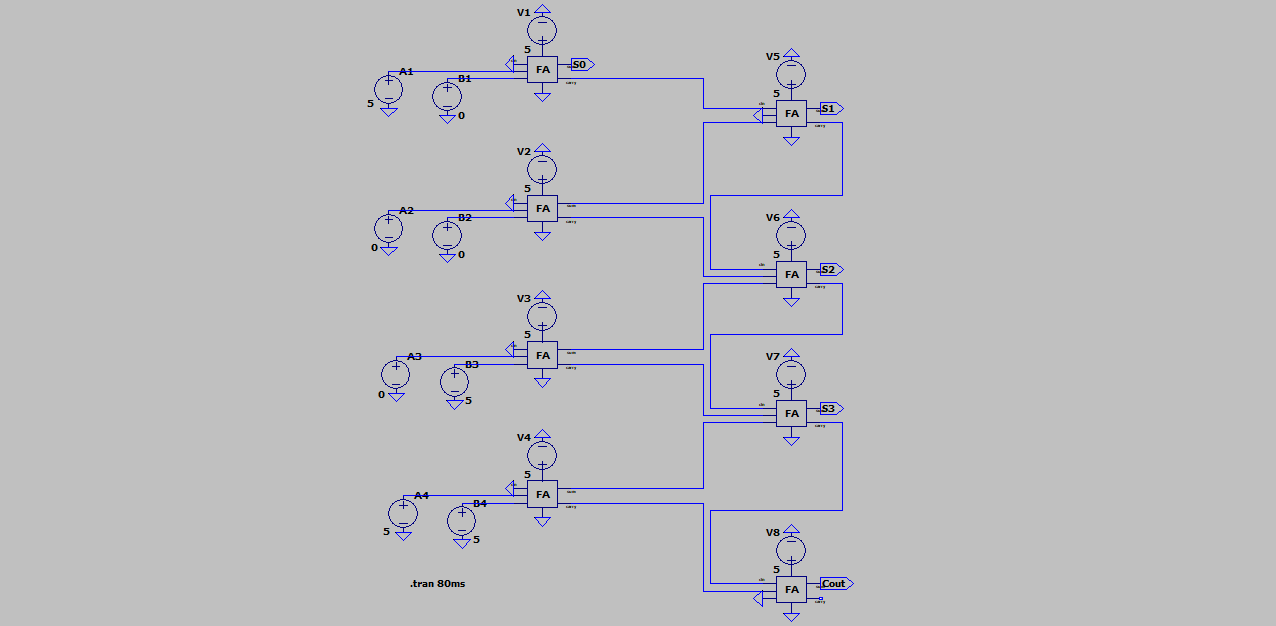
Schematic for 1011+1101



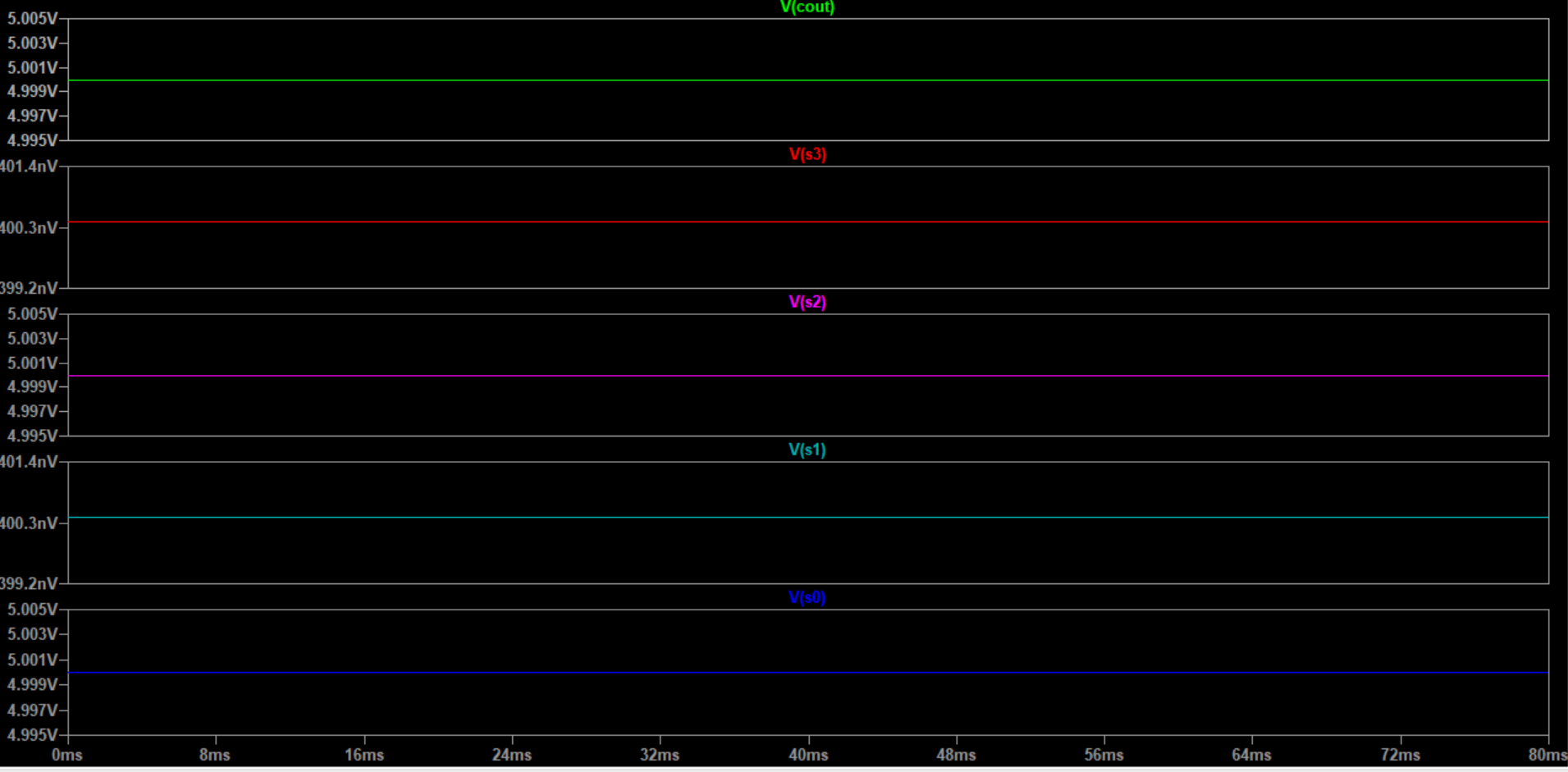
Output:

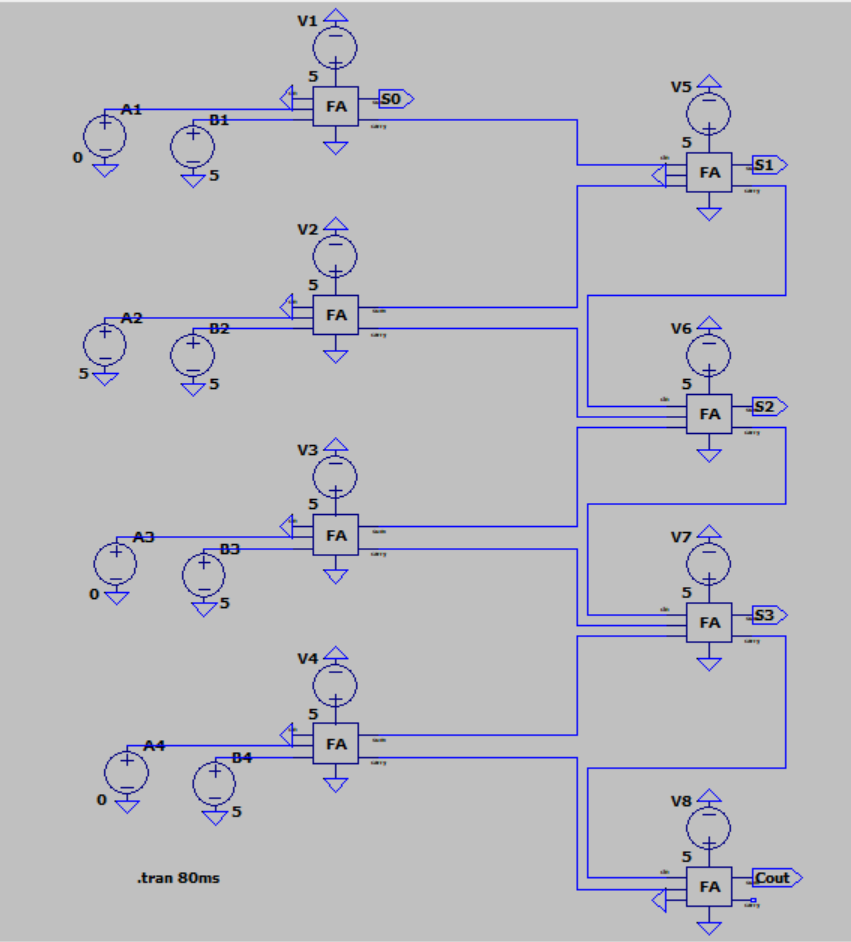


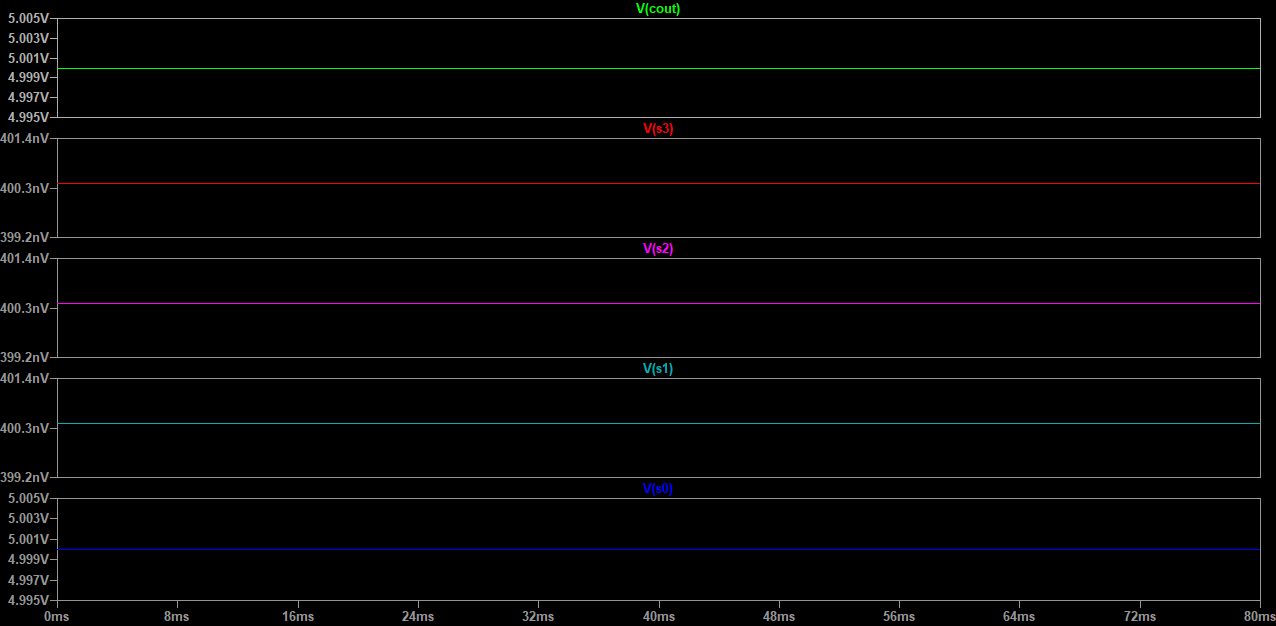
Schematic for 1001+1100



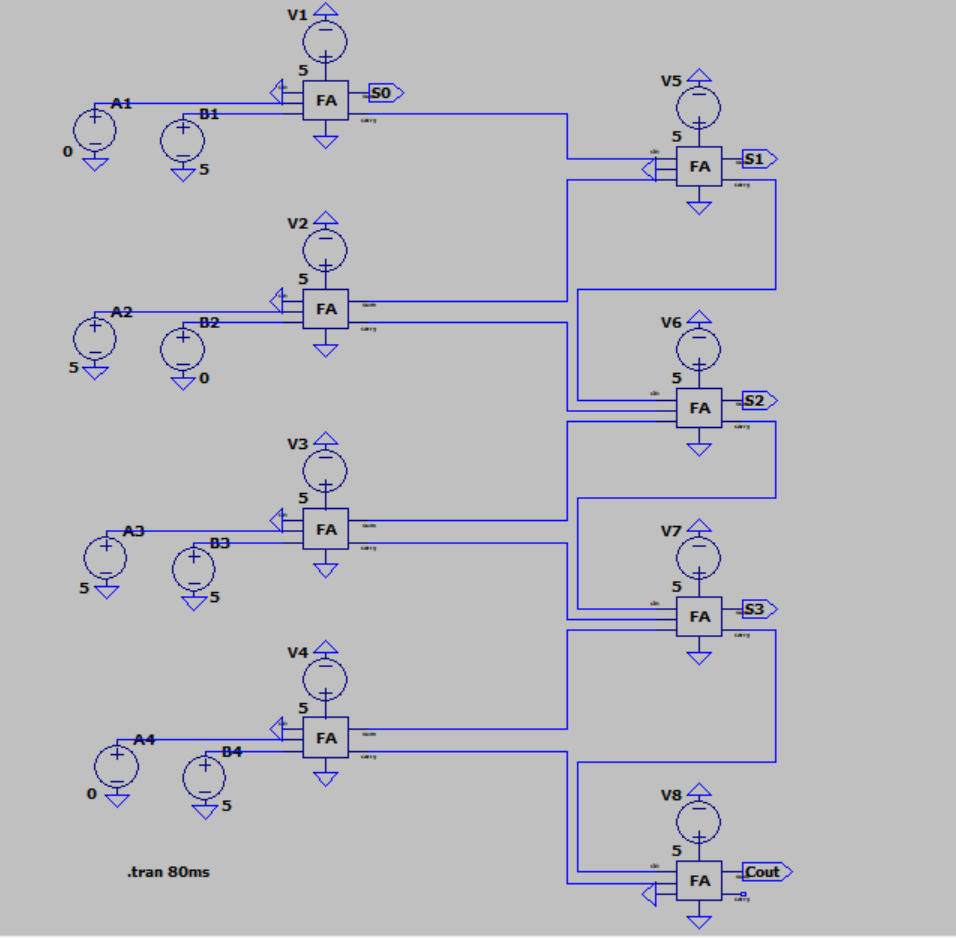
Output:



Schematic for 0010+1111Output:



Schematic for 0110+1101



Output:



AIM

CIRCUIT WITH OUTPUT AND CALCULATIONS

FUNCTION OF CIRCUIT

WORKING

APPLICATIONS

ADVANTAGES

DISADVANTAGES

CONCLUSION