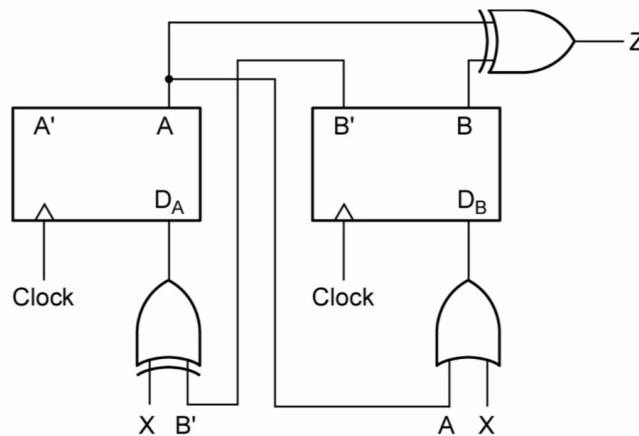


Unit-5 Assignment

1. Implement gated SR, JK, D and T flip-flops using NAND gate only.
2. What do you mean by race condition in JK Flip Flop and mention the methods to overcome it? Explain Master Slave flip-flop using JK flip flop with logic circuit, truth table and timing diagram.
3. Analyze the following circuit diagram and draw the state table and state diagram.



4. Analyze the following circuit diagram and draw the state table and state diagram.

