V_{Source} V_{Point} V_R = ? = V_o Sin(wl)

Before solving, It is always a good idea to be familiar with individual component behavior.

 \rightarrow Possive component. \rightarrow Acts as a representation of load. \rightarrow I = Ve/R = current.

→ Possive component Vc → Tries to maintain voltage around it

-> Discharges due to lood connected to it

-> Store charges by maintaining voltage between plates

> Discharging

Vc = Vinitial e time possed in discharging

RC

load.

- Charging

Vc = (1 - e Rsource) Vsource



→ Po not allow current to poss through when VD is less than (0.7V)

Value cames from the theory of semiconductors physics.
That voltage is a function of semi-conductor doping.

-> Provides infinite resistance for voltage less than ov.

-10V - + -

Vollage drop accross it

Capacitor would charge "if source Voltage at that instant of time is greater than capacitor voltage.

Capacitor would discharge if source voltage is less than capacitor voltage, discharge would be via resistor.