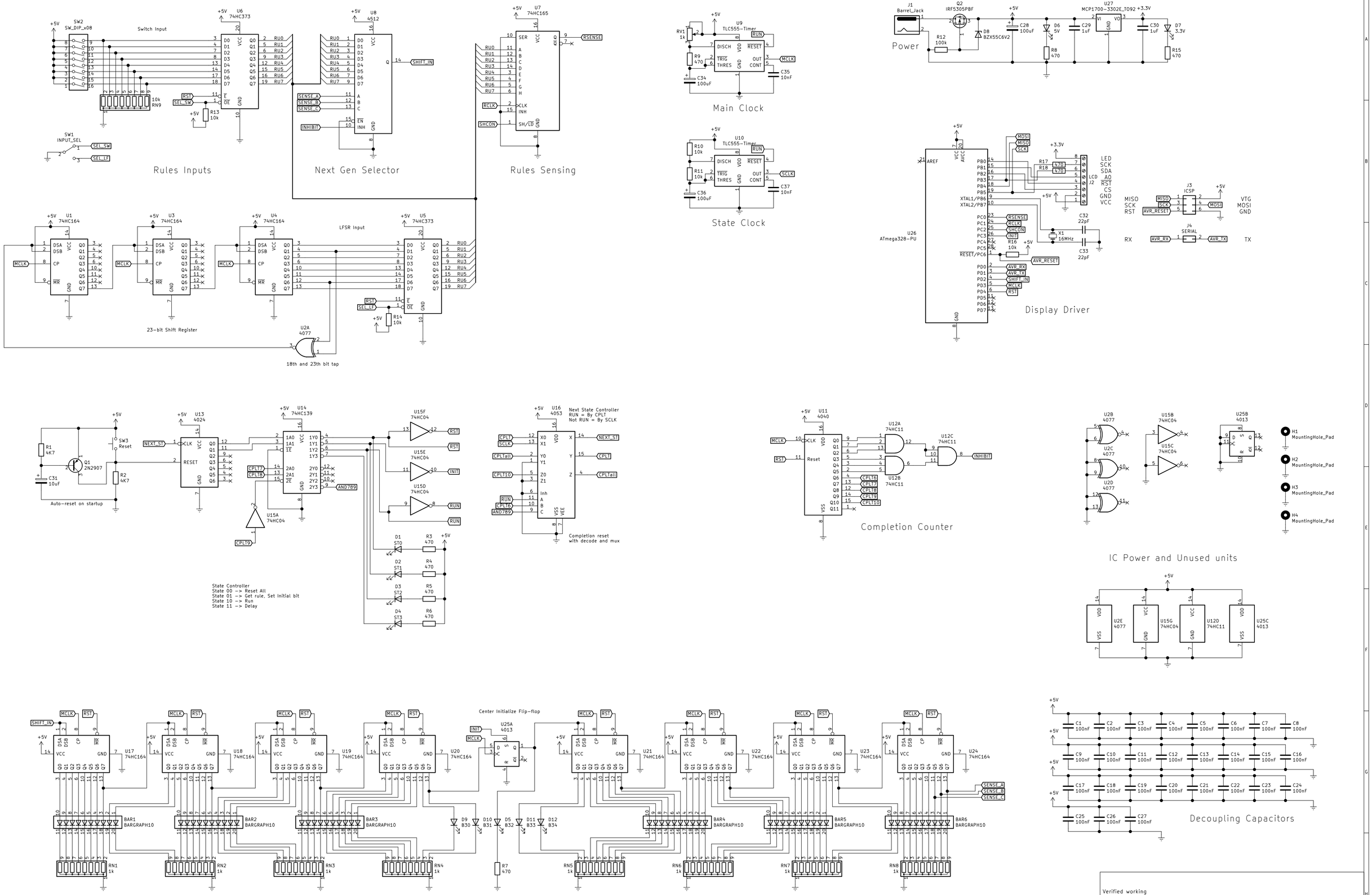
Rules Inputs
Switch Input
Next Gen Selector
Rules Sensing
Main Clock
State Clock
Power
Display Driver
23-bit Shift Register
LFSR Input
18th and 23th bit tap
Auto-reset on startup
State Controller
State 00 -> Reset All
State 01 -> Get rule, Set Initial bit
State 10 -> Run
State 11 -> Delay
Next State Controller
RUN = By CPLT
Not RUN = By SCLK
Completion reset
with decode and mux
Completion Counter
IC Power and Unused units
Center Initialize Flip-flop
65-bit Shift Register
Decoupling Capacitors
Verified working
Rev.3 : Inprovements from rev 2
Sleepy Pony Labs
Sheet: /
File: automata.sch
Title: Elementary Cellular Automata Generator [Standard]
Size: A2
Date: 2020-08-26
Rev: 3
KiCad E.D.A. kicad (5.1.6)-1
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