2020 Digital IC Design Homework 4: RC4 Encrypt

NAME	范皓	 钢		JIIICWOIK 4. KC4	71					
Student ID	P7608	P76081263								
Simulation Result										
Functional simulation	Pass	Gate-level simulation	Pass	Gate-level simulation time	120716883 70749883	Q /				
#										
Synthesis Result										
Total logic el	ements		4,40	04						
Total memory bit				192						
Embedded multiplier 9-bit element 0										
Flow Status Quartus II Version Revision Name RC4 Top-level Entity Name Family Device Timing Models Met timing requirements Total logic elements Dedicated logic registers Total registers Total pins Total virtual pins Total virtual pins Total PLLs Flow Status Successful - Sun May 24 23:34:47 2020 10.0 Build 262 08/18/2010 SP 1 SJ Full Version RC4 Cyclone II EP2C70F896C8 Final										
Description of your design										

如何使用 RC4 演算法做加解密								

Scoring = (Total logic elements + total memory bit + 9*embedded multiplier 9-bit element) × (gate-level simulation time in \underline{ns})