2020 Digital IC Design Homework 3: Approximate Average

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| **Simulation Result** | | | | | | | | |
| Functional simulation | Pass | | Gate-level simulation | Pass | | | Gate-level simulation time | 208520 (ns) |
| (your pre  -s im result) | | | | | | (your post-si  m result) | | |
| **Synthesis Result** | | | | | | | | |
| Total logic elements | | | | | 505 | | | |
| Total memory bit | | | | | 0 | | | |
| Embedded multiplier 9-bit element | | | | | 0 | | | |
|  | | | | | | | | |
| **Description of your design** | | | | | | | | |
| 目前只有想到以迴圈的方式來找到closely value，但整體速度很慢，相要加clock調到105左右才能all pass. | | | | | | | | |

*Scoring = (Total logic elements + total memory bit + 9\*embedded multiplier 9-bit element) (gate-level simulation time in ns)*