2020 Digital IC Design Homework 5: Sobel

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| NAME | | 范皓翔 | | | | | | |
| Student ID | | P76081263 | | | | | | |
| **Simulation Result** | | | | | | | | |
| Functional simulation | PASS | | Gate-level simulation | PASS | | | Gate-level simulation time | 19660971604 (ps) |
|  | | | | | | (your post-sim result) | | |
| **Synthesis Result** | | | | | | | | |
| Total logic elements | | | | | 1238 | | | |
| Total memory bit | | | | | 0 | | | |
| Embedded multiplier 9-bit element | | | | | 0 | | | |
|  | | | | | | | | |
| **Description of your design** | | | | | | | | |
| 這次作業讓我學會如何讀寫MEMORY，花了我非常多的時間  最小cycle數25. | | | | | | | | |

*Scoring = (Total logic elements + total memory bit + 9\*embedded multiplier 9-bit element) (gate-level simulation time in ns)*