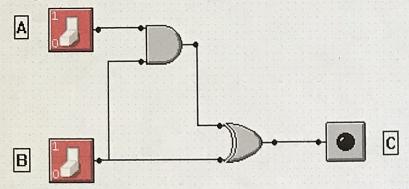
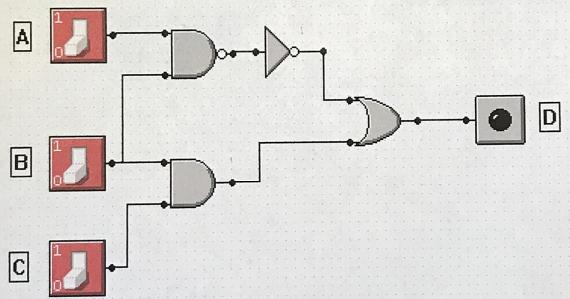
Name: Ashuin G. Date Due: Tuesday, April 14, 2020 Mark: [APPL: /10]

Using MultimediaLogic construct truth tables for the following circuits.



INPUTS		OUTPUT	
A	В	C	
0	0	0	
0	1	9	
1	0	0	
1	1	0	



INPUTS			OUTPUT
A	В	C	D
9	0	0	0
0	0	1	0
0		0	0
0	1		1
1	0	0	0
1	0	1	0
1		0	1
(1		1