# Project Demo: Chip Validation

## Goals

1. All derived test cases must be fulfilled, documented, and analysed in a test report in 1.5 months after the arrival of samples in the company (expected chip arrival date: 01.10.2024).
2. The validation plan of the ADC must be planned and released by 01.05.2024.
3. The chip validation PCB must be designed, tested, and released by 01.09.2024.
4. The Software Validation Framework must pass all derived test cases for the software specification by 15.08.2024.
5. A new Digital Memory Oscilloscope from “LabTools GmbH” must be in the laboratory and ready for the validation measurement of the ADC until 15.08.2024.

## Non-Goals

1. The specified EMV and ESD measurements have not to be done by our team.