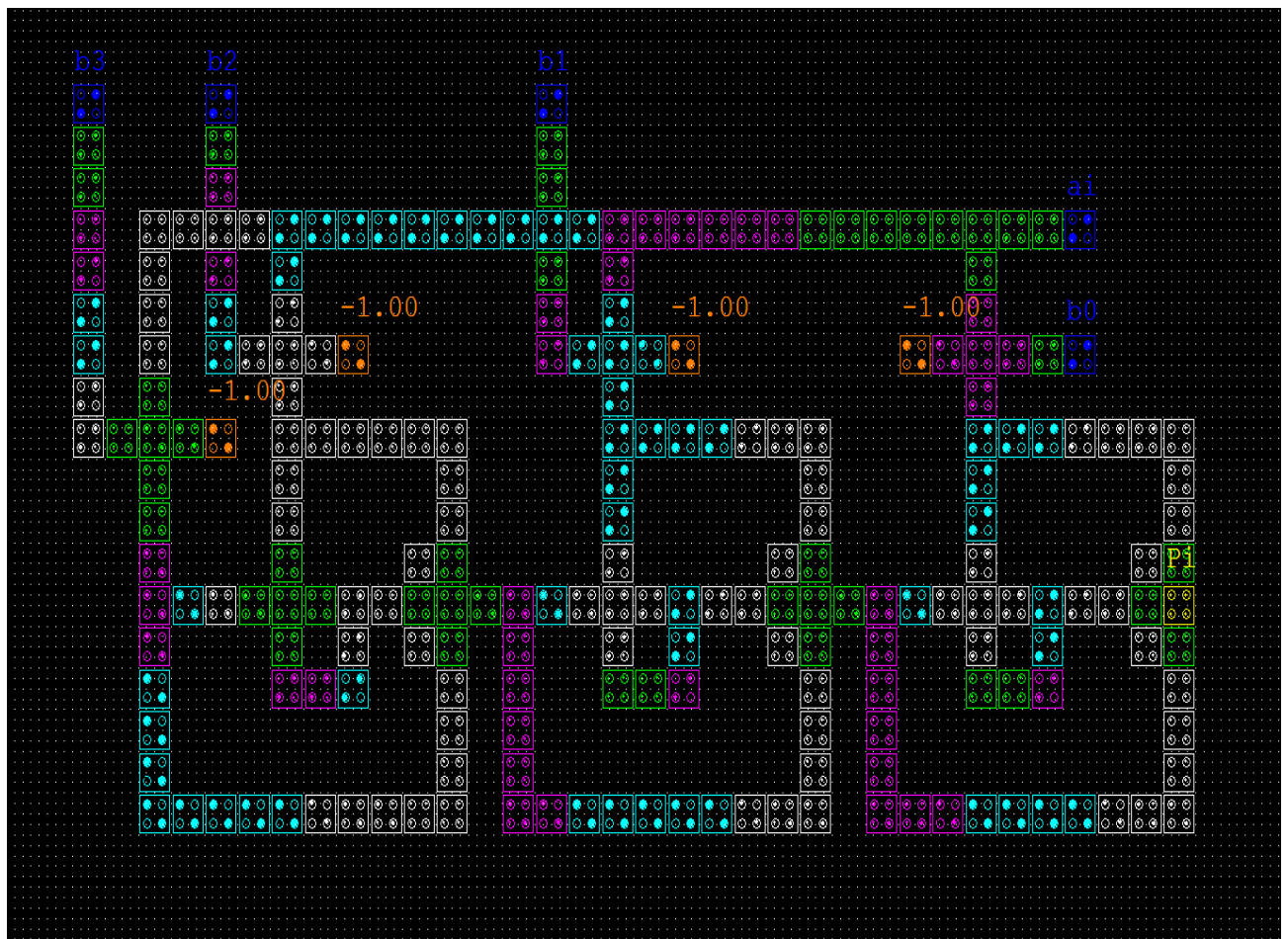


# DESIGN OF QCA-SERIAL PARALLEL MULTIPLIER (QSPM) WITH ENERGY DISSIPATION ANALYSIS

## Circuit



# Results

