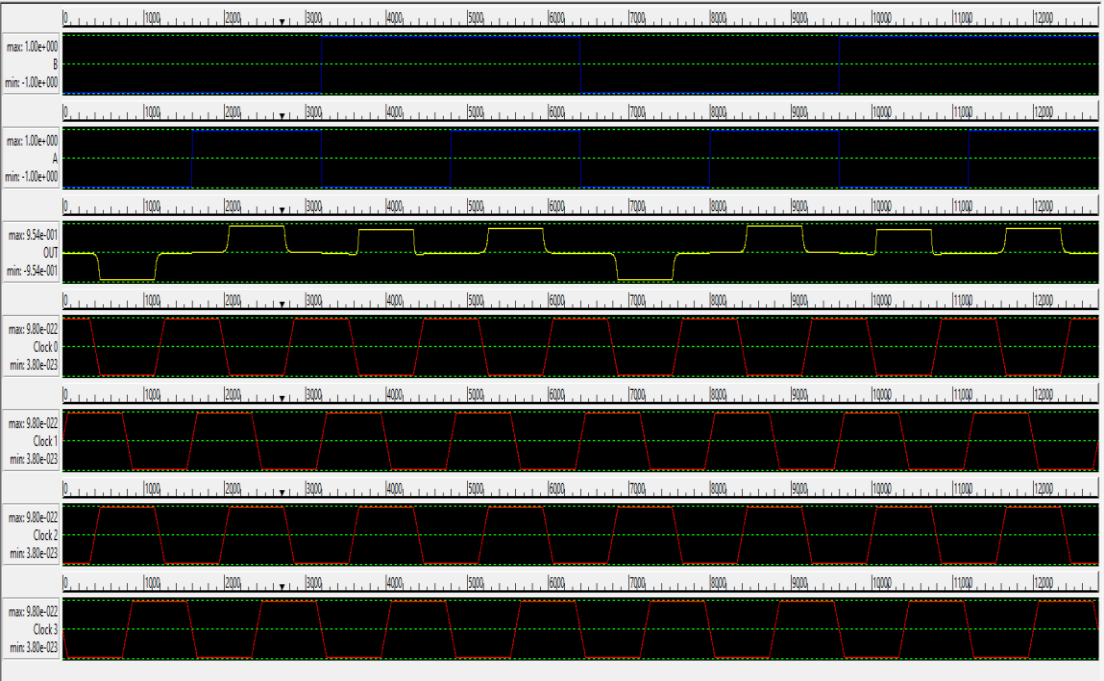
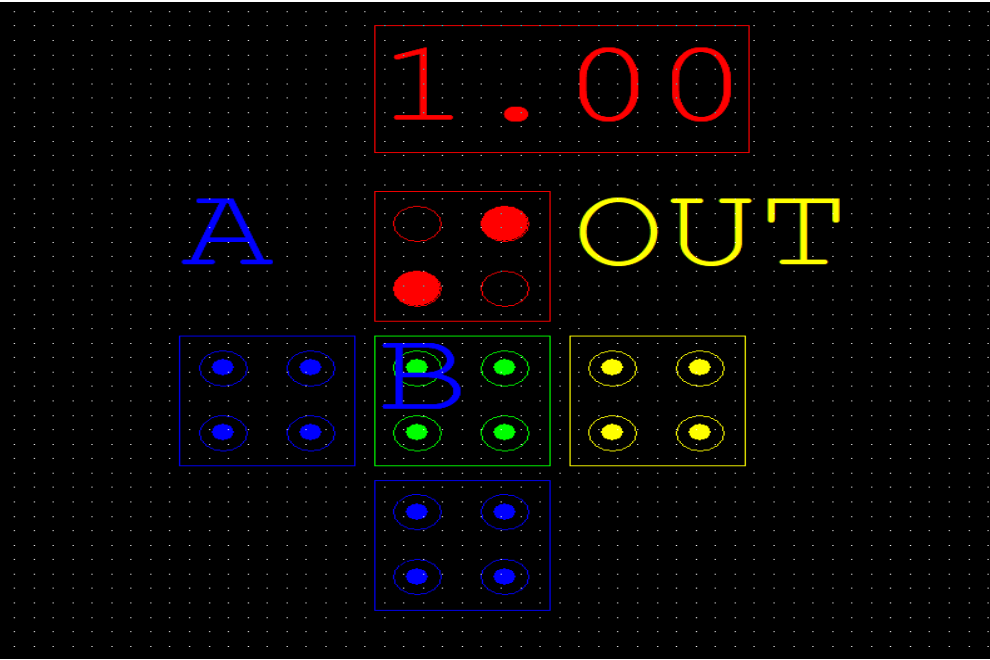


OR Gate

A	B	A+B
0	0	0
0	1	1
1	0	1
1	1	1



AND Gate

A	B	A.B
0	0	0
0	1	0
1	0	0
1	1	1

