

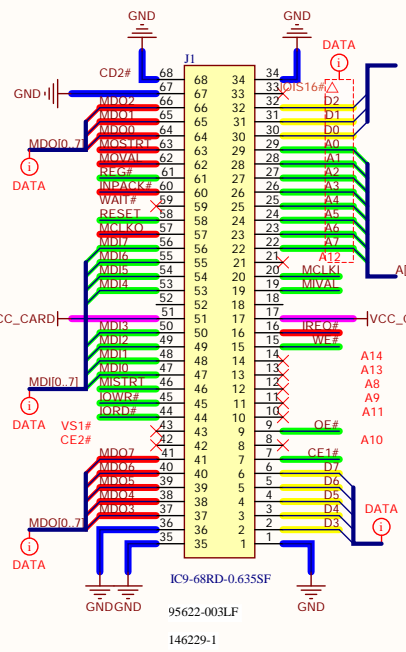
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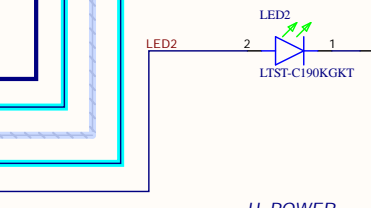
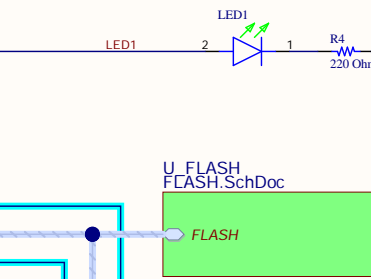
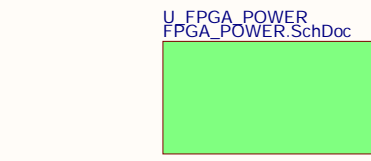
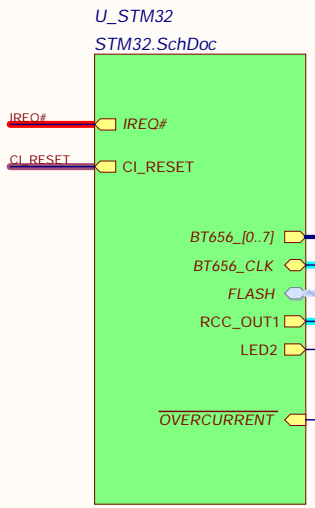
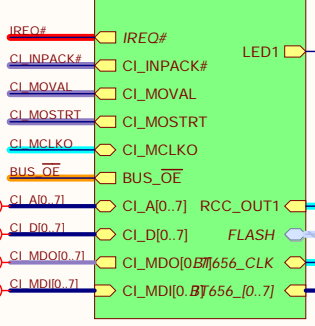
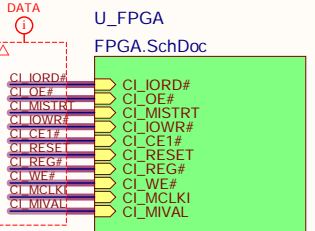
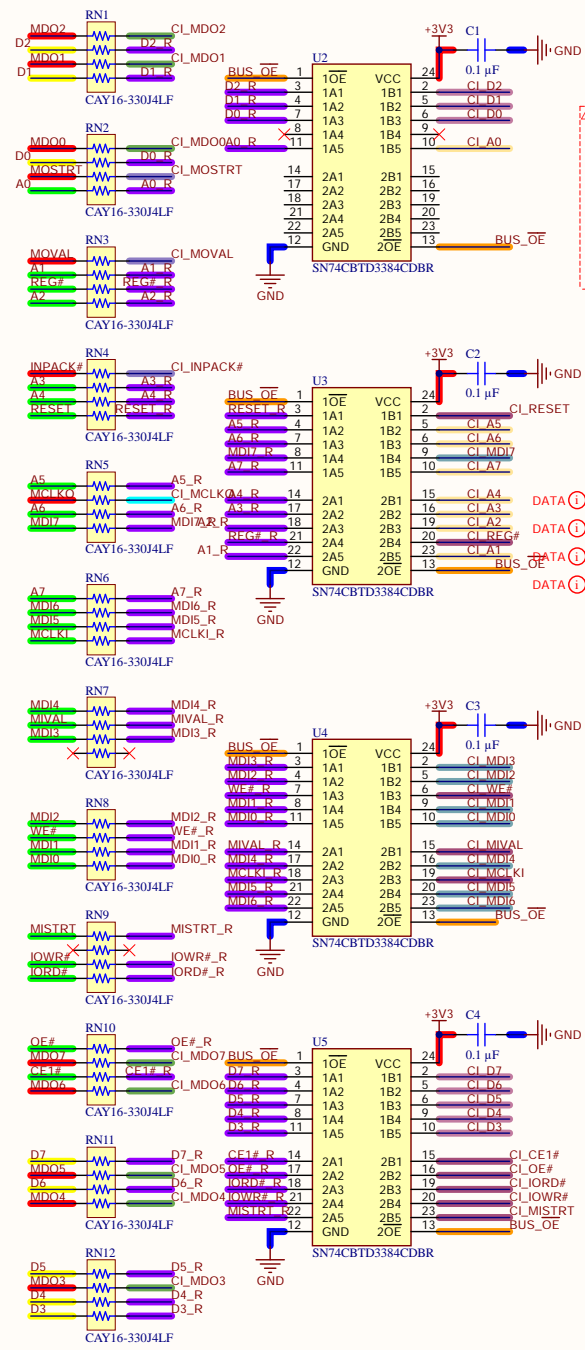
C

D

68	GND	
67	CD2#	O
66	MDO2	O
65	MDO1	O
64	MDO0	O
63	MOSTRT	I
62	MOVAL	O
61	REG#	I
60	INPACK#	O
59	WAIT#	O
58	RESET	I
57	MCLK0	O
56	MDI7	I
55	MDI6	I
54	MDI5	I
53	MDI4	I
52	VPP2	
51	VCC	
50	MDI3	I
49	MDI2	I
48	MDI1	I
47	MDI0	I
46	MISTR	I
45	IOWR#	I
44	IORD#	I
43	VS1#	O
42	CE2#	I
41	MDO7	O
40	MDO6	O
39	MDO5	O
38	MDO4	O
37	MDO3	O
36	CD1#	O
35	GND	



GND	34
way	IOIS1
I/O	D2
I/O	DI
I/O	DO
I	AO
I	AI
I	A2
I	A3
I	A4
I	A5
I	A6
I	A7
I	A12
I	MCLK
I	MIVA
tag	VPP1
O	IREQ
I	WE#
I	A14
I	A13
I	A8
I	A9
I	AI1
I	AI0
I	CE1#
I/O	D7
I/O	D6
I/O	D5
I/O	D4
I/O	D3
I/O	D2
GND	1



Title		
Size	Number	Revision
A3		
Date:	9.18.2024	Sheet of
File:	C:\Users\MAIN\SchDoc	Drawn By:

A

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1

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MAIN[6B] CI_MDO[0..7] CI_MDO[0..7]

MAIN[6B] CI_MDI[0..7] CI_MDI[0..7]

MAIN[6B] CI_A[0..7] CI_A[0..7]

MAIN[6B] CI_D[0..7] CI_D[0..7]

SPI

FLASH_CS

FLASH_SCK

FLASH_MISO

FLASH_MOSI

FLASH

MAIN[6B] BT656[0..7] BT656[0..7]

MAIN[6B] BT656_CLK BT656_CLK

MAIN[6B] BUS_OE BUS_OE

MAIN[6B] CI_MCLKO CI_MCLKO

MAIN[6B] RCC_OUT1 STM_RCC_OUT

MAIN[6B] CI_MOVA CI_MOVA

MAIN[6B] CI_MOSTRT CI_MOSTRT

MAIN[6B] CI_INPACK# CI_INPACK#

MAIN[6A] CI_MCLK CI_MCLK

MAIN[6A] CI_MIVA CI_MIVA

MAIN[6A] CI_IORD# CI_IORD#

MAIN[6A] CI_MISTRT CI_MISTRT

MAIN[6A] CI_IOWR# CI_IOWR#

MAIN[6A] CI_CE1# CI_CE1#

MAIN[6A] CI_RESET CI_RESET

MAIN[6A] CI_REG# CI_REG#

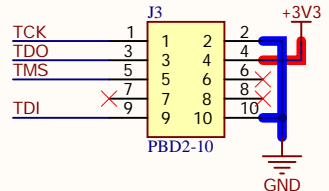
MAIN[6A] CI_WE# CI_WE#

MAIN[6A] CI_OE# CI_OE#

MAIN[6B] IREQ# IREQ#

MAIN[6B] LED1 LED1

JTAG MODE



EPCS_ASDI 6

CI_MDO0 7

EPCS_nCS 8

CI_MDO1 10

CI_MDO2 11

EPCS_DATA03

CI_MOSTRT 28

CI_D2 30

CI_D1 31

CI_D0 32

CI_A0 33

CI_A1 39

CI_A2 42

CI_A3 44

CI_A4 46

CI_A5 49

CI_A6 50

CI_A7 51

CI_MCLKI

CI_WE#

CI_RESET

CI_MDI[0..7]

CI_MDI7 64

CI_MDI6 65

CI_MDI5 66

CI_MDI4 67

CI_MDI3 68

CI_MDI2 69

CI_MDI1 71

TDO 20

TMS 18

TCK 16

TDI 15

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TDO

TMS

TCK

TDI

NSTATUS

DCLK

NCONFIG

NCE

CONF_DONE

MSEL2

MSEL1

MSEL0

EP4CE22E22C6N

IO_30

IO_31

IO_32

IO_33

IO_34

IO_35

IO_36

IO_37

IO_38

IO_39

IO_40

IO_41

IO_42

IO_43

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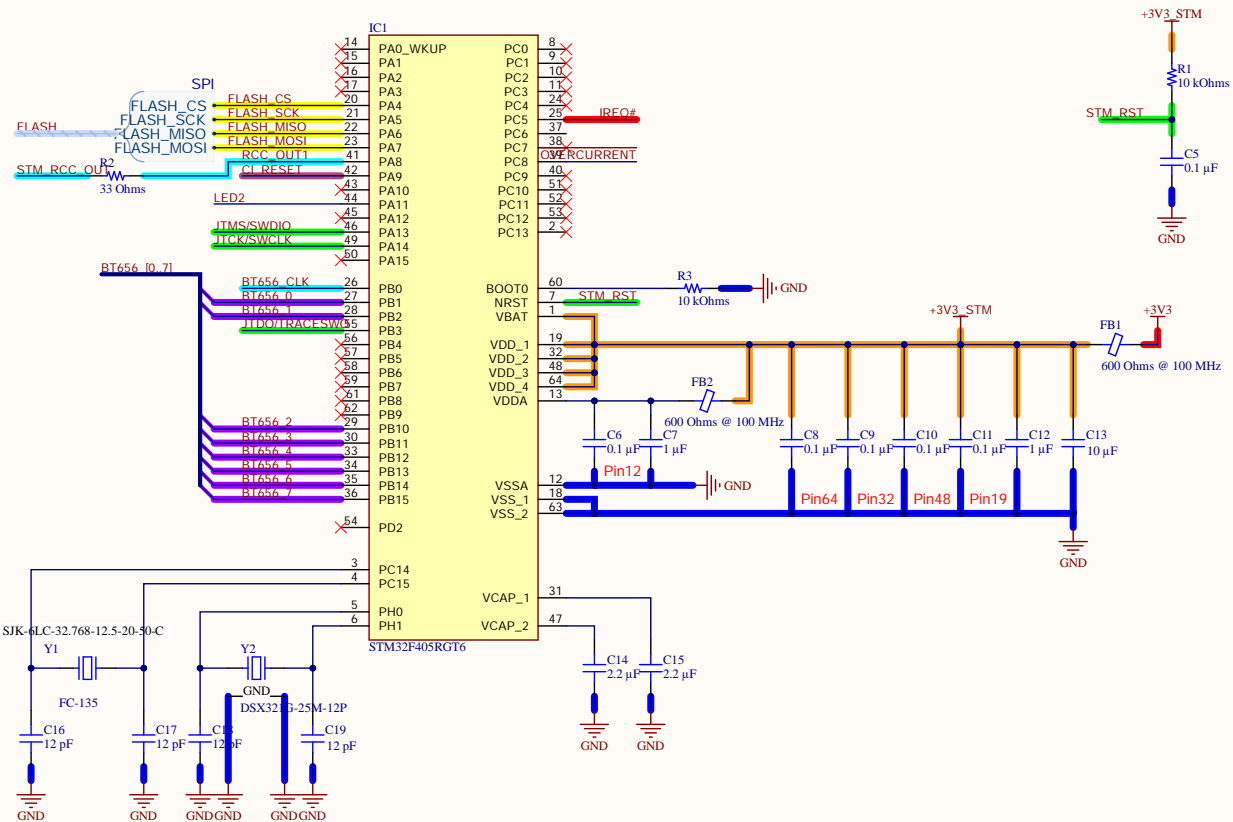
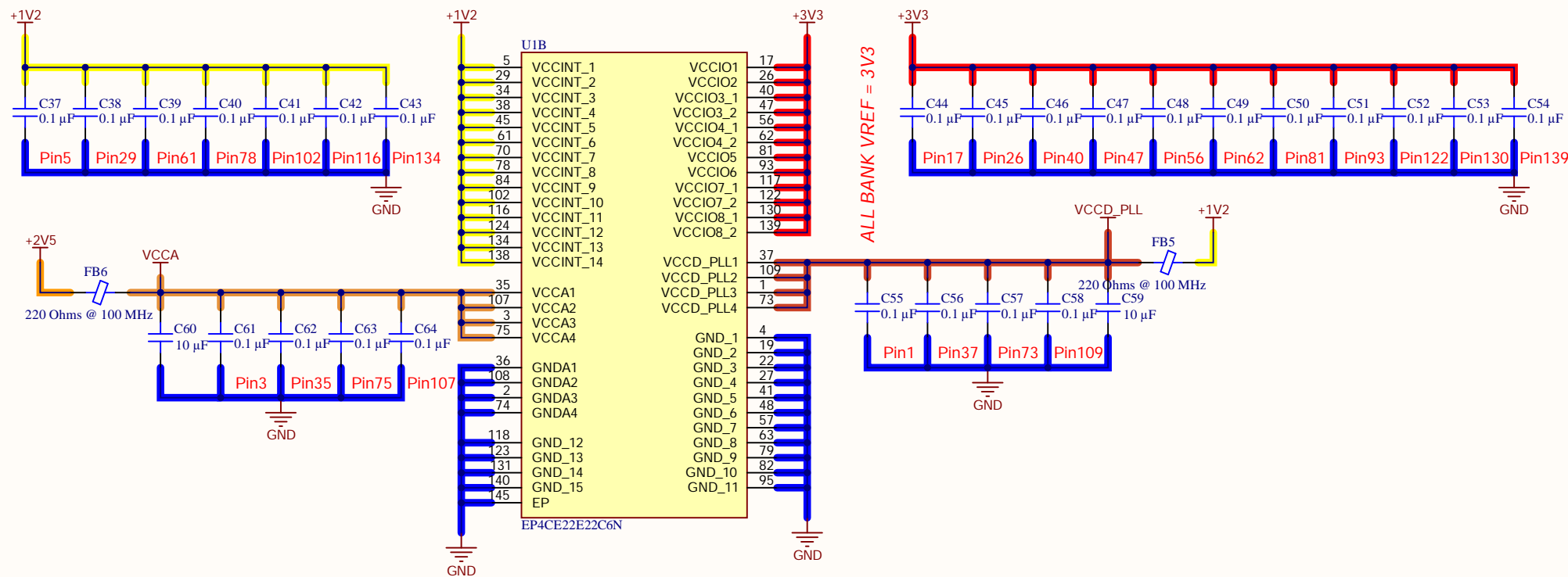


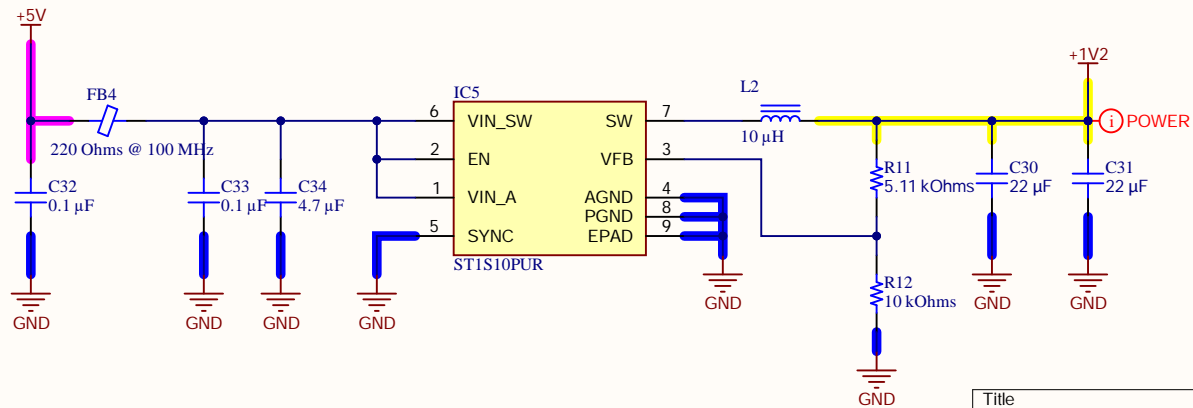
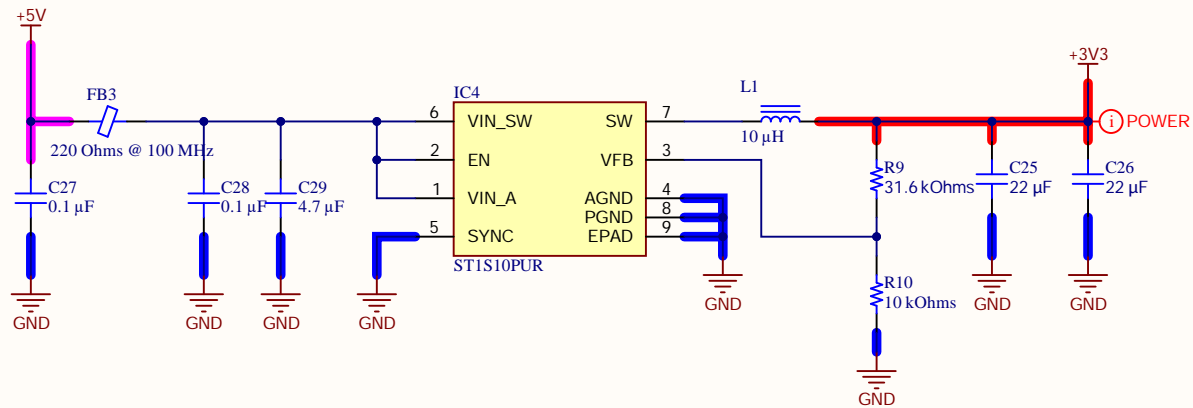
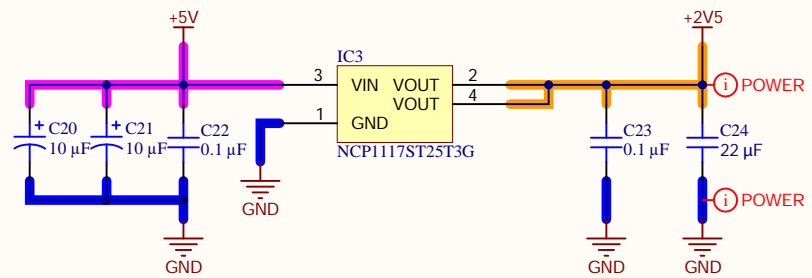
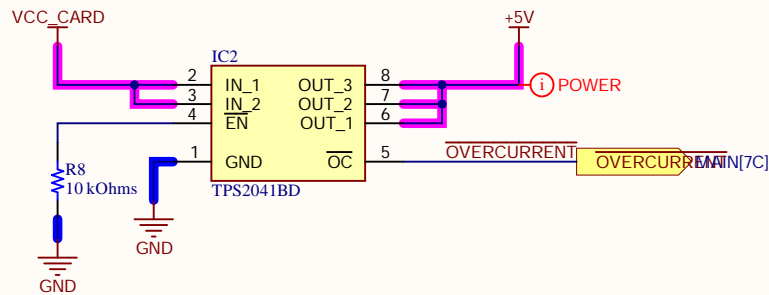
Diagram illustrating the pinout for the J2 header (STM32F103C8T6):

ЦЕПЬ	КОНТ	Сигнал
	1	+3V3_STM
	2	JTCK/SWCLK
	3	JTMS/SWDIO
	4	JTMS/SWDIO
	5	STM_RST
	6	JTDOTRACESWO

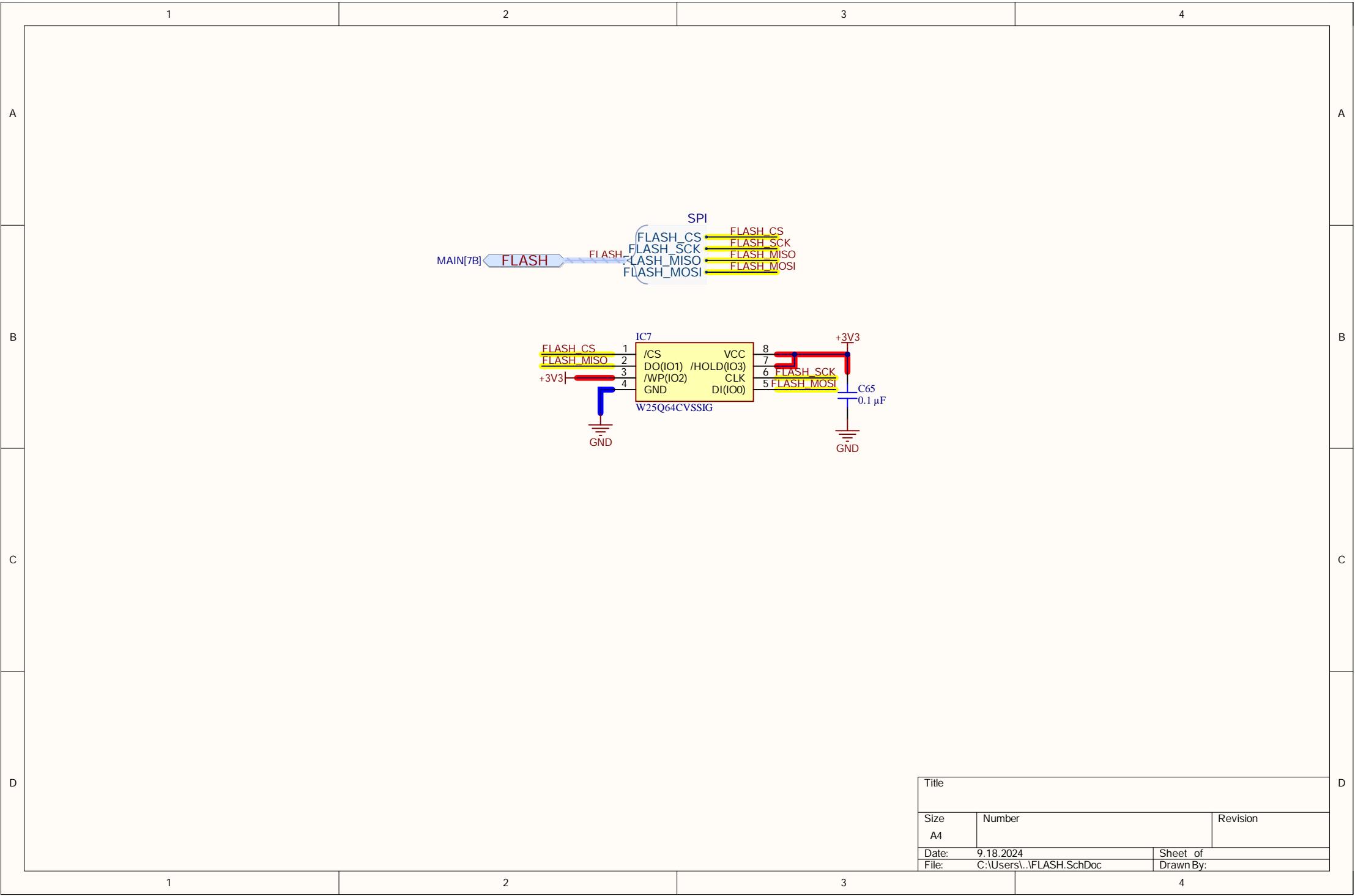
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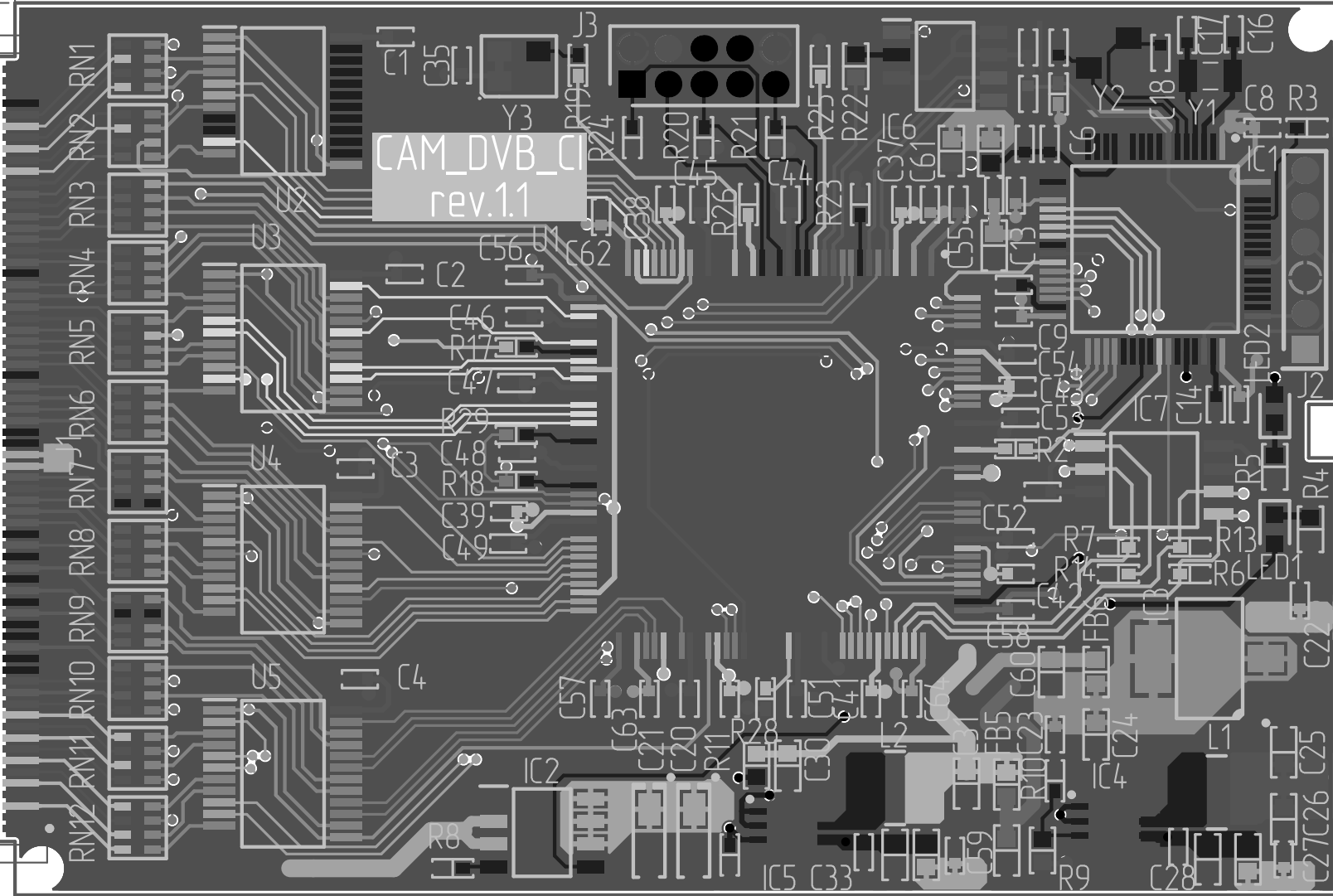
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CAM_DVB_CI
rev.1.1



Comment	Description	Designator	Footprint	LibRef	Quantity
0.1 µF	CAP CER 0.1UF 16V X7R 0402	C1, C2, C3, C4, C5, C6, C8, C9, C10, C11, C22, C23, C27, C28, C32, C33, C35, C36, C37, C38, C39, C40, C41, C42, C43, C44, C45, C46, C47, C48, C49, C50, C51, C52, C53, C54, C55, C56, C57, C58, C61, C62, C63, C64, C65	CAP 0402_1005	CC0402KRX7R7BB104	45
1 µF	CAP CER 1UF 10V X5R 0402	C7, C12	CAP 0402_1005	CC0402KRX5R6BB105	2
CC0603KRX5R6BB106	CAP CER 10UF 10V X5R 0603	C13, C59, C60	CAP 0603_1608	CC0603KRX5R6BB106	3
2.2 µF	CAP CER 2.2UF 10V X5R 0402	C14, C15	CAP 0402_1005	CC0402KRX5R6BB225	2
12 pF	CAP CER 12PF 50V C0G/NPO 0402	C16, C17, C18, C19	CAP 0402_1005	CC0402FRNPO9BN12 0	4
10 µF	CAP TANT 10UF 10% 16V 1206	C20, C21	CAP POL 1206_3216	T494A106K016AT	2
22 µF	CAP CER 22UF 6.3V X5R 0603	C24, C25, C26, C30, C31	CAP 0603_1608	CC0603MRX5R5BB22 6	5
4.7 µF	CAP CER 4.7UF 10V X5R 0603	C29, C34	CAP 0603_1608	CC0603KRX5R6BB475	2
600 Ohms @ 100 MHz	FERRITE BEAD 600 OHM 0603 1LN	FB1, FB2	FER 0603_1608	BLM18KG601SN1D	2
220 Ohms @ 100 MHz	FERRITE BEAD 220 OHM 0603 1LN	FB3, FB4, FB5, FB6	FER 0603_1608	BLM18EG221SN1D	4
STM32F405RGT6	Integrated Circuit	IC1	QFP50P1200X1200X16 0-64N	STM32F405RGT6	1
TPS2041BD	Integrated Circuit	IC2	SOIC127P600X175-8N	TPS2041BD	1
NCP1117ST25T3G	IC REG LINEAR 2.5V 1A SOT223	IC3	ON SEMI SOT-223 ST(318H)	NCP1117ST25T3G	1
ST1S10PUR	Integrated Circuit	IC4, IC5	SON80P400X400X100- 9N-D	ST1S10PUR	2
EPCS16S18N	Integrated Circuit	IC6	SOIC127P600X175-8N	EPCS16S18N	1
W25Q64CVSSIG	Integrated Circuit	IC7	SOIC127P790X216-8N	W25Q64CVSSIG	1
IC9-68RD-0.635SF	Connector Card Bus Socket	J1	IC9-68RD-0.635SF	IC9-68RD-0.635SF	1
PBS2-6	Connector, Header, Pitch 2mm, H=4.3mm	J2	PBS2-6	PBS2-6	1
PBD2-10	Connector	J3	PBD2-10	PBD2-10	1
10 µH	FIXED IND 10UH 1.3A 180 MOHM SMD	L1, L2	BOURNS SRN4018	SRN4018-100M	2
	LED, 0603, GREEN	LED1	LEDC1608X80N	LTST-C190KGKT	1
LTST-C190KGKT	LED, 0603, GREEN	LED2	LEDC1608X80N	LTST-C190KGKT	1
10 kOhms	RES 10K OHM 1% 1/16W 0402	R1, R3, R6, R7, R8, R10, R12, R13, R14, R20, R21, R23, R25, R26, R28	RES 0402_1005	RC0402FR-0710KL	15
33 Ohms	RES 33 OHM 1% 1/16W 0402	R2, R19	RES 0402_1005	RC0402FR-0733RL	2
220 Ohms	RES 220 OHM 1% 1/10W 0603	R4, R5	RES 0603_1608	RC0603FR-07220RL	2
31.6 kOhms	RES 31.6K OHM 1% 1/10W 0603	R9	RES 0603_1608	RC0603FR-0731K6L	1
5.11 kOhms	RES 5.11K OHM 1% 1/10W 0603	R11	RES 0603_1608	RC0603FR-075K11L	1
0 Ohms	RES 0 OHM JUMPER 1/16W 0402	R17, R18, R29	RES 0402_1005	RC0402JR-070RL	3
33 Ohms	RES 33 OHM 1% 1/10W 0603	R22	RES 0603_1608	RC0603FR-0733RL	1
1 kOhms	RES 1K OHM 1% 1/16W 0402	R24	RES 0402_1005	RC0402FR-071KL	1
CAY16-330J4LF	RES ARRAY 4 RES 33 OHM 1206	RN1, RN2, RN3, RN4, RN5, RN6, RN7, RN8, RN9, RN10, RN11, RN12	RESNET 1206-8 CONVEX	CAY16-330J4LF	12
EP4CE22E22C6N	IC FPGA 79 I/O 144EQFP	U1	QFP50P2200X2200X16 5-145N	EP4CE22E22C6N_1	1
SN74CBTD3384CDBR	Integrated Circuit	U2, U3, U4, U5	SOP65P780X200-24N	SN74CBTD3384CDBR	4
FC-135	Crystal or Oscillator	Y1	FC135	FC-135_32.7680KA-A3	1
DSX321G-25M-12P	Crystal or Oscillator	Y2	DSX321G20M	DSX321G-25M	1
27 MHz	XTAL OSC XO 27.0000MHZ CMOS SMD	Y3	ABRACON ASE	ASE-27.000MHZ-ET	1