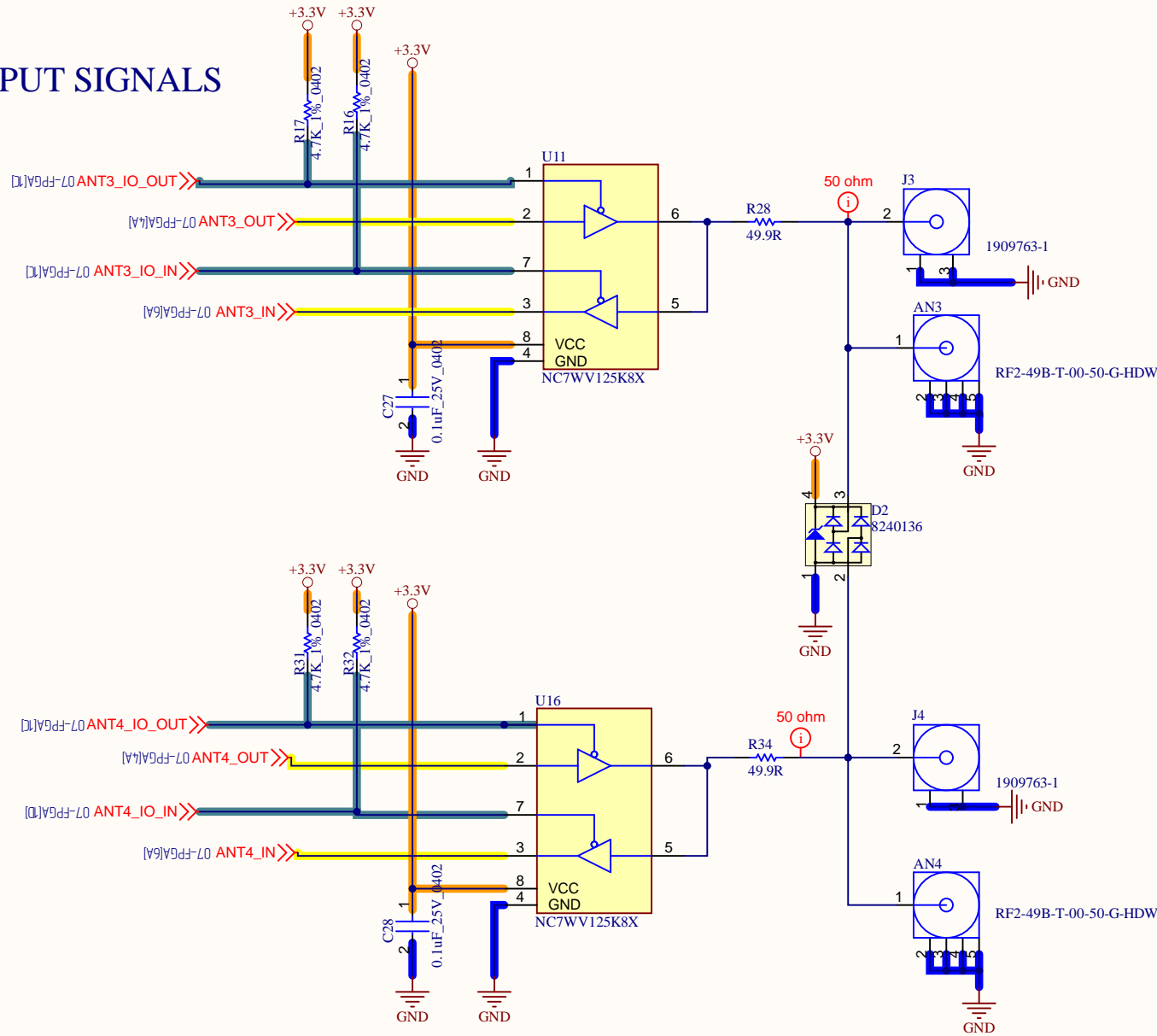
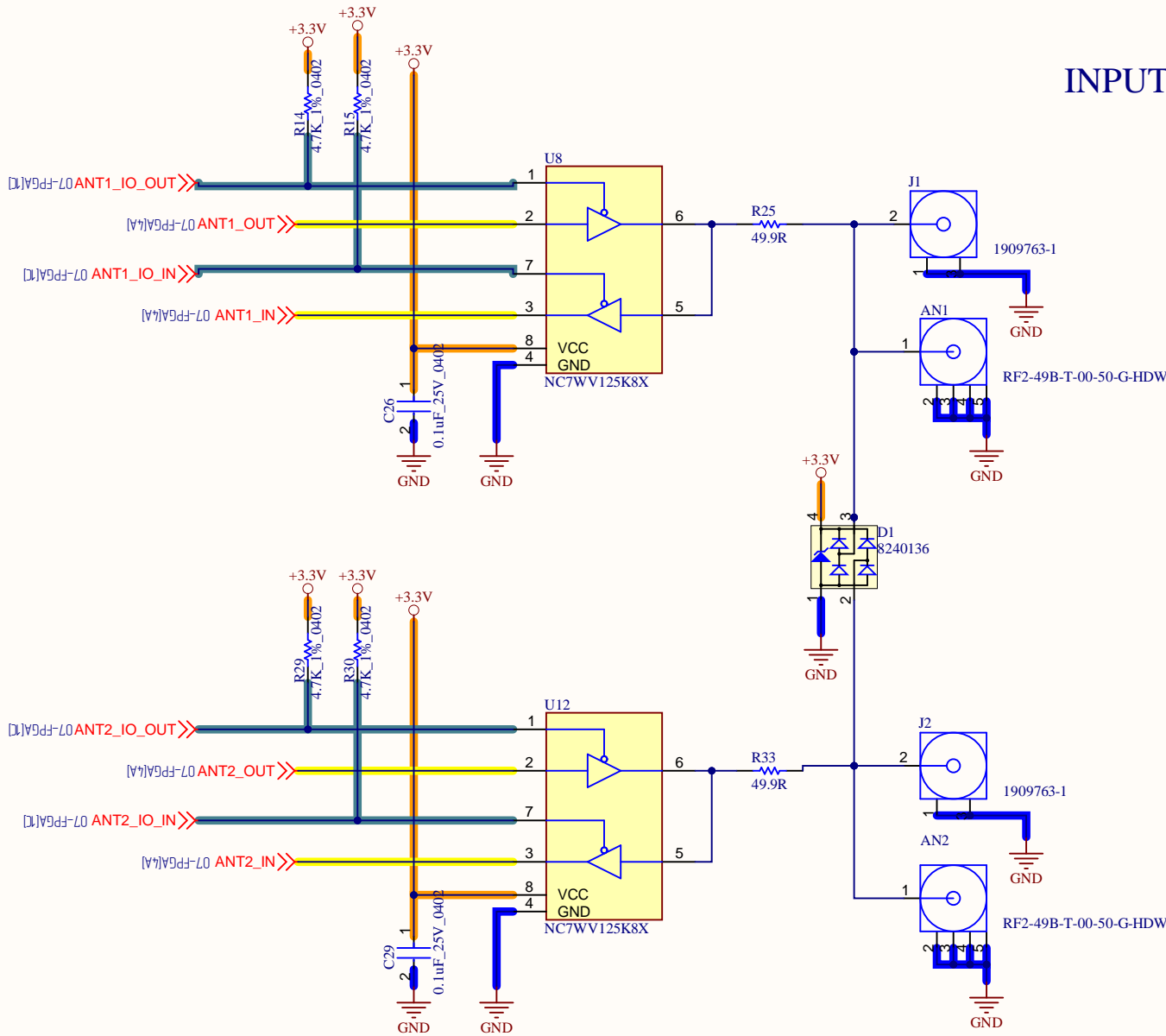
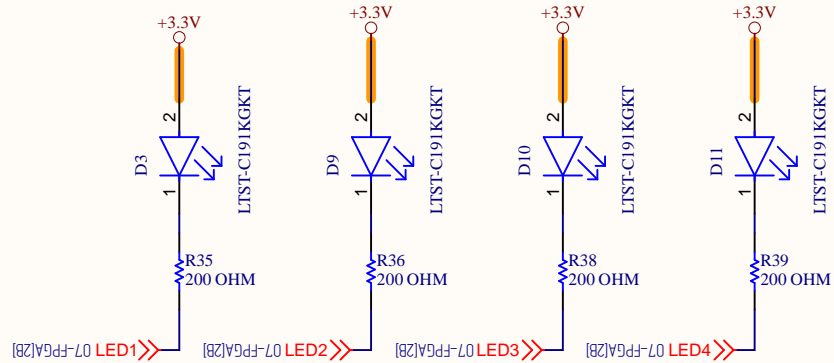


INPUT/OUTPUT SIGNALS

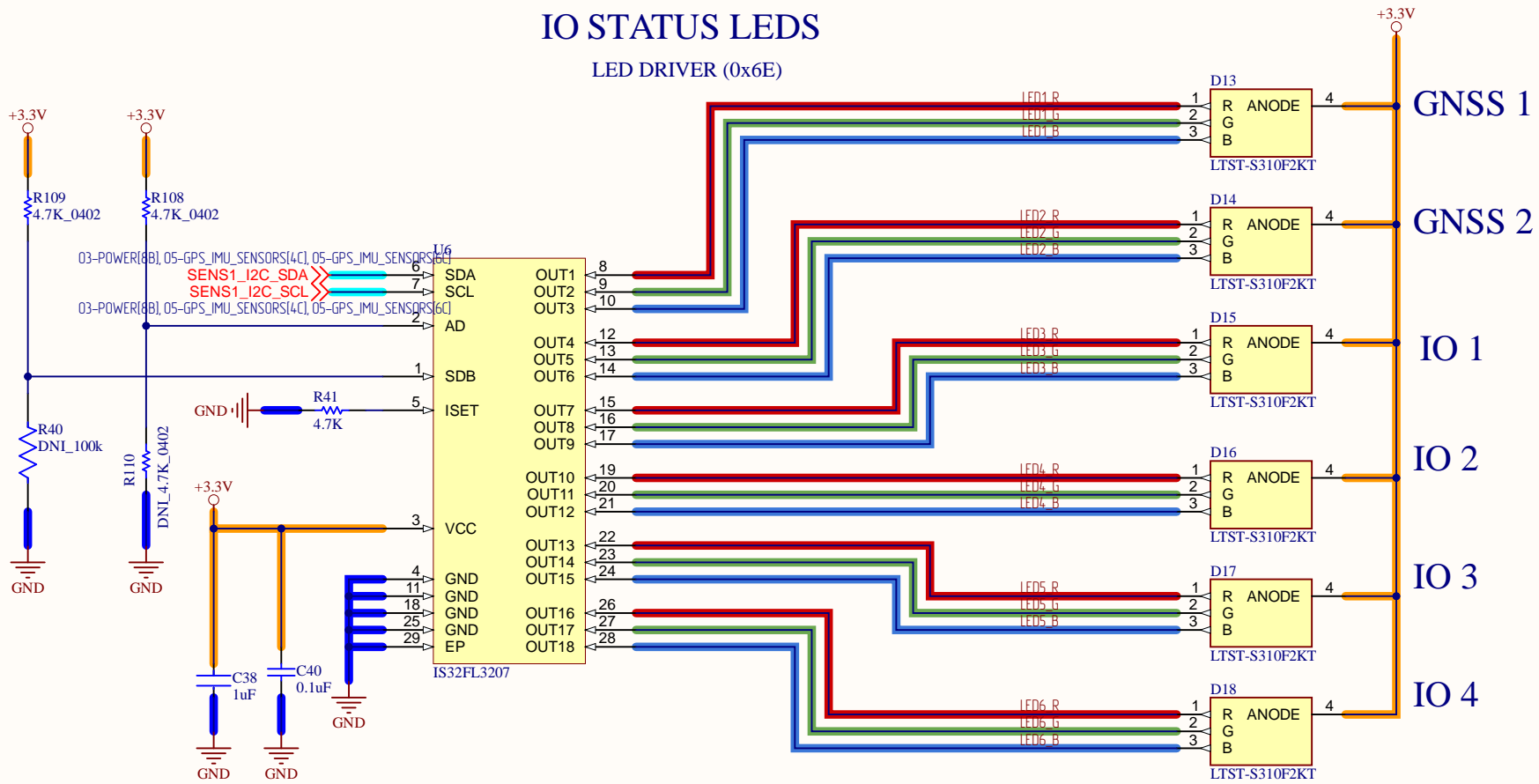


## SYSTEM STATUS LEDS

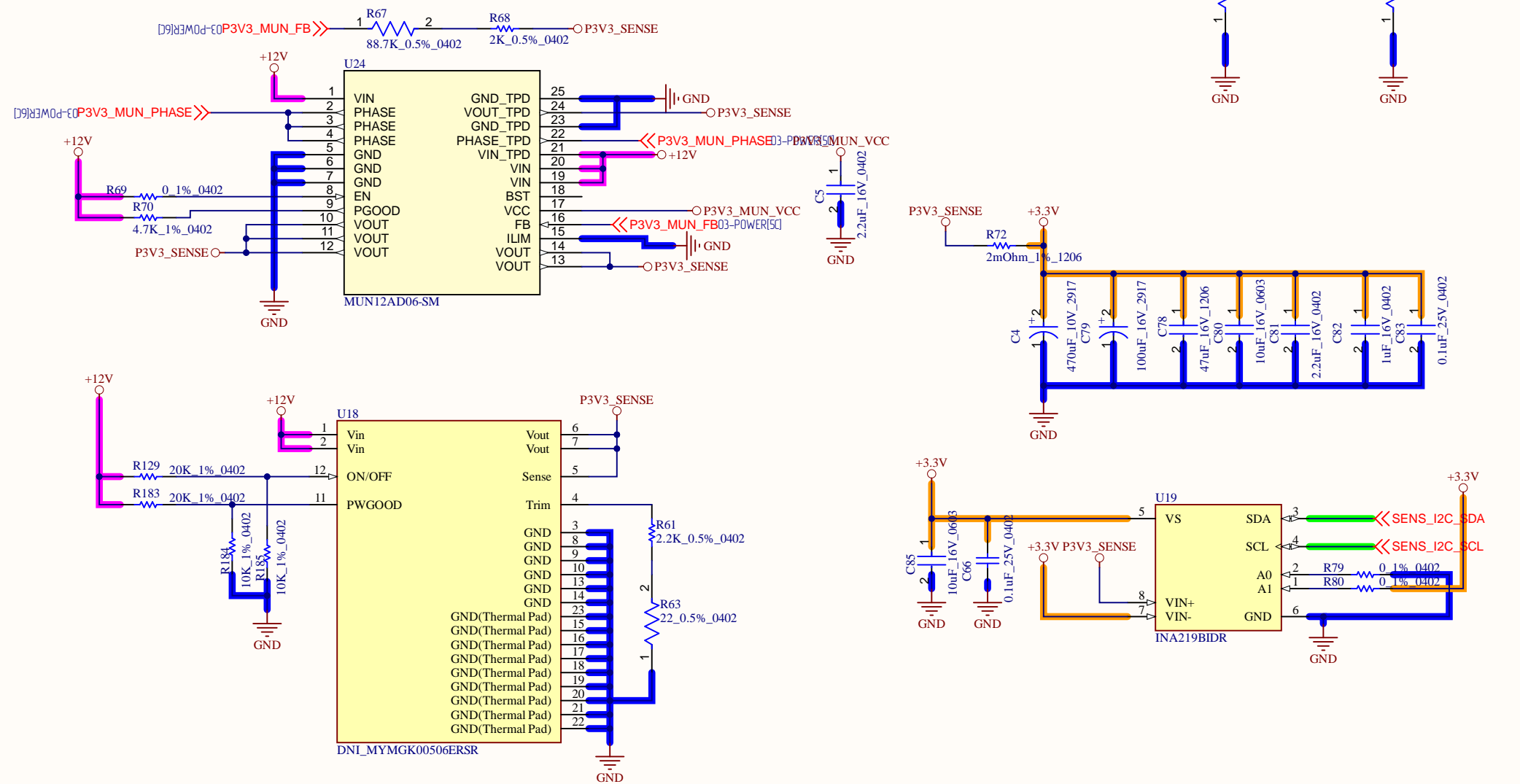
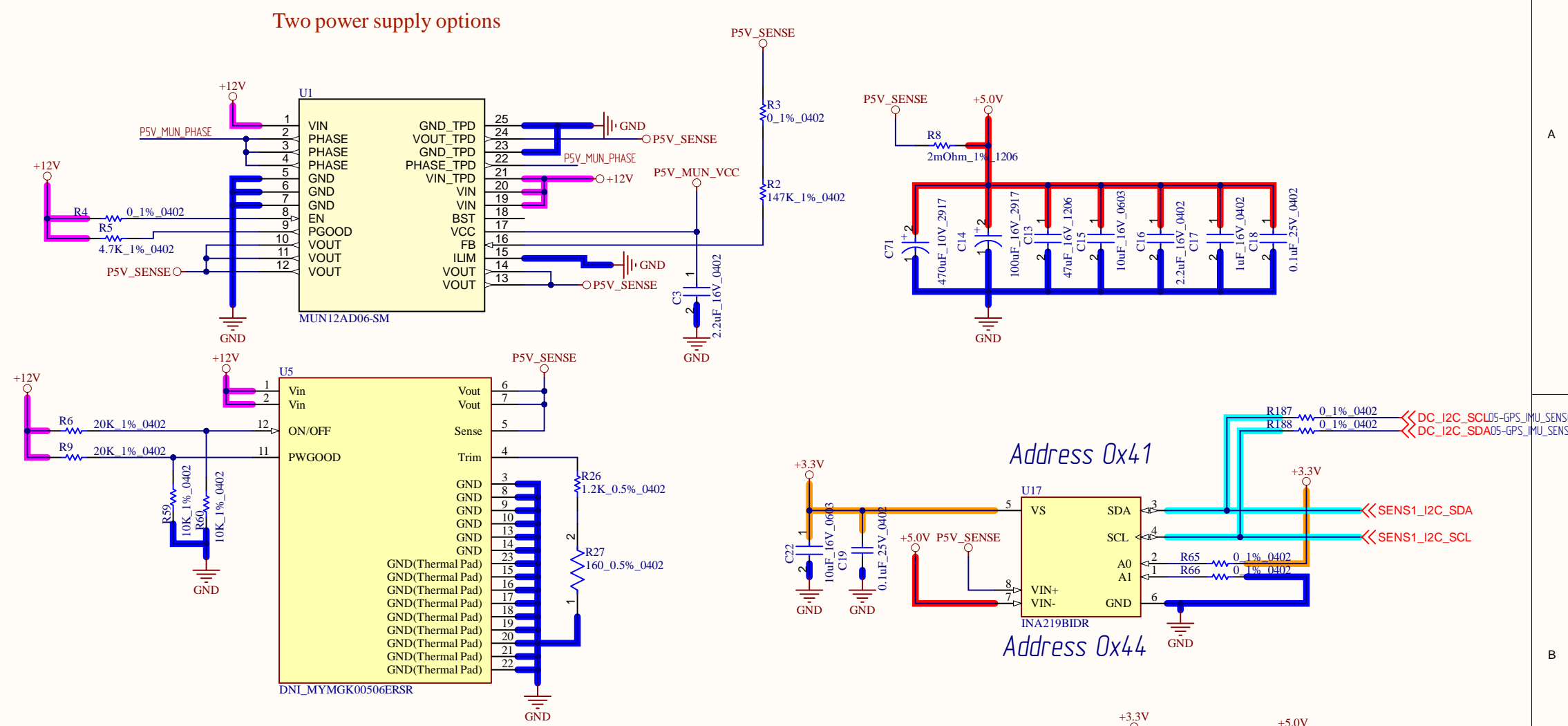
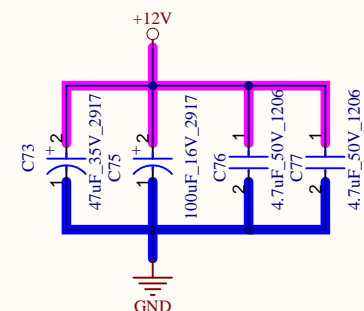
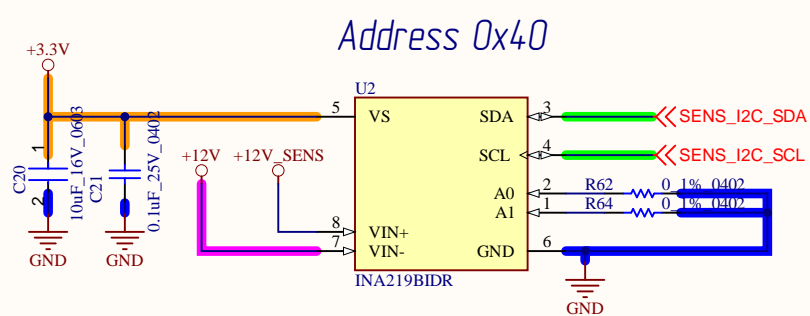
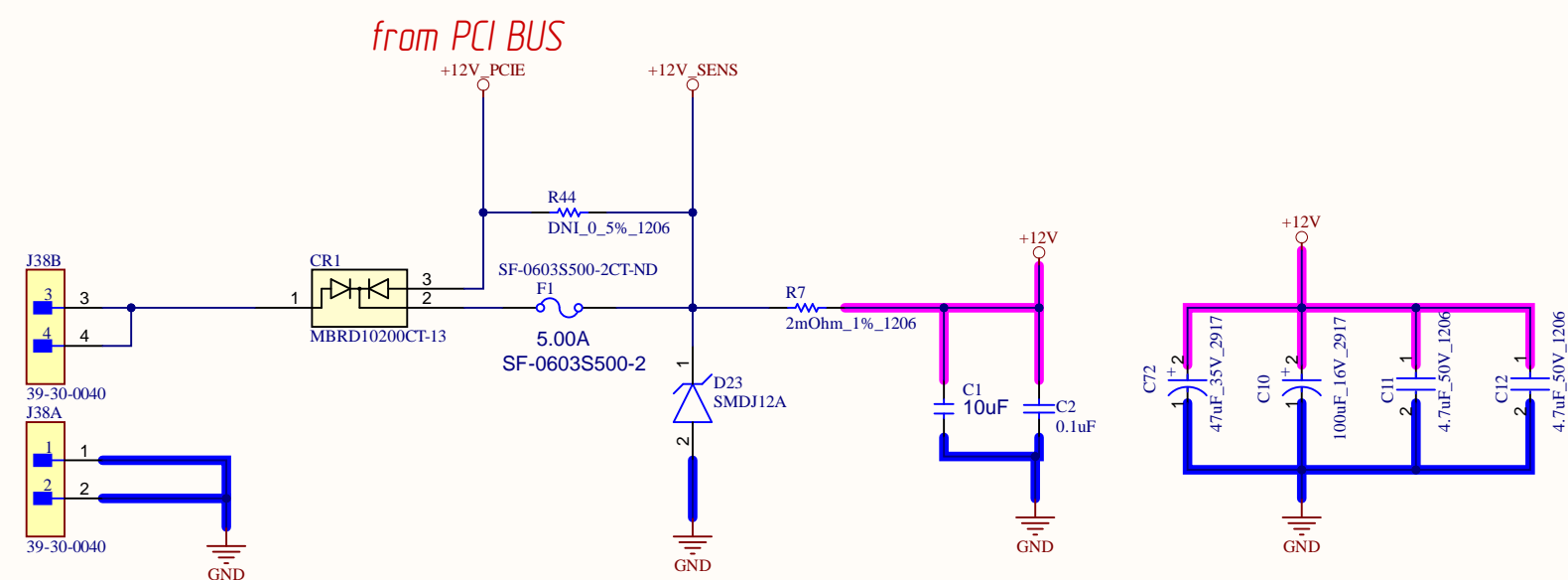


## IO STATUS LEDS

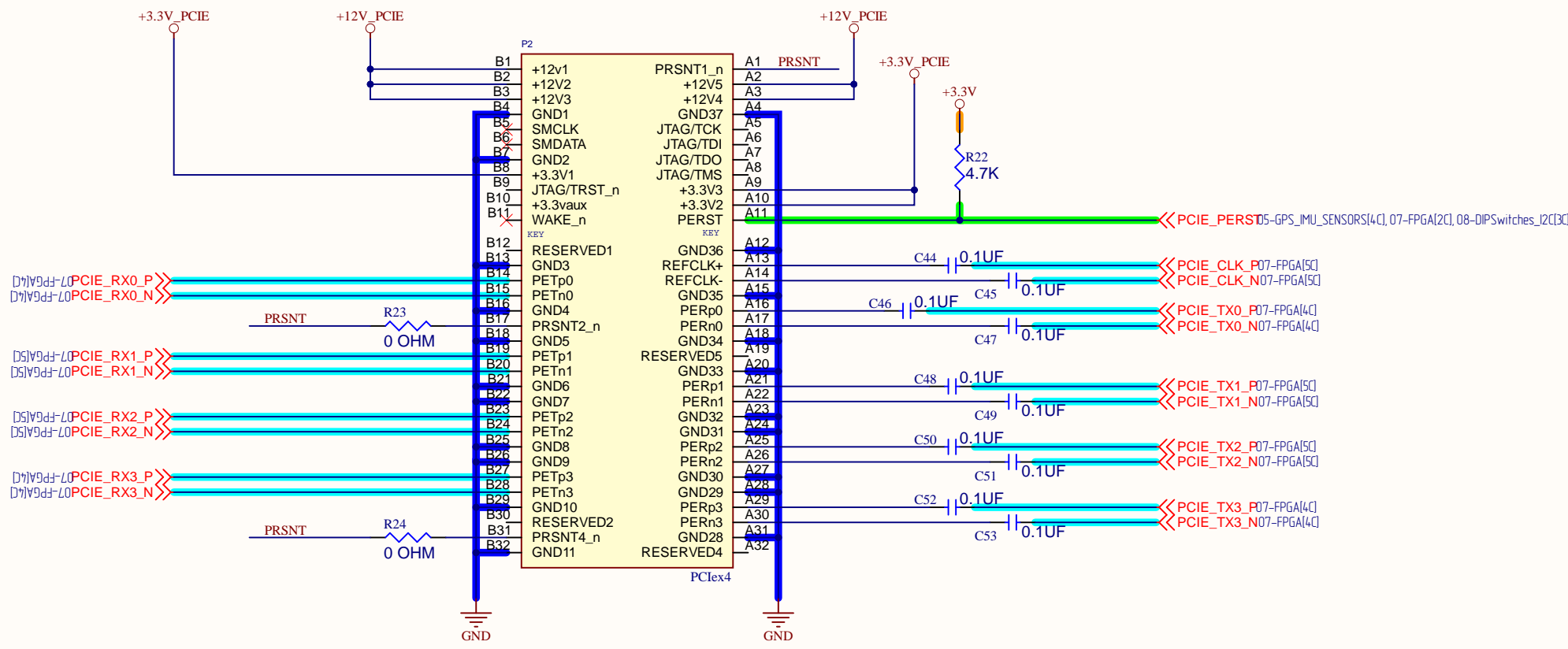
LED DRIVER (0x6E)



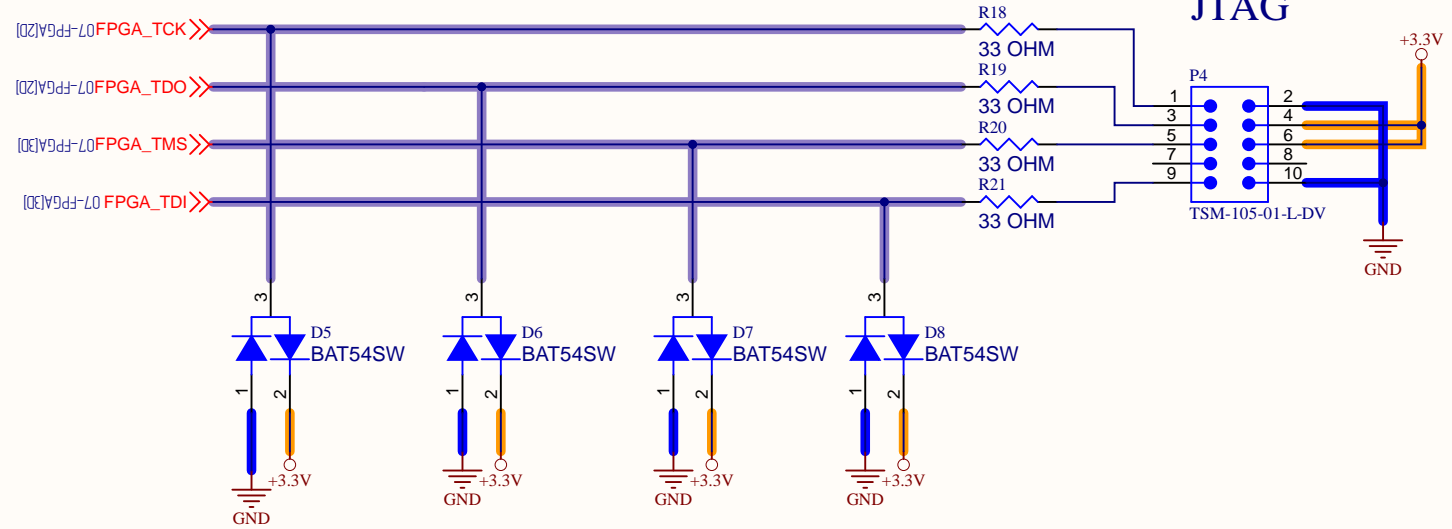
Title		
Size	Number	Revision
A3		
Date:	4.13.2025	Sheet of
File:	C:\Documents\..02-USER_IO_STATUS	1



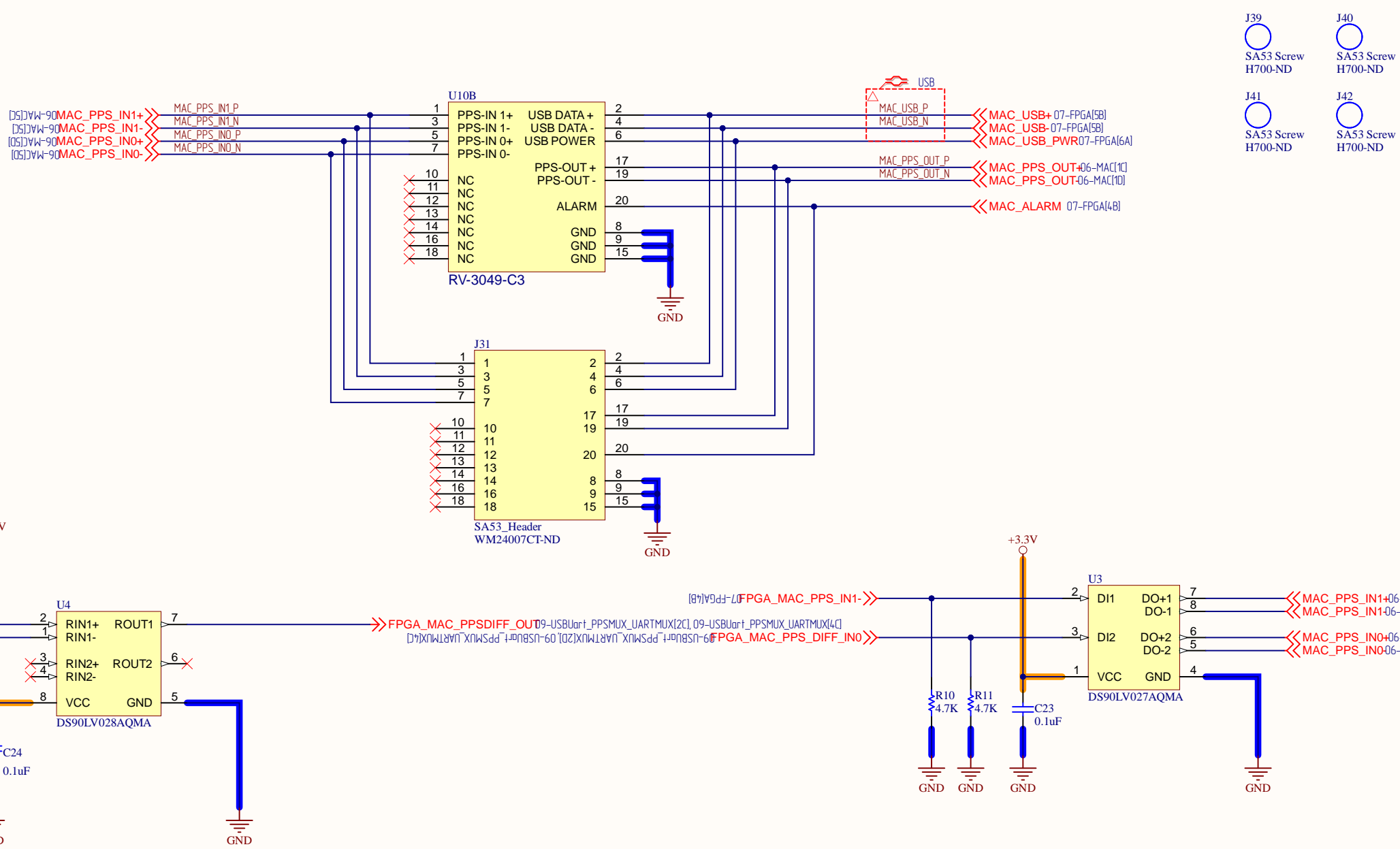
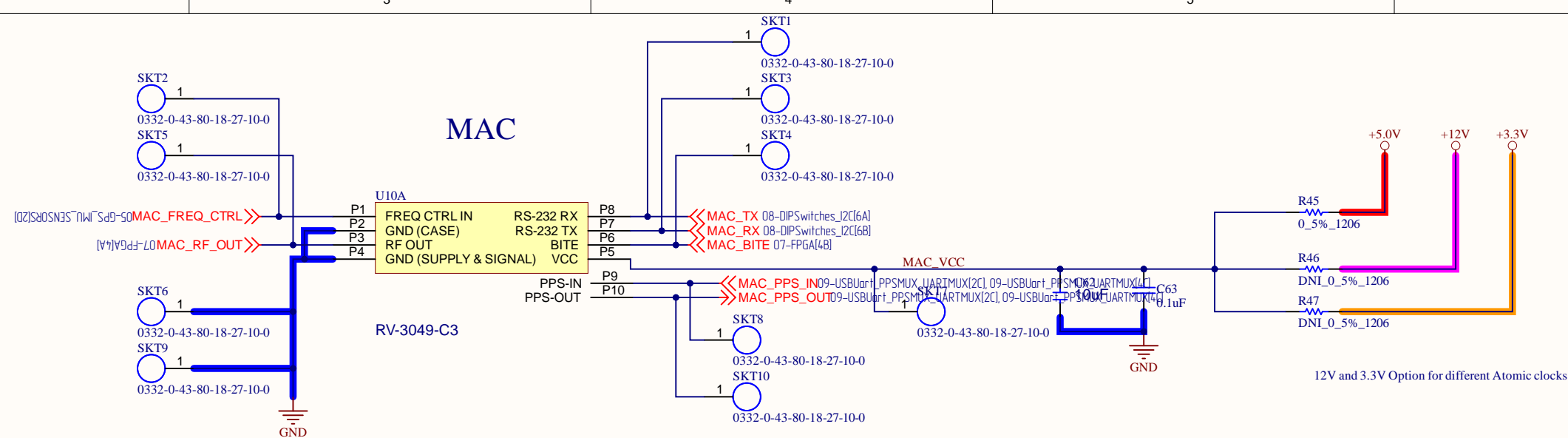
PCIe Connector



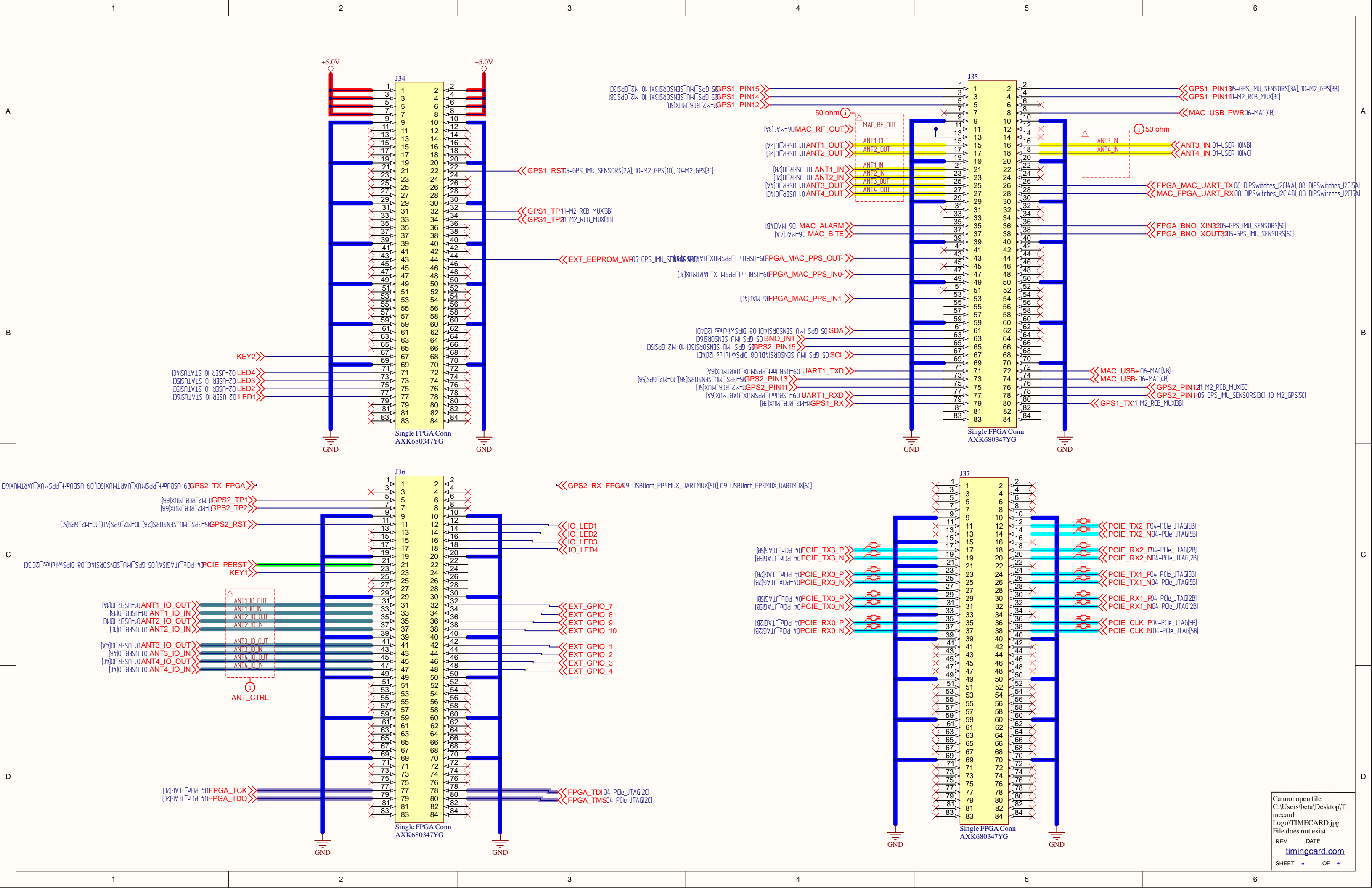
JTAG





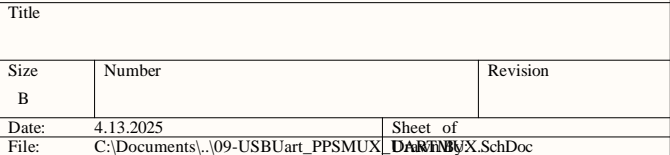




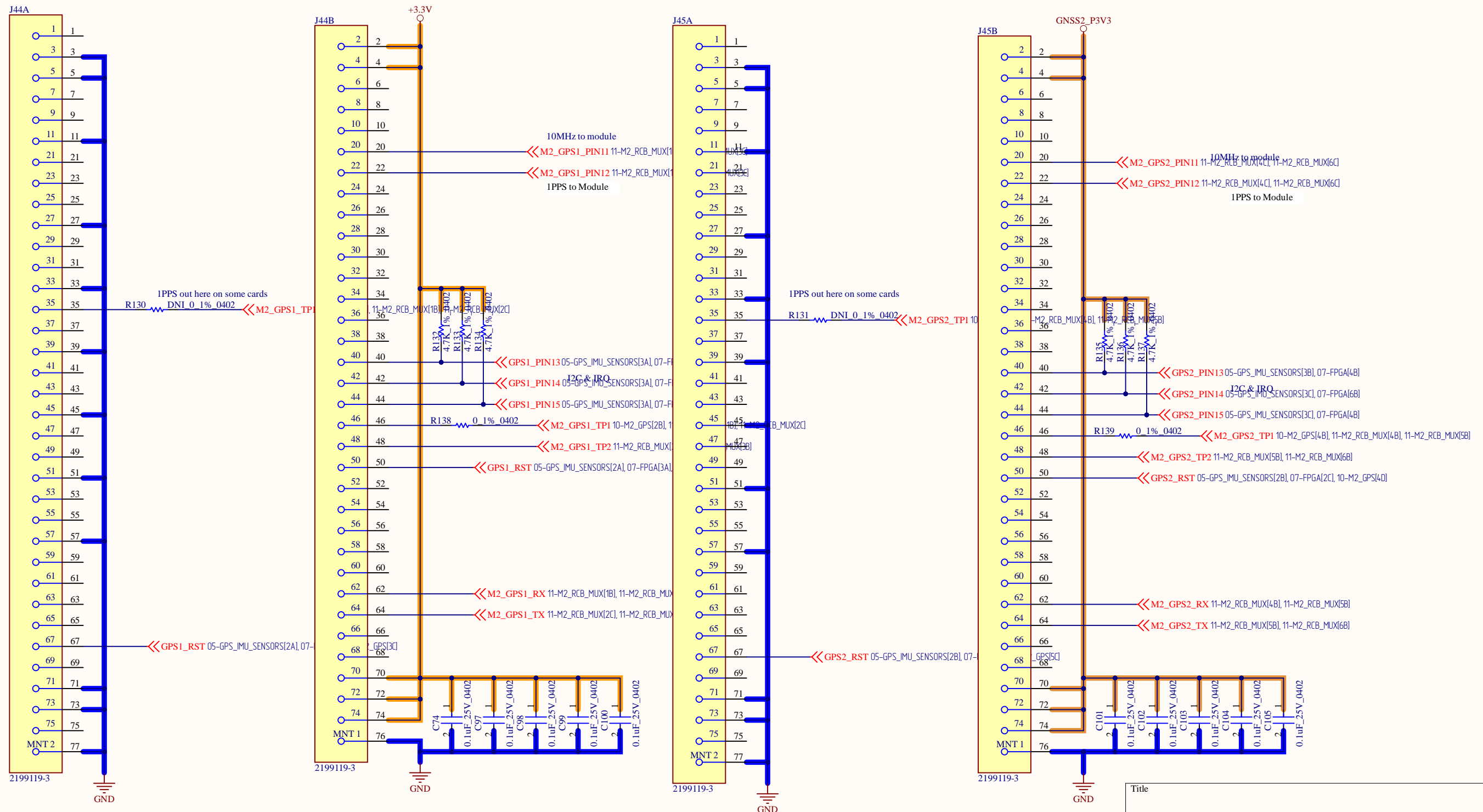








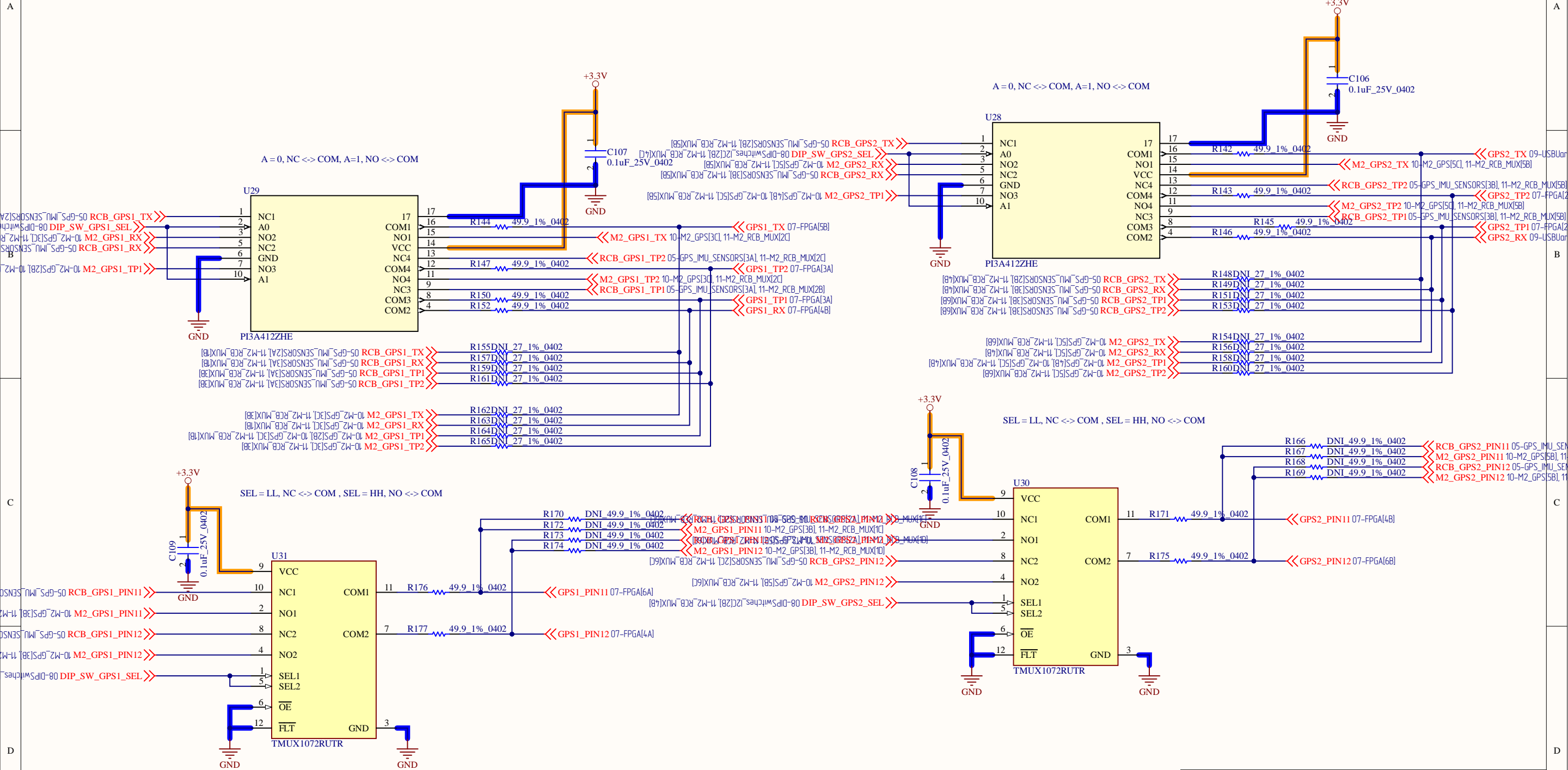
Socket 2 Key B PCIe-based WWAN Adapter Pinout , table 3-21 in M.2 spec



Title		
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GNSS modules need to MUX 6 signals, use 1x4 channel MUX and 1x2 channel MUX

RST is just tied to both, and I2C and IRQ is tied to both



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B		
Date:	4.13.2025	Sheet of
File:	C:\Documents\11-M2_RCB_MUX.Sch	Drawn By: