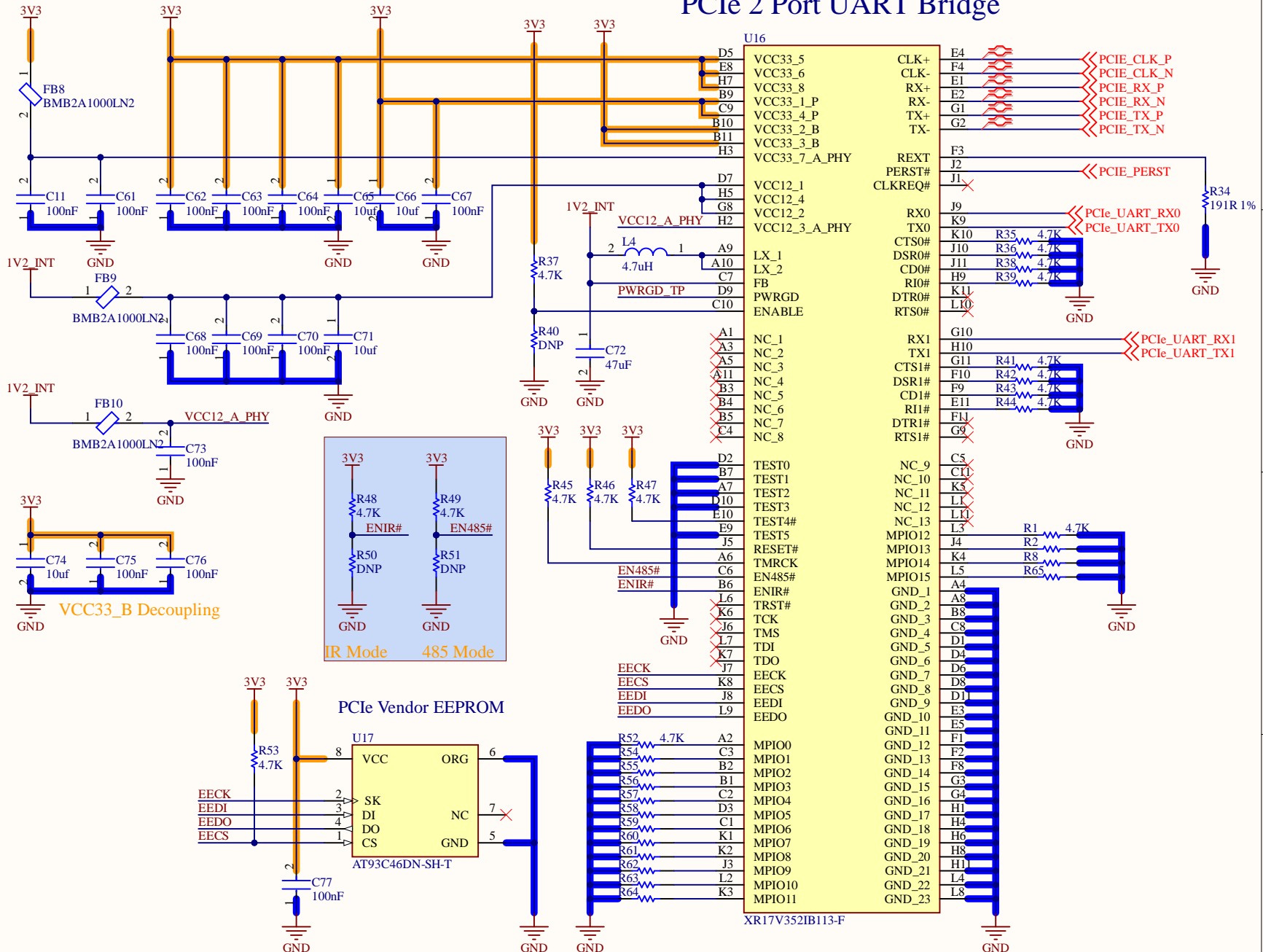
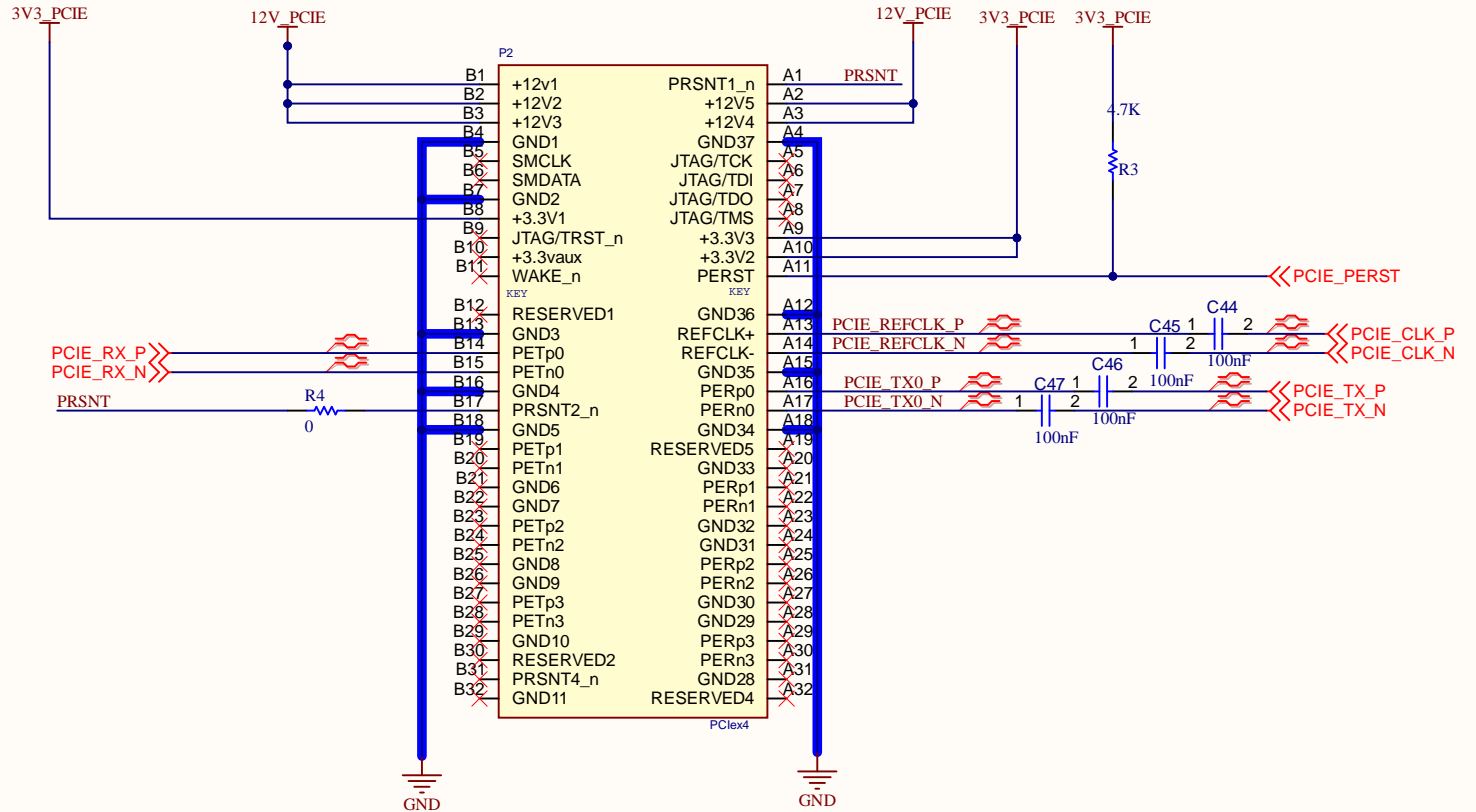


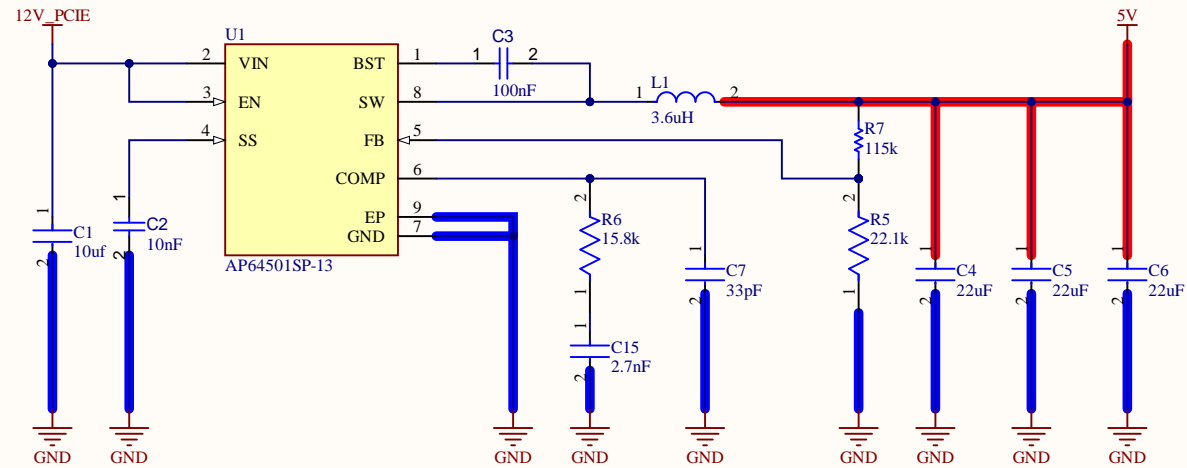
PCIe 2 Port UART Bridge



PCIe Connector

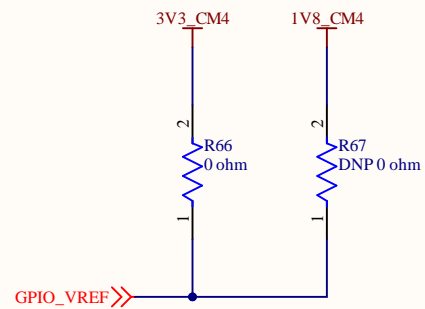


12v to 5v MLS*

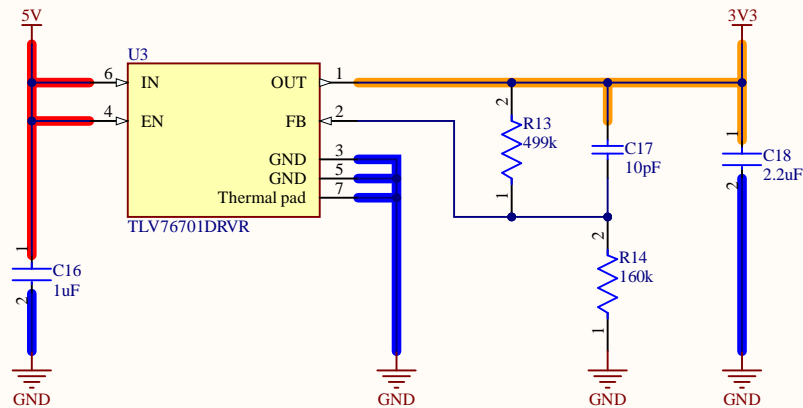


*Mike Lambeta Special

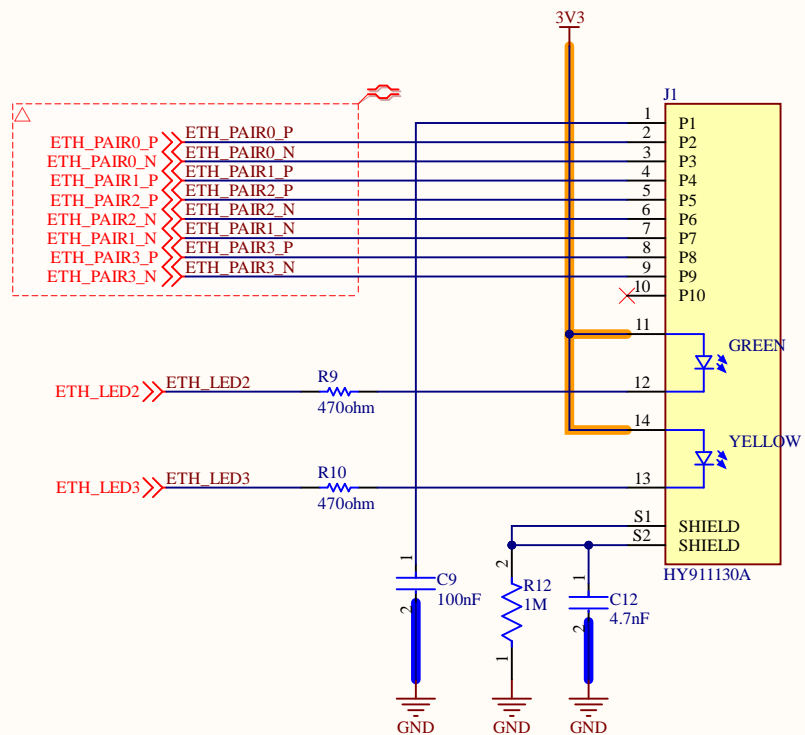
GPIO Voltage Select



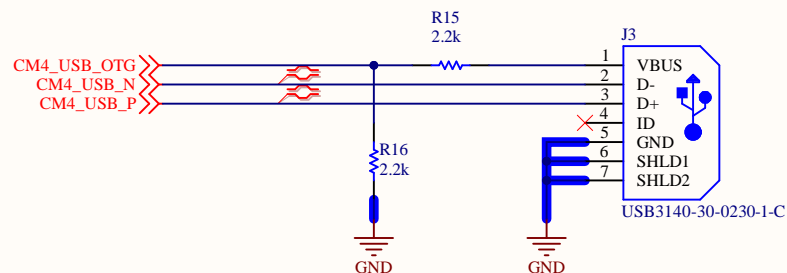
3.3V Power



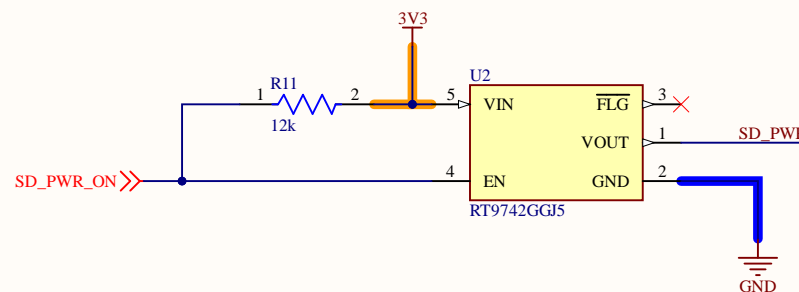
MagJack Connector



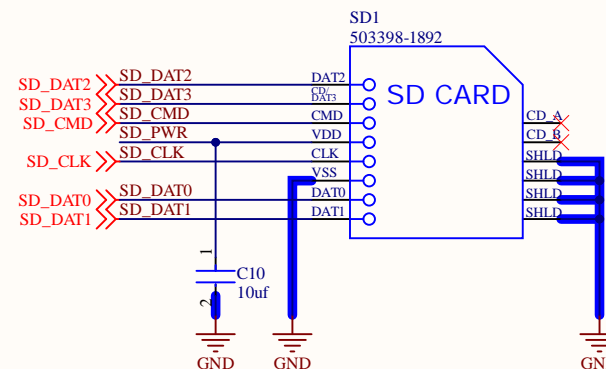
USB Micro-B eMMC Connector



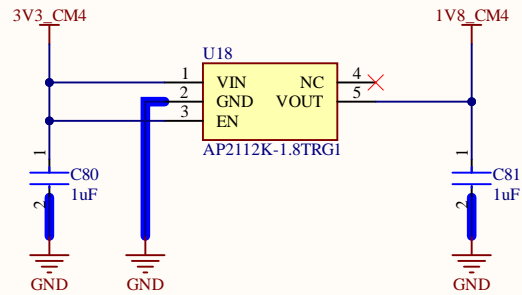
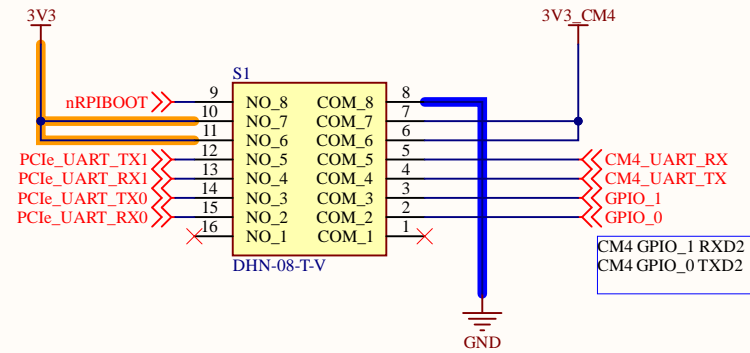
microSD Power Switch



microSD

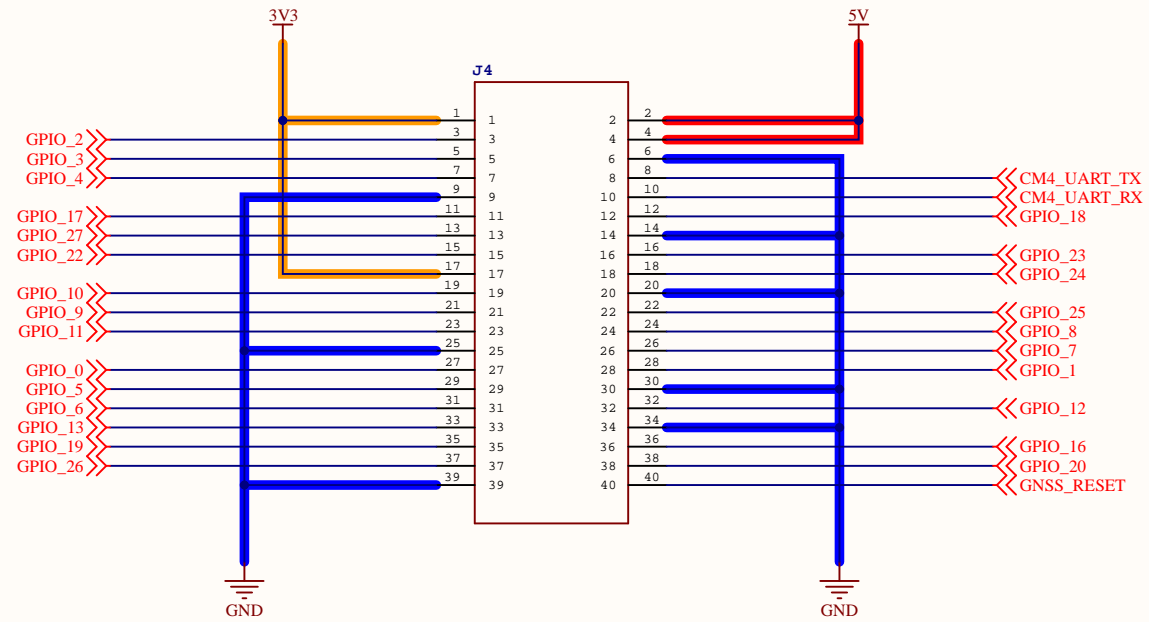


Title		
Size A4	Number	Revision
Date:	7.02.2025	Sheet of
File:	C:\Documents\...\External_Connectors.Schema	Drawn By:



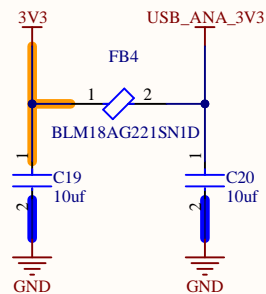
Title		
Size A4	Number	Revision
Date:	7.02.2025	Sheet of
File:	C:\Documents\...\Dip_Switch.SchDoc	Drawn By:

40 Pin RPI GPIO Connector



Title		
Size	Number	Revision
A4		
Date:	7.02.2025	Sheet of
File:	C:\Documents\...\40_Pin_Connector.SchDoc	Drawn By:

A



USB_ANA_3V3

1V05

U5

VDD
VDD
VDD
VDD
VCCA10SSRX34
VCCA10SSRX34
VCCA10SSRX12
VCCA10SSRX12

VSUS33
VSUS33

VCCA33PEX
VCCA33SSM
VCCA33SS12
VCCA33SS12
VCCA33SS34
VCCA33SS34

PEXTX0+
PEXTX0-
PEXR0+
PEXR0-
PEXCLK+
PEXCLK-

PEXWAKE#
PEXRST#
PEXCREQ#

SSXI
SSXO
SSREXT

SPICS#
SPISCLK
SPISI
SPISO
PONRST#
SMI#
EP

VL805

nEXTRST

USB_TX_INT_P
USB_TX_INT_N
USB_RX_INT_P
USB_RX_INT_N
USB_D_INT_P
USB_D_INT_N
USB_STX1_P
USB_STX1_N
USB_RX1_P
USB_RX1_N
USB_D1_P
USB_D1_N
USB_STX2_P
USB_STX2_N
USB_RX2_P
USB_RX2_N
USB_D2_P
USB_D2_N
USB_nOC
USB_nPWR

USB_TX_INT_P
USB_TX_INT_N
USB_RX_INT_P
USB_RX_INT_N
USB_D_INT_P
USB_D_INT_N
USB_STX1_P
USB_STX1_N
USB_RX1_P
USB_RX1_N
USB_D1_P
USB_D1_N
USB_STX2_P
USB_STX2_N
USB_RX2_P
USB_RX2_N
USB_D2_P
USB_D2_N
USB_nOC
USB_nPWR

USB_TX_INT_P
USB_TX_INT_N
USB_RX_INT_P
USB_RX_INT_N
USB_D_INT_P
USB_D_INT_N
USB_STX1_P
USB_STX1_N
USB_RX1_P
USB_RX1_N
USB_D1_P
USB_D1_N
USB_STX2_P
USB_STX2_N
USB_RX2_P
USB_RX2_N
USB_D2_P
USB_D2_N
USB_nOC
USB_nPWR

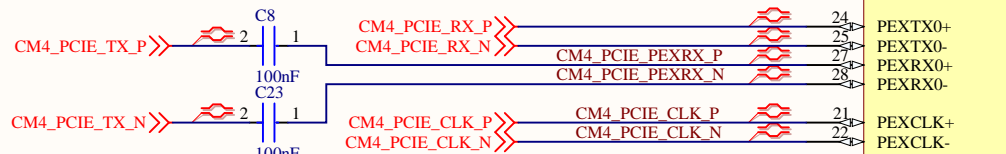
USB_TX_INT_P
USB_TX_INT_N
USB_RX_INT_P
USB_RX_INT_N
USB_D_INT_P
USB_D_INT_N
USB_STX1_P
USB_STX1_N
USB_RX1_P
USB_RX1_N
USB_D1_P
USB_D1_N
USB_STX2_P
USB_STX2_N
USB_RX2_P
USB_RX2_N
USB_D2_P
USB_D2_N
USB_nOC
USB_nPWR

USB_TX_INT_P
USB_TX_INT_N
USB_RX_INT_P
USB_RX_INT_N
USB_D_INT_P
USB_D_INT_N
USB_STX1_P
USB_STX1_N
USB_RX1_P
USB_RX1_N
USB_D1_P
USB_D1_N
USB_STX2_P
USB_STX2_N
USB_RX2_P
USB_RX2_N
USB_D2_P
USB_D2_N
USB_nOC
USB_nPWR

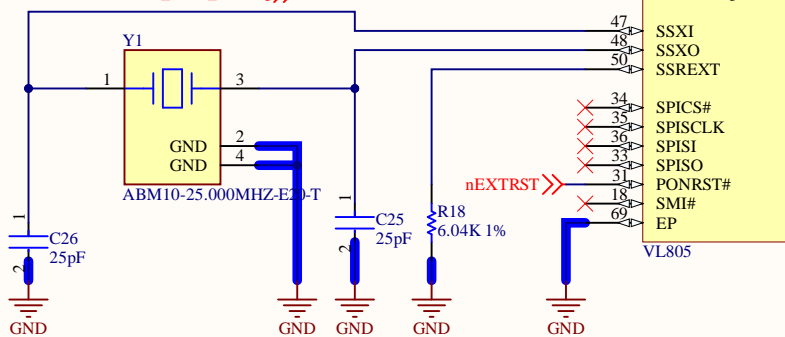
USB_TX_INT_P
USB_TX_INT_N
USB_RX_INT_P
USB_RX_INT_N
USB_D_INT_P
USB_D_INT_N
USB_STX1_P
USB_STX1_N
USB_RX1_P
USB_RX1_N
USB_D1_P
USB_D1_N
USB_STX2_P
USB_STX2_N
USB_RX2_P
USB_RX2_N
USB_D2_P
USB_D2_N
USB_nOC
USB_nPWR

USB_TX_INT_P
USB_TX_INT_N
USB_RX_INT_P
USB_RX_INT_N
USB_D_INT_P
USB_D_INT_N
USB_STX1_P
USB_STX1_N
USB_RX1_P
USB_RX1_N
USB_D1_P
USB_D1_N
USB_STX2_P
USB_STX2_N
USB_RX2_P
USB_RX2_N
USB_D2_P
USB_D2_N
USB_nOC
USB_nPWR

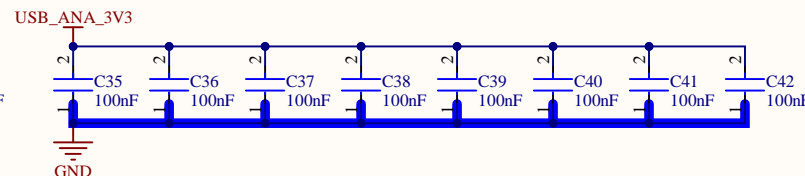
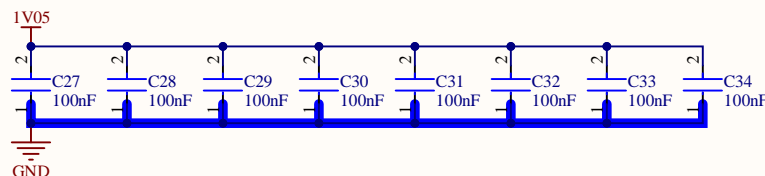
B



C

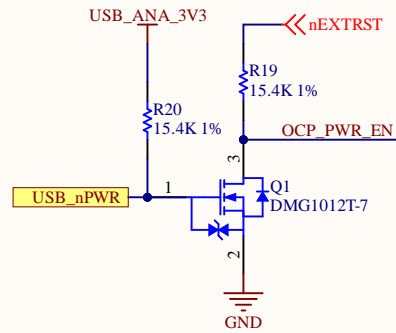


D



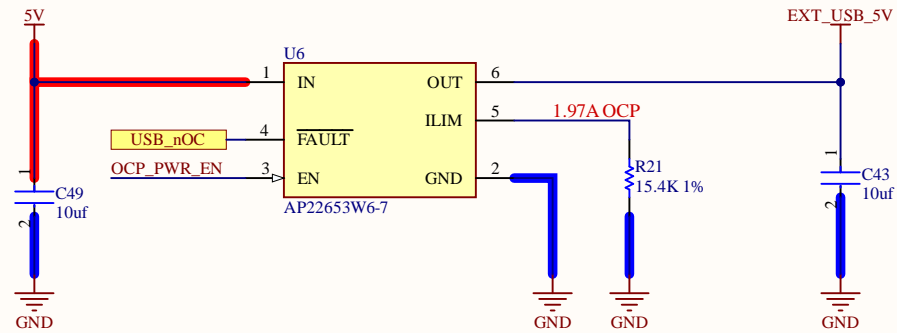
USB Port Disable

Disable VUSB output during startup

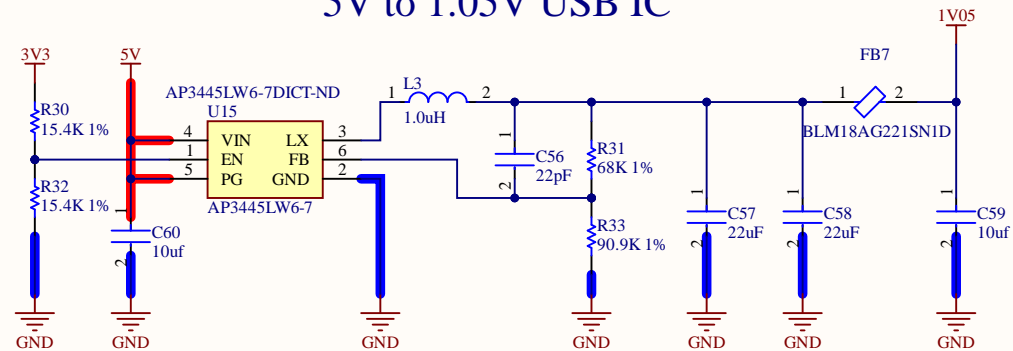


USB Port OC Protection

~2A Maximum from Load Switch



5V to 1.05V USB IC



A

B

C

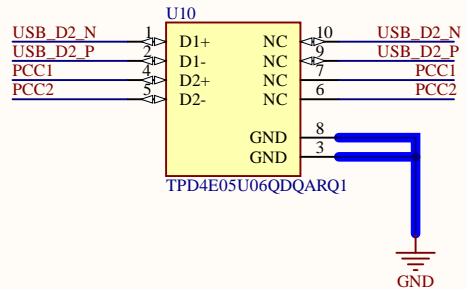
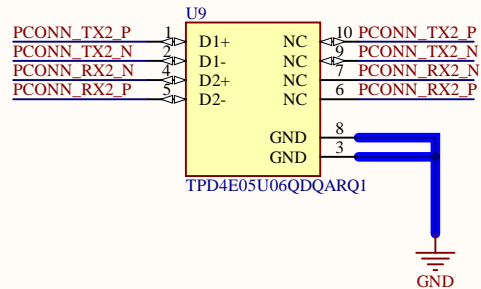
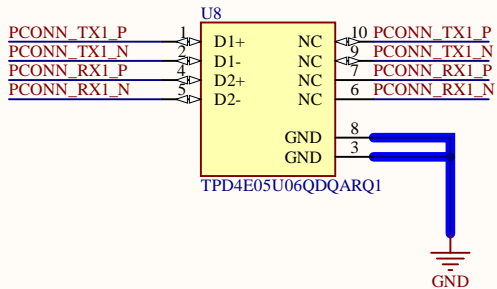
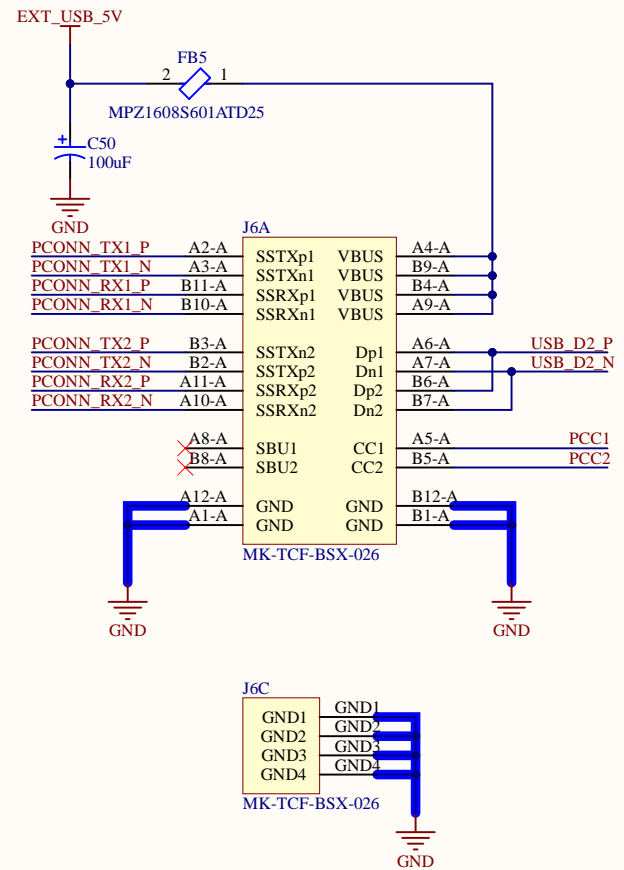
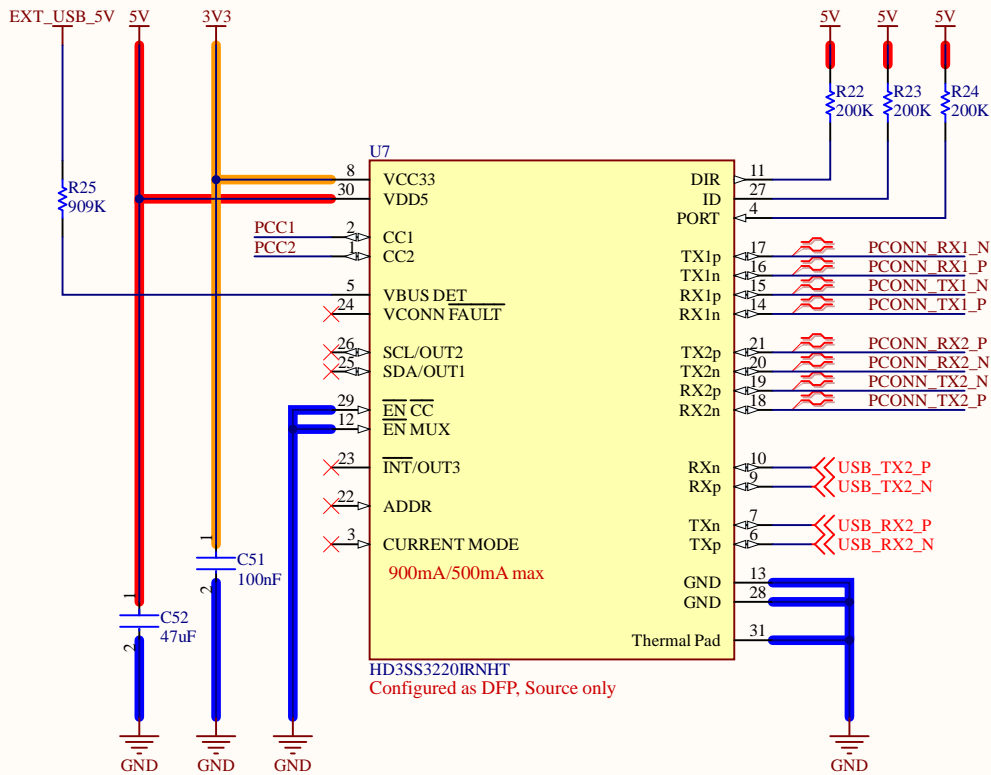
D

A

B

C

D



A

B

C

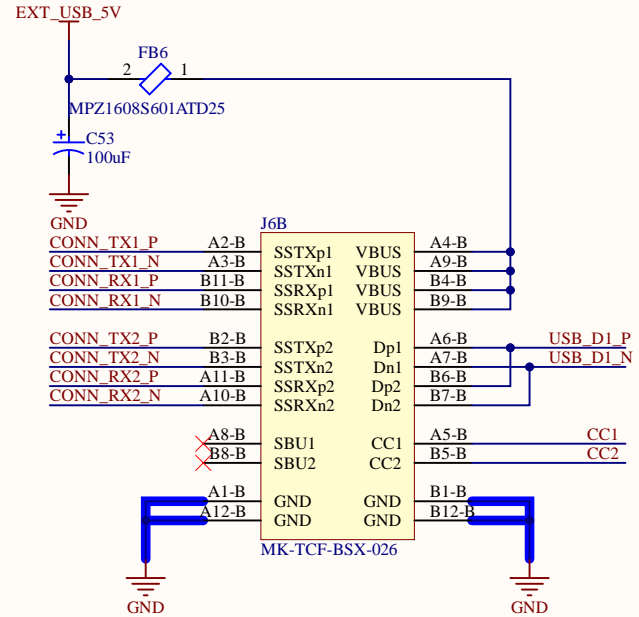
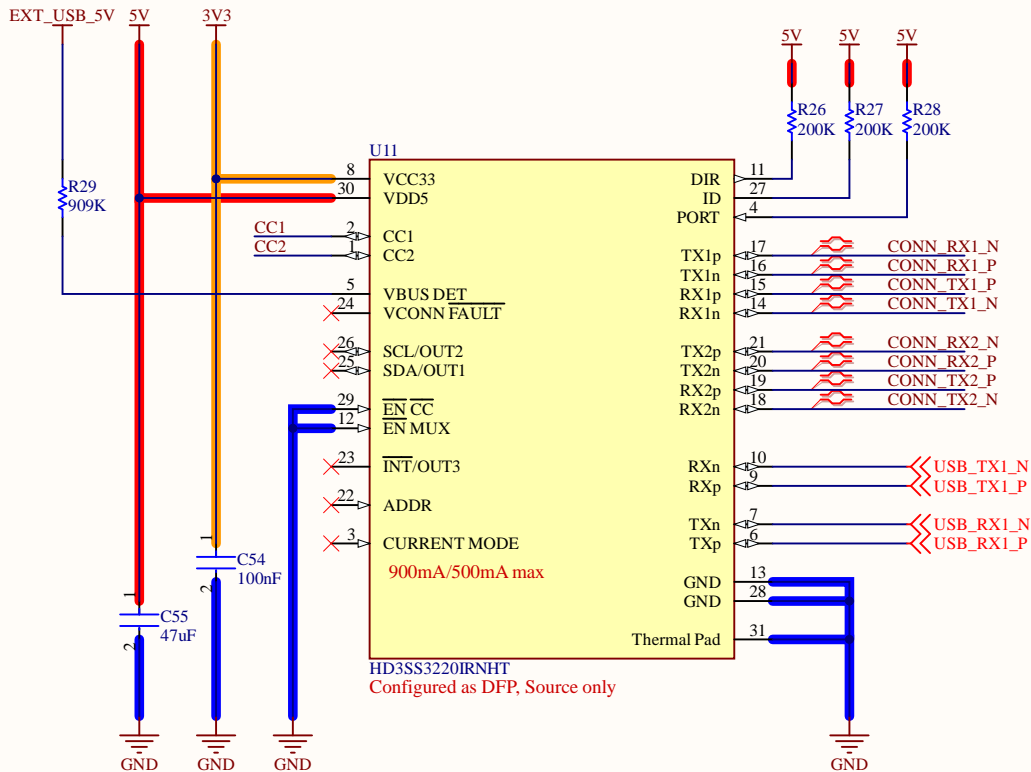
D

A

B

C

D



USB_D1_P >> USB_D1_P
USB_D1_N >> USB_D1_N

