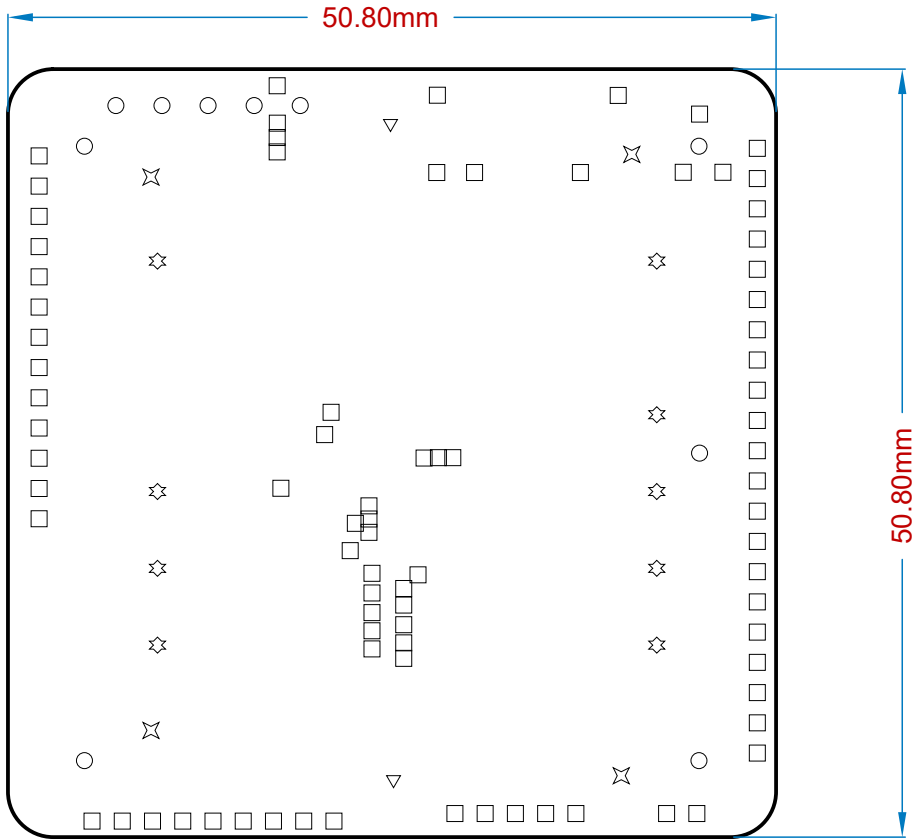
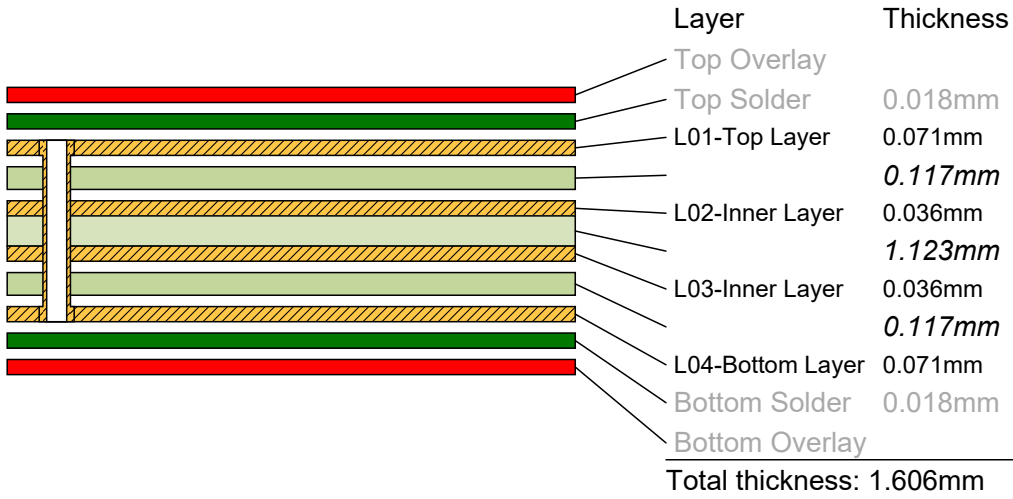


Layer Stack Legend



Drill Drawing View (Scale 2:1)

Drill Table

Symbol	Count	Hole Size	Plated	Drill Layer Pair	Via / Pad	Hole Tolerance
□	84	0.300mm	Plated	L01-Top Layer - L04-Bottom Layer	Via	+0.000mm/-0.300mm
○	10	1.090mm	Plated	L01-Top Layer - L04-Bottom Layer	Pad	+/-0.076mm
☆	9	1.200mm	Plated	L01-Top Layer - L04-Bottom Layer	Pad	+/-0.075mm
▽	2	3.000mm	Non-Plated	L01-Top Layer - L04-Bottom Layer	Pad	+/-0.051mm
✧	4	3.700mm	Plated	L01-Top Layer - L04-Bottom Layer	Pad	+/-0.075mm
	109 Total					

REV	DESCRIPTION	DATE	APPROVED
X1	INITIAL RELEASE	01/24/2023	

NOTES: UNITS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED

- INTERPRET THIS DRAWING IN ACCORDANCE WITH IPC-D-325A.
- BOARD FABRICATION AND QUALITY PER IPC-6012, CLASS 2, EXCEPT SPECIFIED HEREIN.
- MUST COMPLY WITH EUROPEAN DIRECTIVE 2002/95/EC (RoHS).
- DIMENSIONAL LIMITS APPLY AFTER PLATING OR COATING.
- BOW AND TWIST MAXIMUM IS 0.75%.
- MATERIAL: LAMINATE AND PREPREG SHALL BE IN ACCORDANCE WITH IPC-4101/21. 170 DEGREES CELSIUS MINIMUM Tg, UL 94V-0.
- STACKUP SUMMARY:
 - NUMBER OF COPPER LAYERS: 4
 - BOARD THICKNESS SHALL BE 1.6mm +/- 10%
 - COPPER: See Layer Stack
 - DEFAULT TRACE/SPACE: 0.2mm/0.2mm
 - CONDUCTOR WIDTH TOLERANCE = +/- 0.01mm
- ALL VIAS TO BE TENTED BY SOLDERMASK
- SURFACE FINISH/PLATING:
 - BOARD SHALL BE IMMERSION GOLD PLATED (ENIG) ACCORDING TO IPC-4552. THICKNESS SHALL BE A MINIMUM OF 0.05µm GOLD OVER 3-6µm NICKEL.
- SOLDERMASK WITH LIQUID PHOTO IMAGEABLE (LPI) PER IPC-SM-840C, CLASS T. COLOR: BLACK
- SILKSCREEN PER SUPPLIED ARTWORK WITH ORGANIC, NON-CONDUCTIVE, EPOXY INK. SILKSCREEN MAY BE TRIMMED OFF ANY SOLDERABLE ENTITY. COLOR: WHITE
- 100% BARE BOARD ELECTRICAL TEST TO BE DONE WITH REFERENCE TO SUPPLIED NETLIST.
- LOCATE MANUFACTURER'S IDENTIFICATION AND LOT CODE ON PRIMARY SIDE FREE FROM ALL METAL ENTITY RENDERED IN SILKSCREEN.
- CONTROLLED IMPEDANCE REQUIRED ON BOARD. SEE TABLE 1: SINGLE ENDED CONTROLLED IMPEDANCE

TABLE1: SINGLE ENDED CONTROLLED IMPEDANCE

Trace layer	Reference layers	Target Impedance	Wide Trace Width	Target Tolerance
L01-Top Layer	L02-Inner Layer	50	0.19mm	10%
L04-Bottom Layer	L03-Inner Layer	50	0.19mm	10%