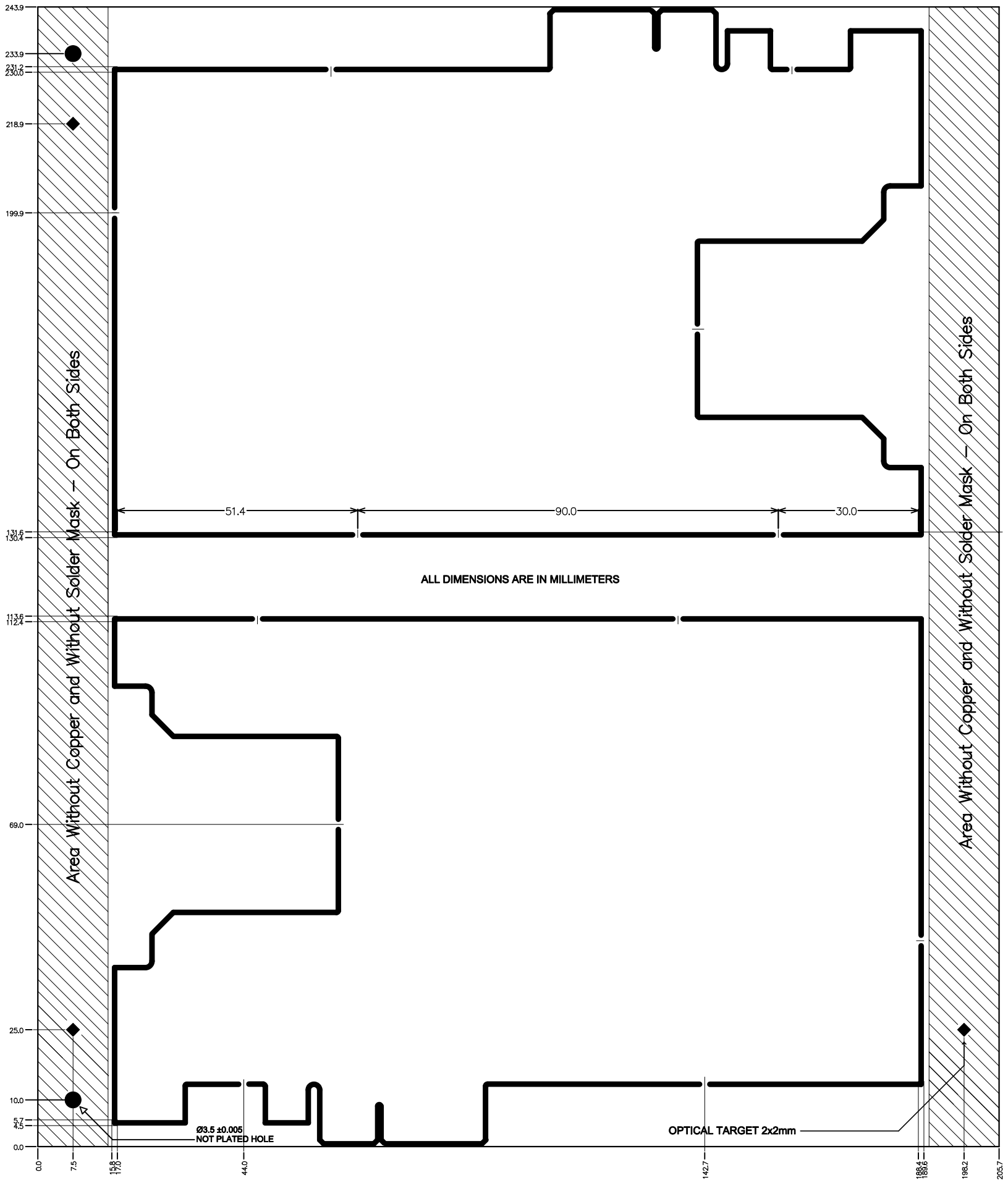
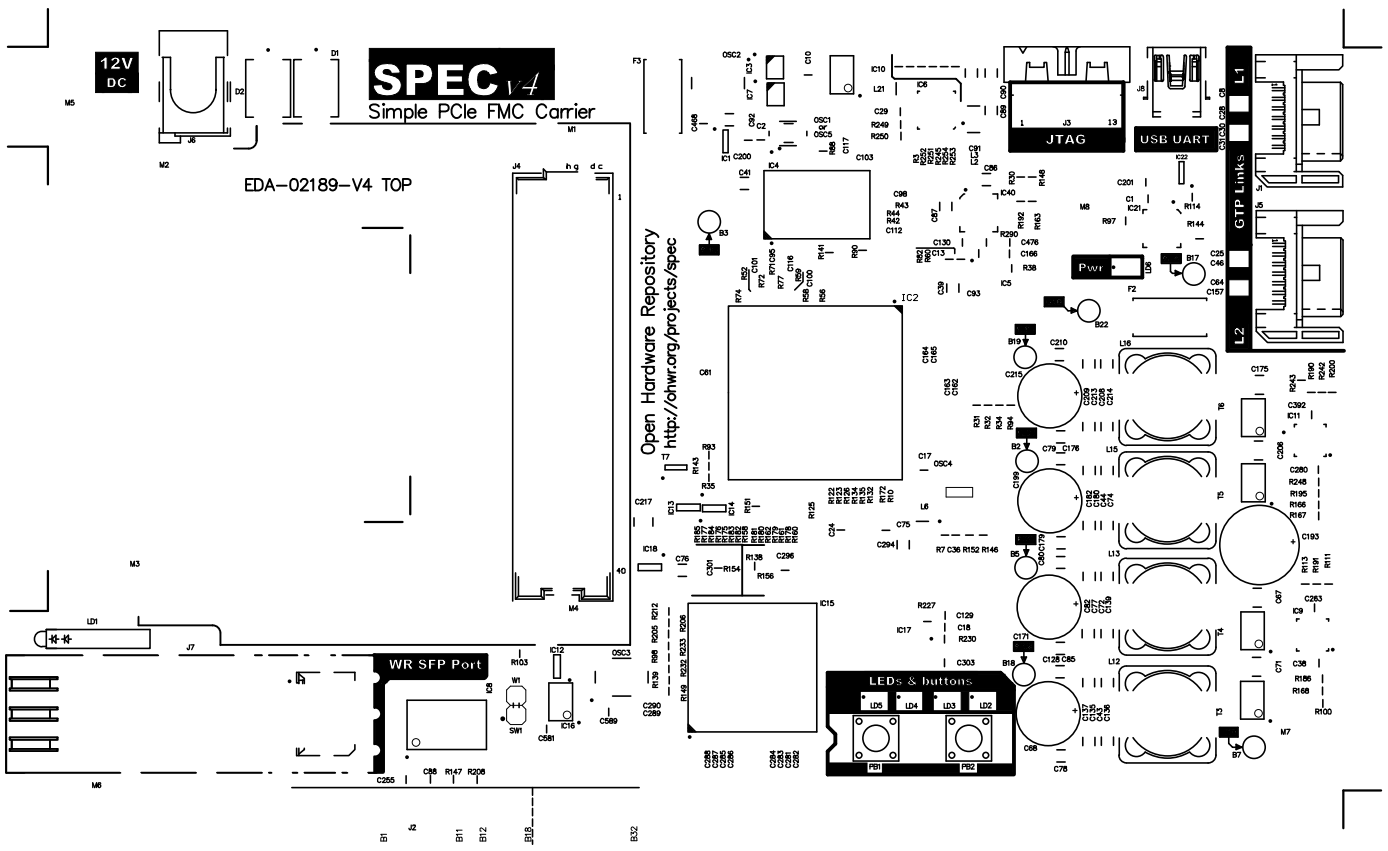


Number	EDA–02189–V4
Drawn By	GK/TW
DATE	18–07–2011
CHECK BY	CEGELEC NL (23–08–2011)

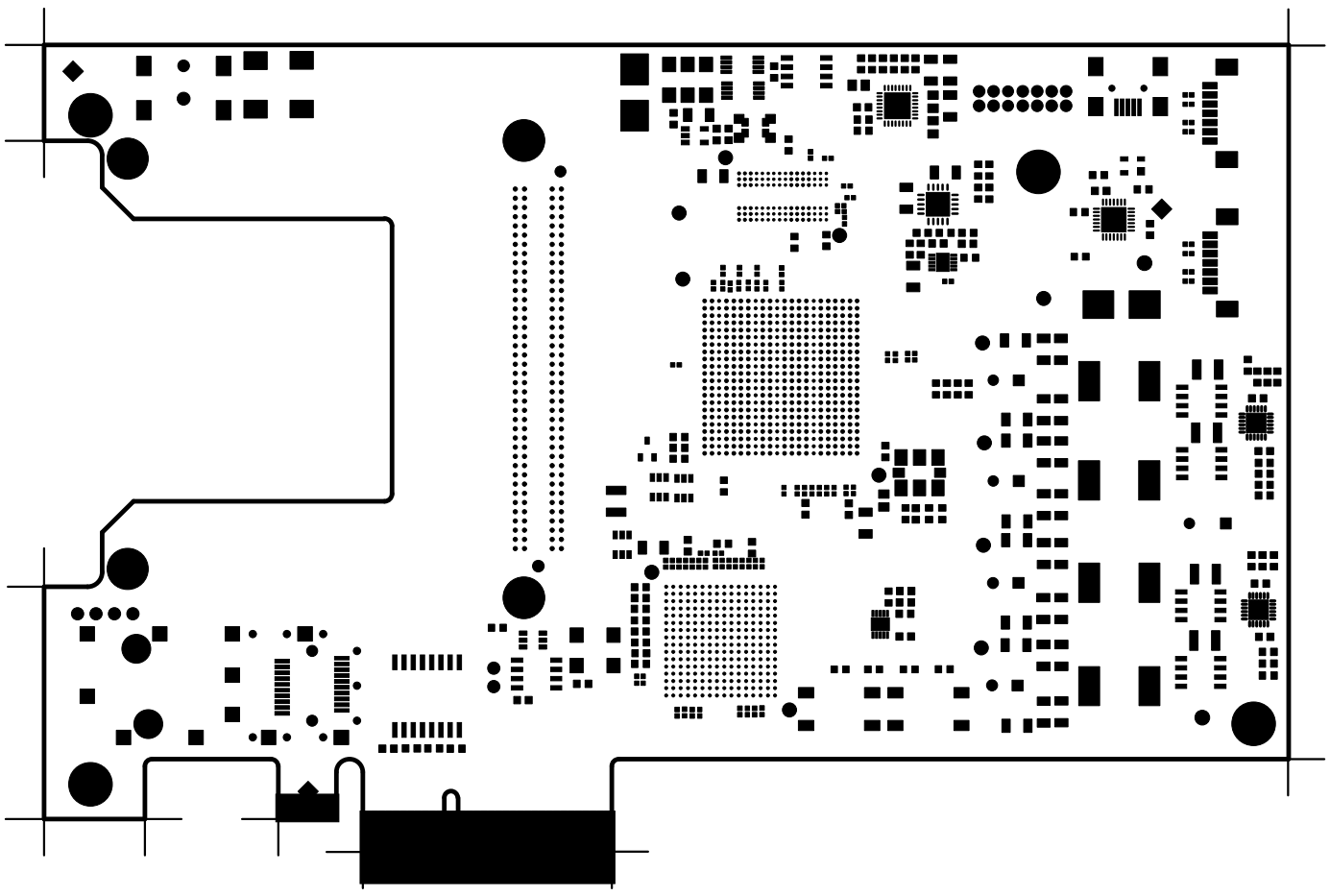
SCALE: 1.00 Print Date 23/08/2011 14:55:01



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:54

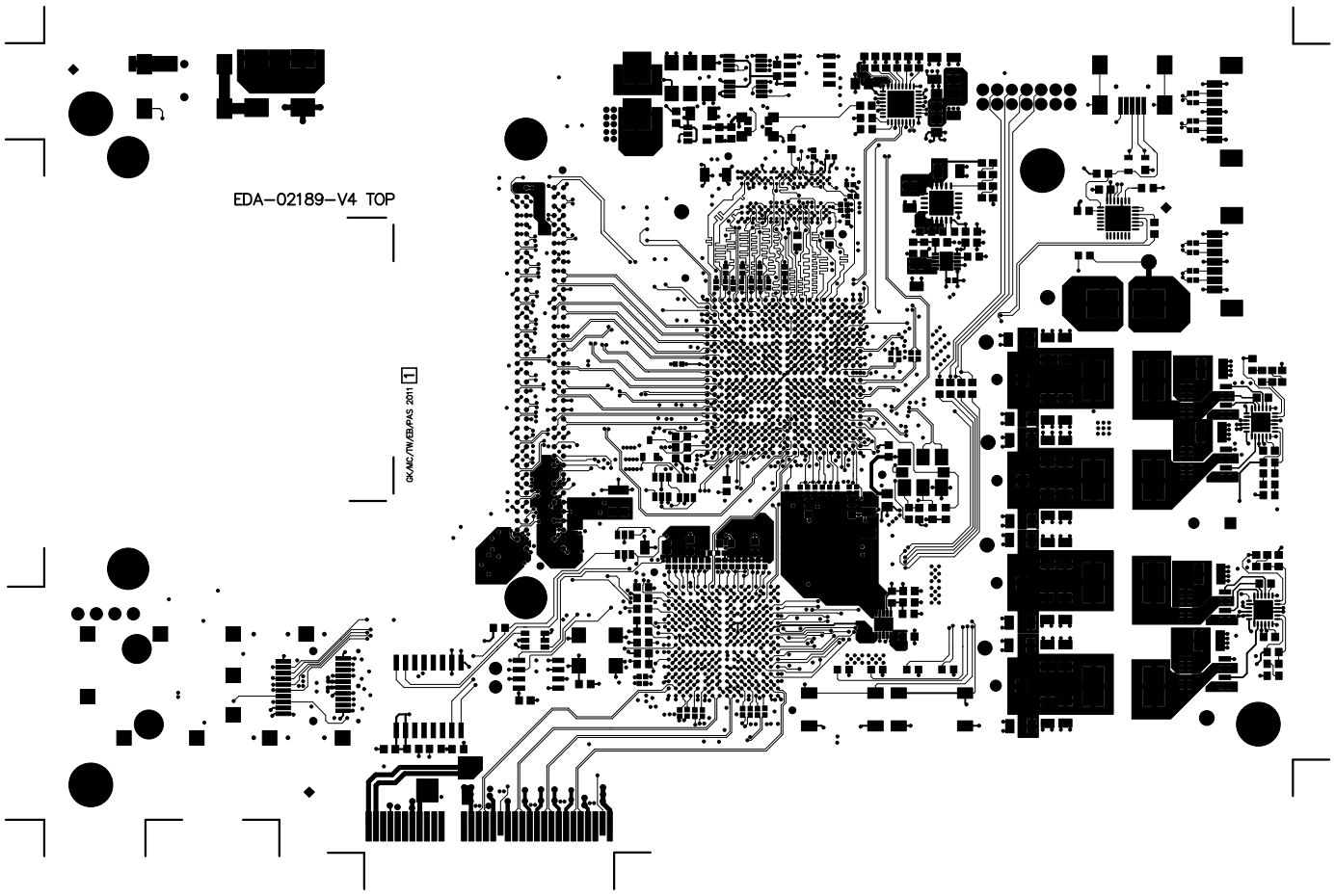
Top Overlay



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:59

Top Solder

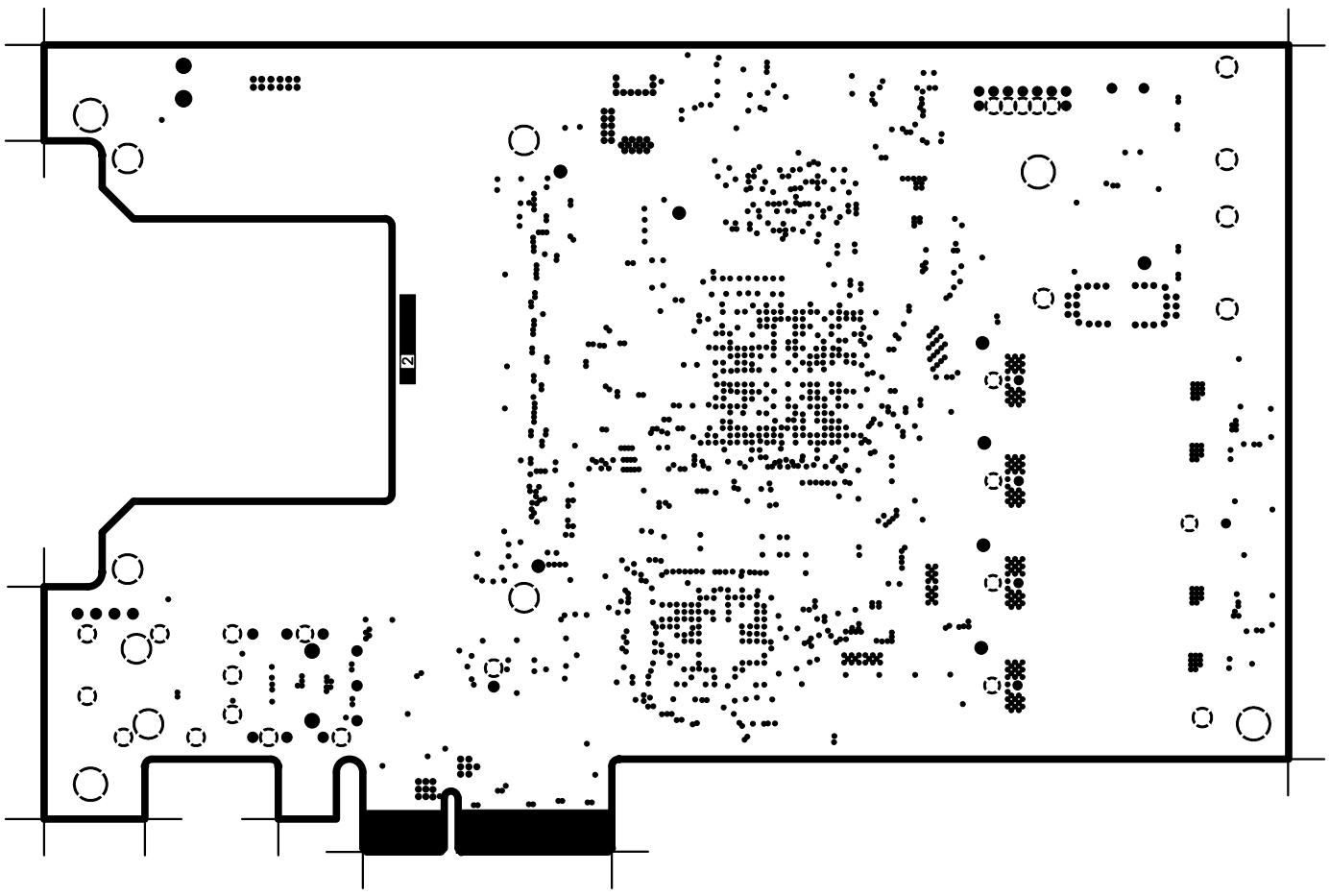


Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:52

Top Layer

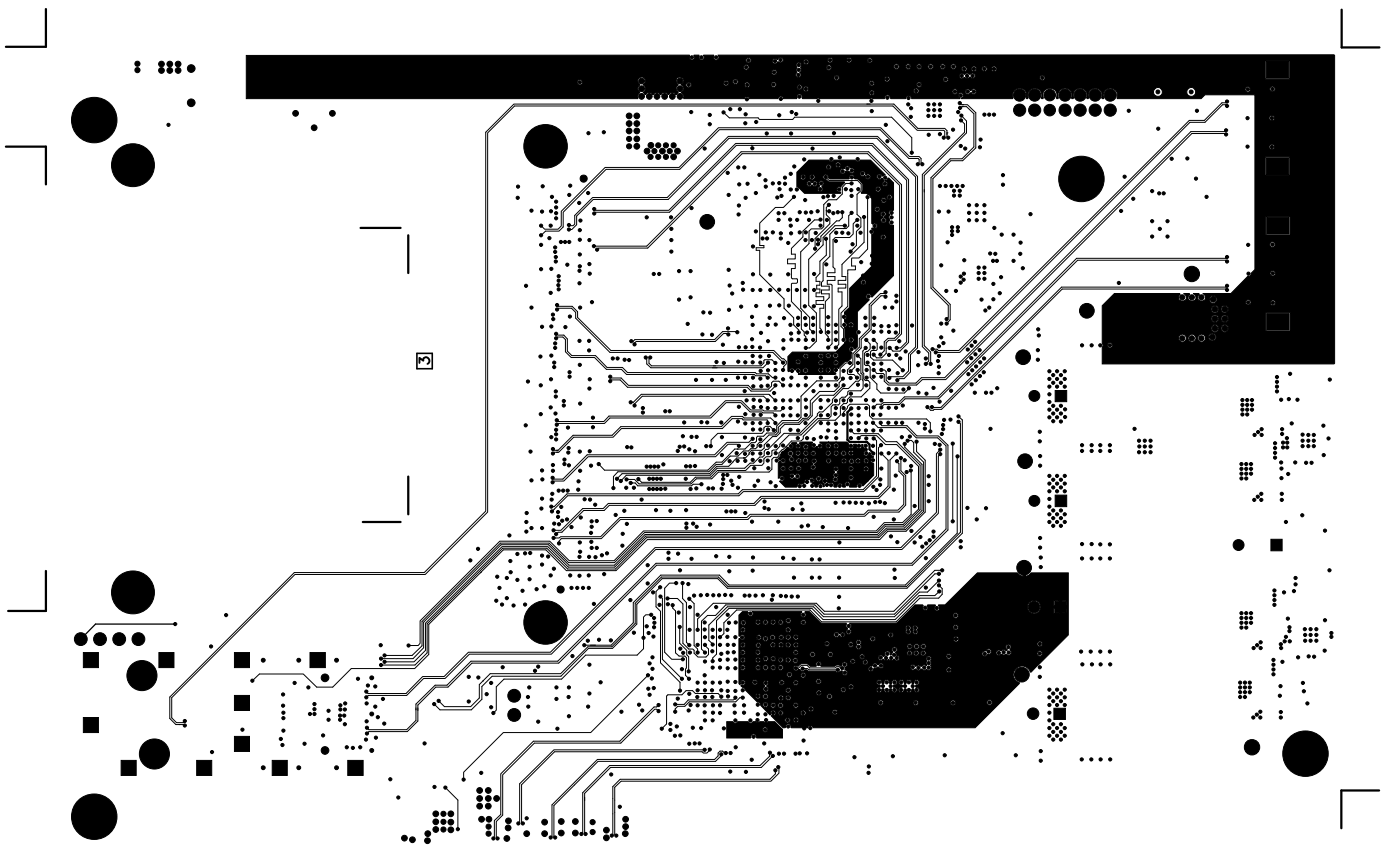
L1					



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:59

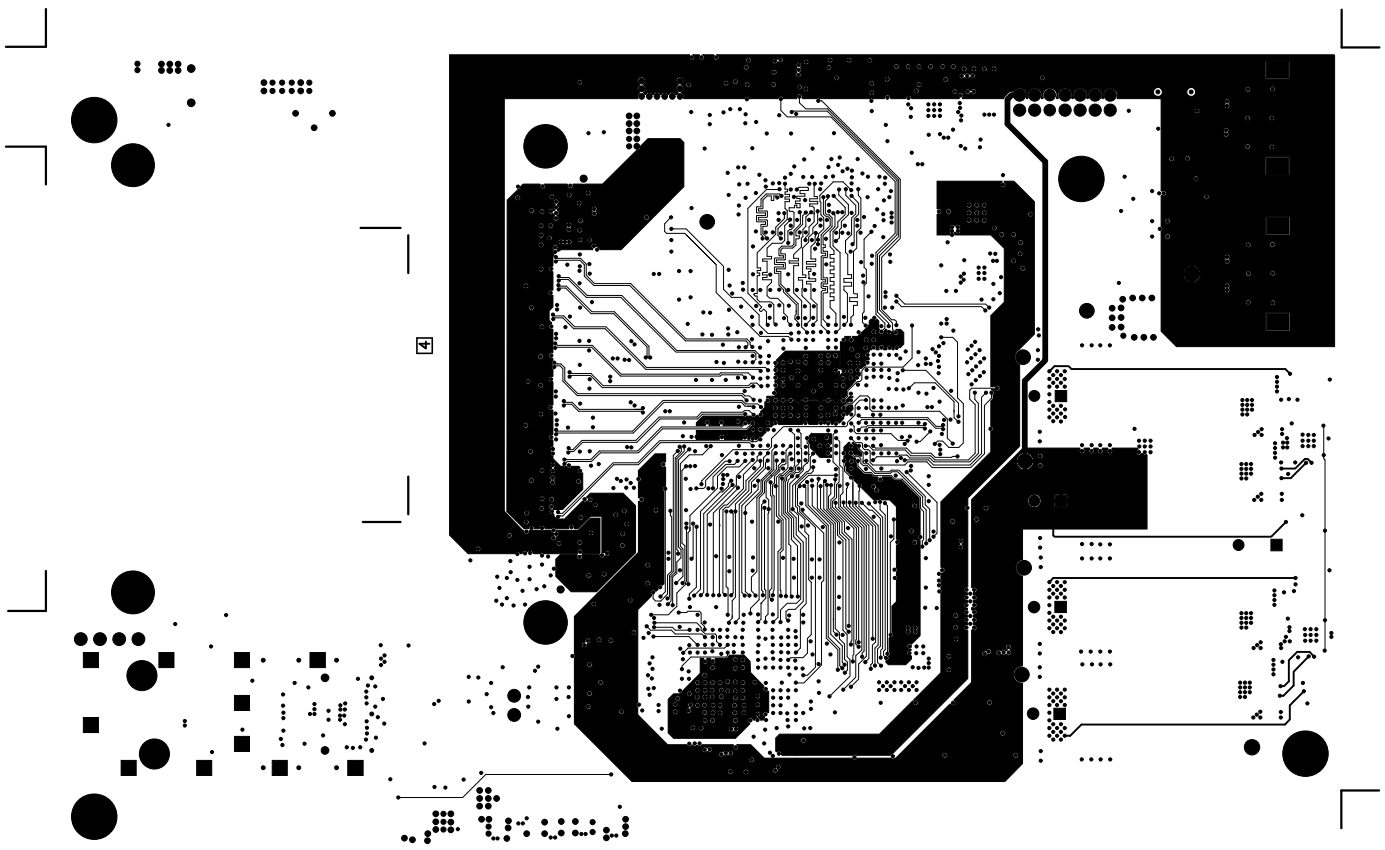
L2



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:52

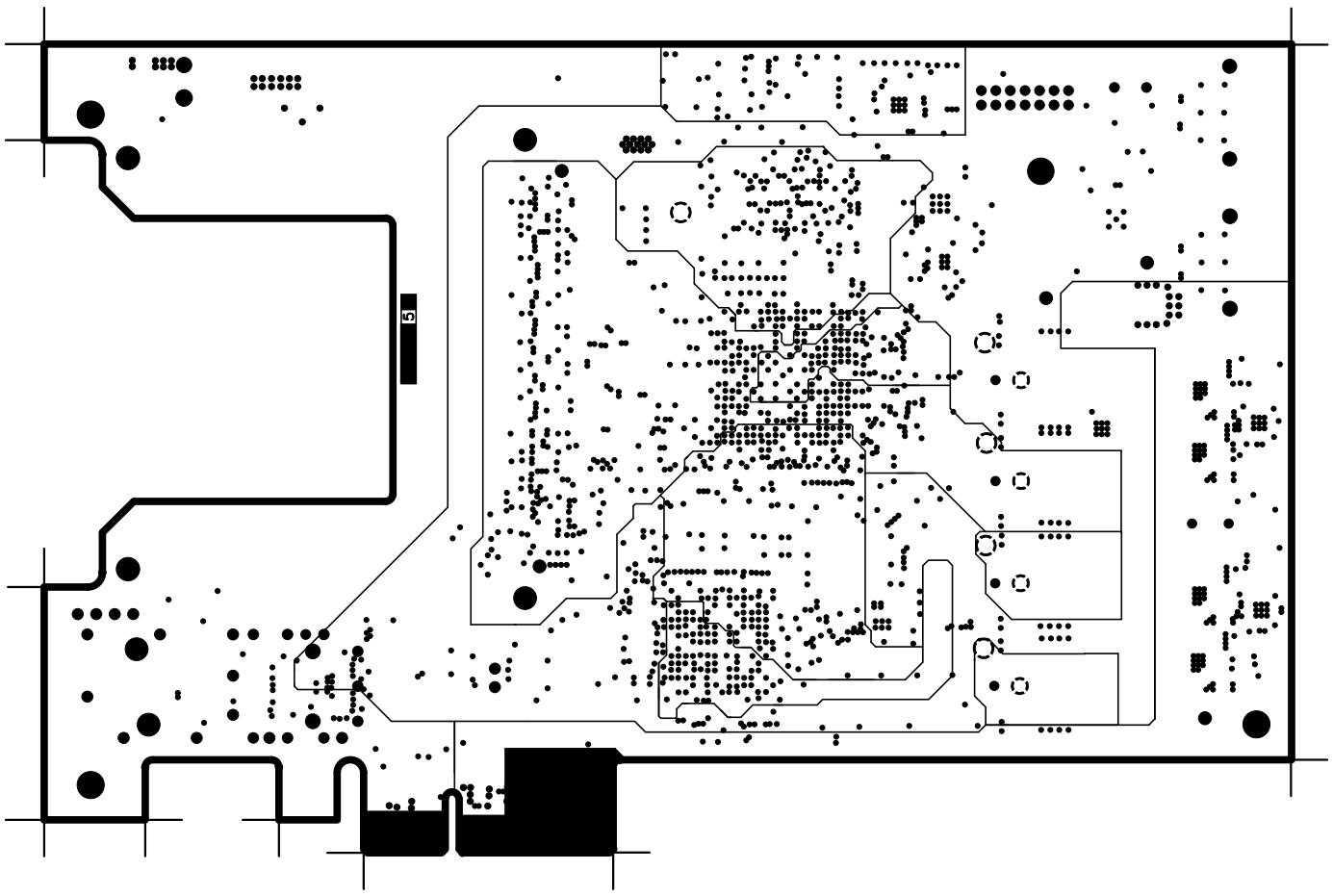
		L3			



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:52

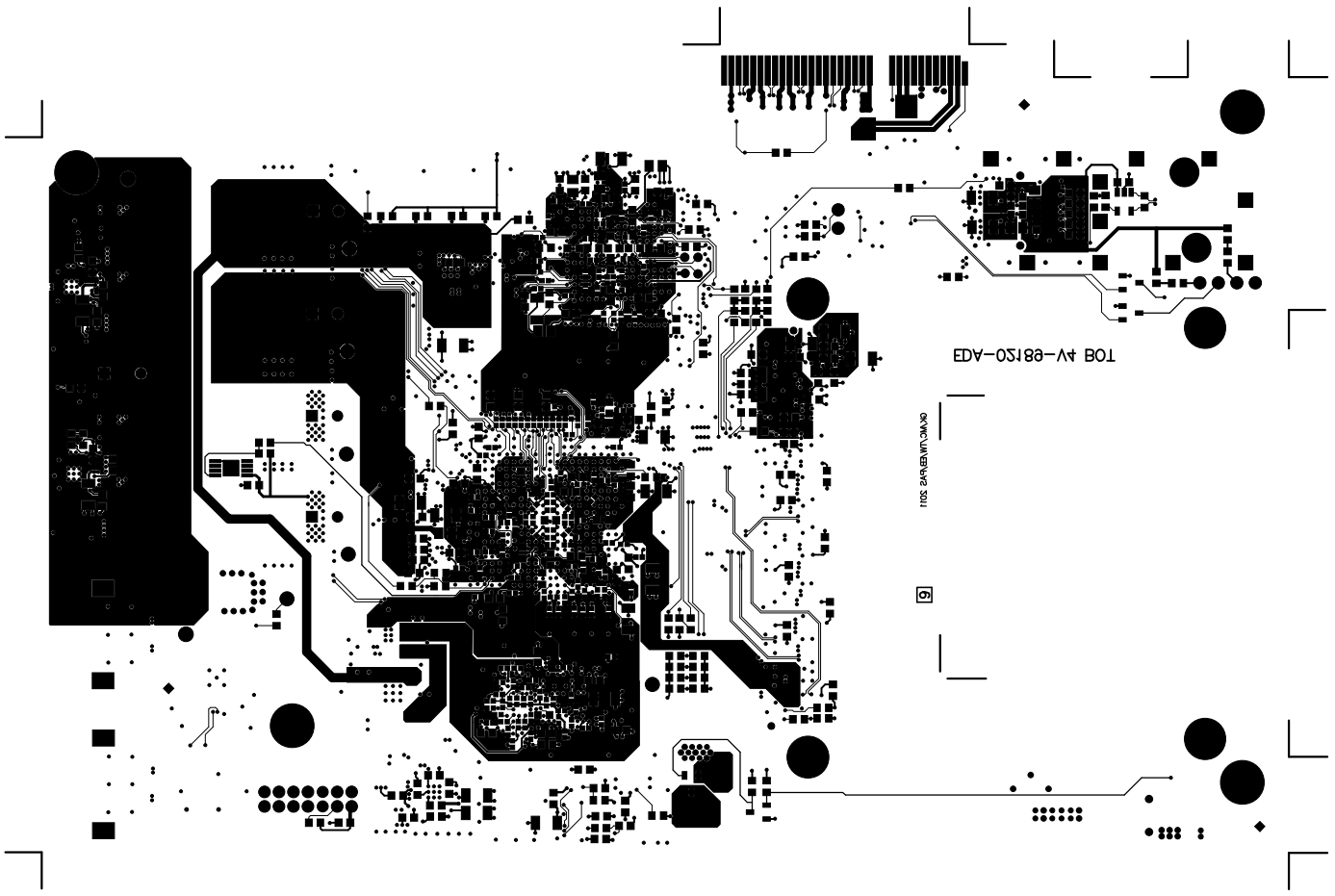
			L4		



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:59

L5



					L6

Bottom Layer

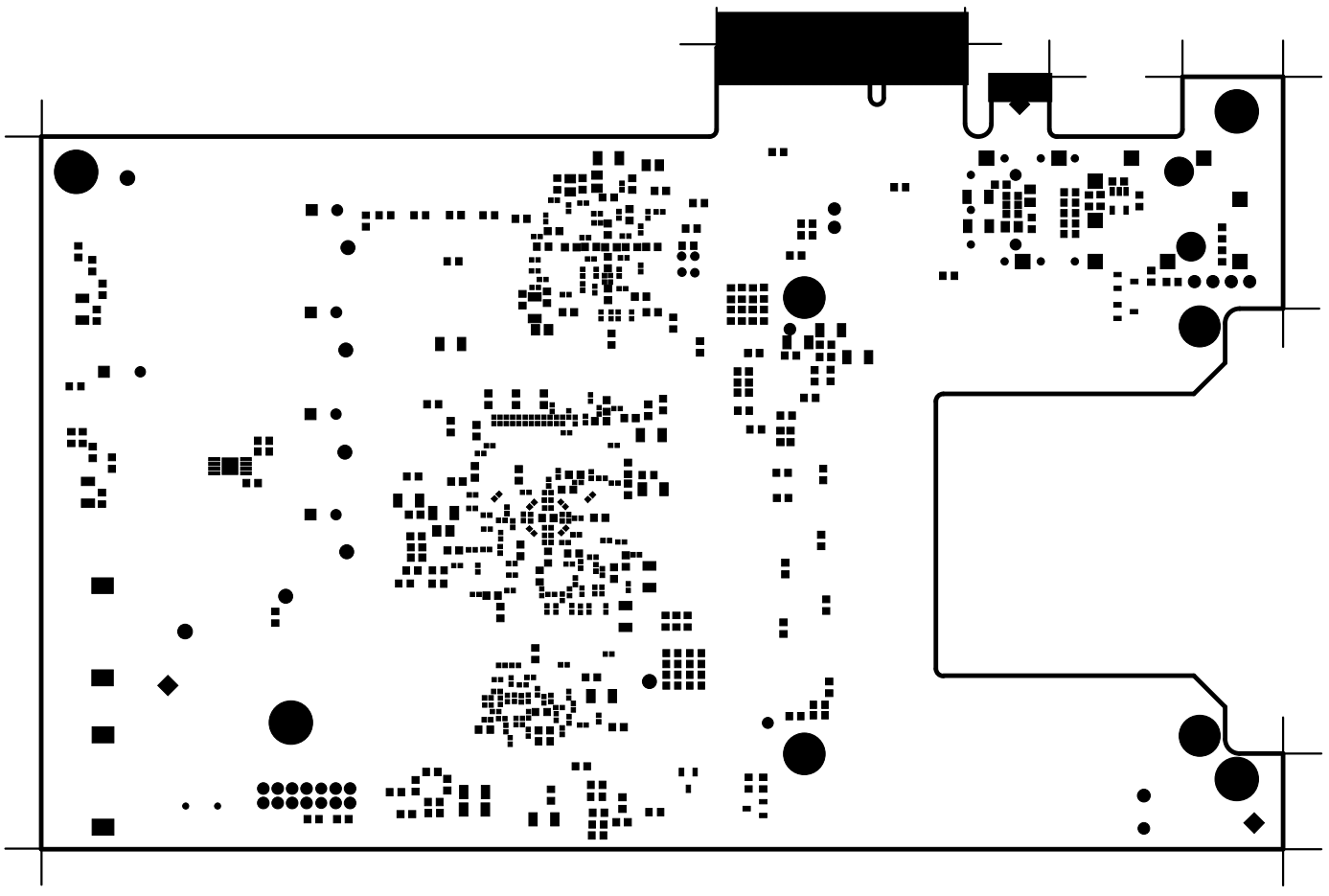
Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

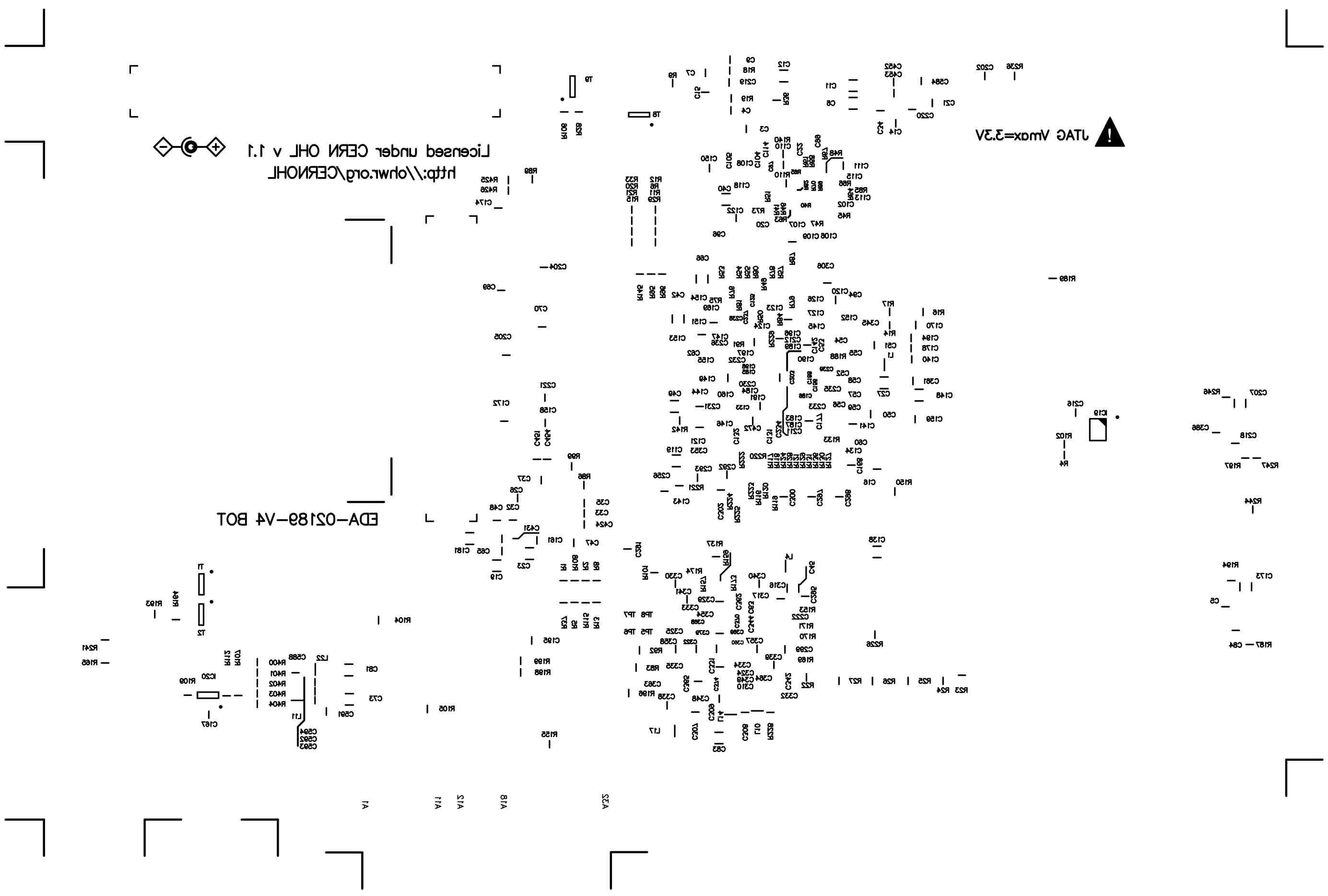
SCALE: 1.00 Print Date 23/08/2011 14:54:54

Bottom Solder

Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:59

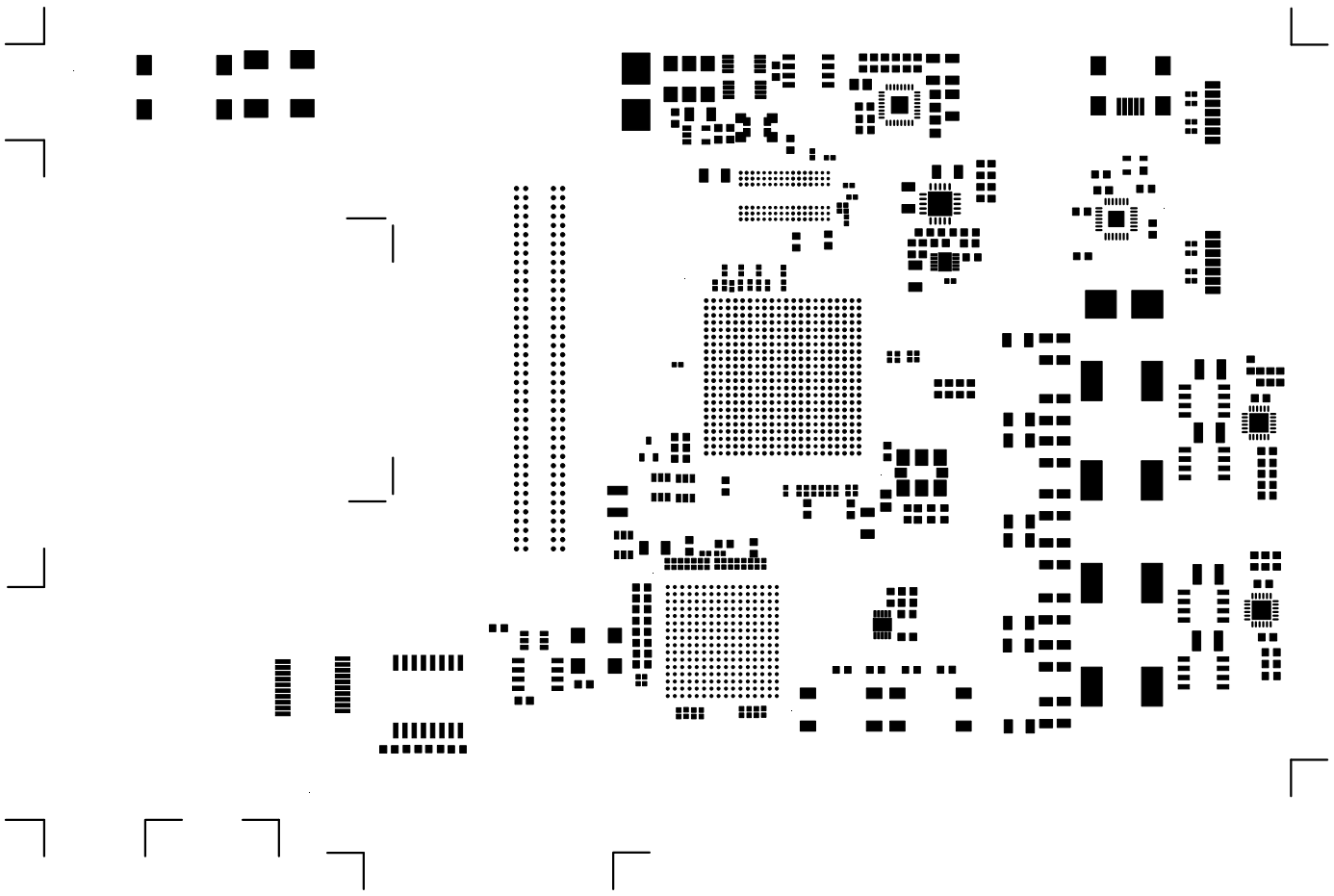




Bottom Overlay

Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:55



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

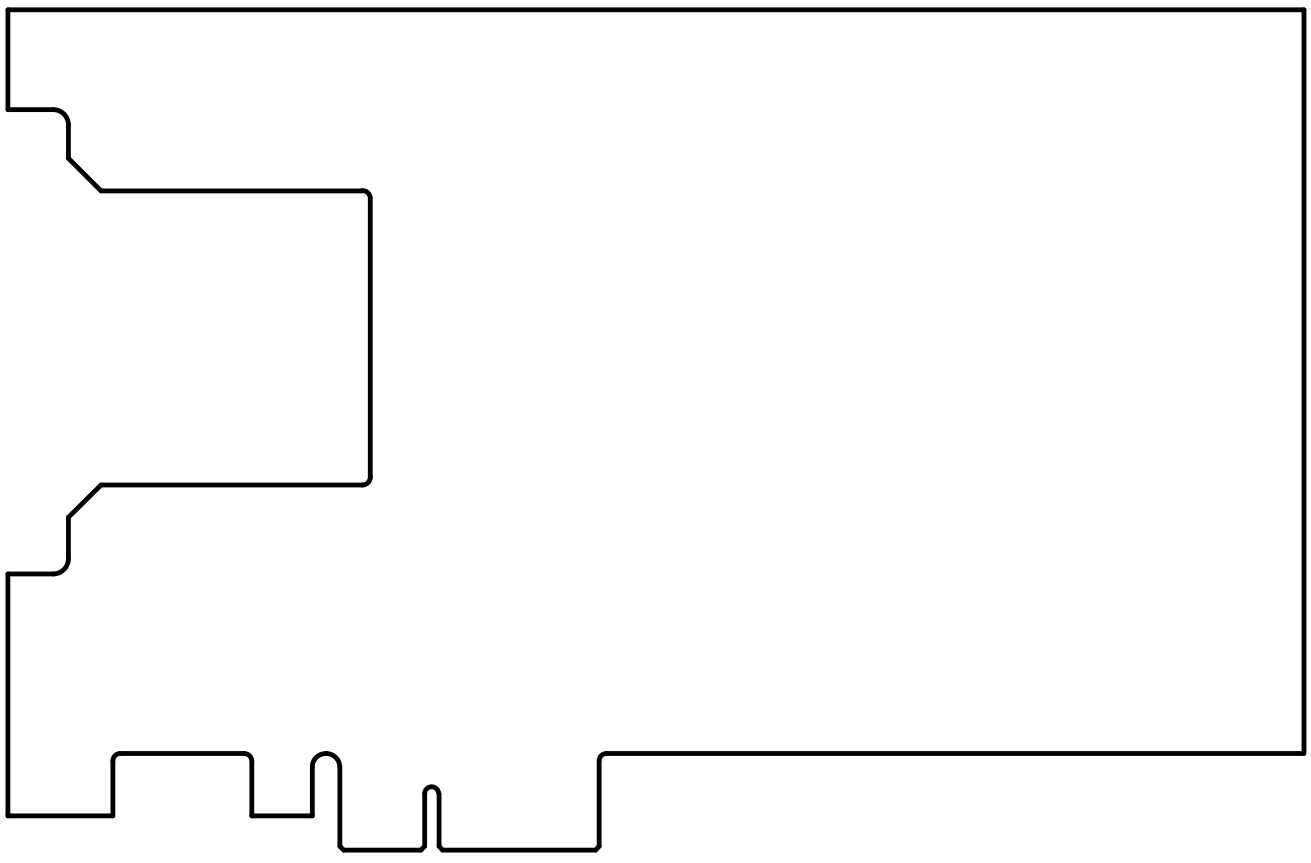
SCALE: 1.00 Print Date 23/08/2011 14:54:54

Top Paste

Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 14:54:55

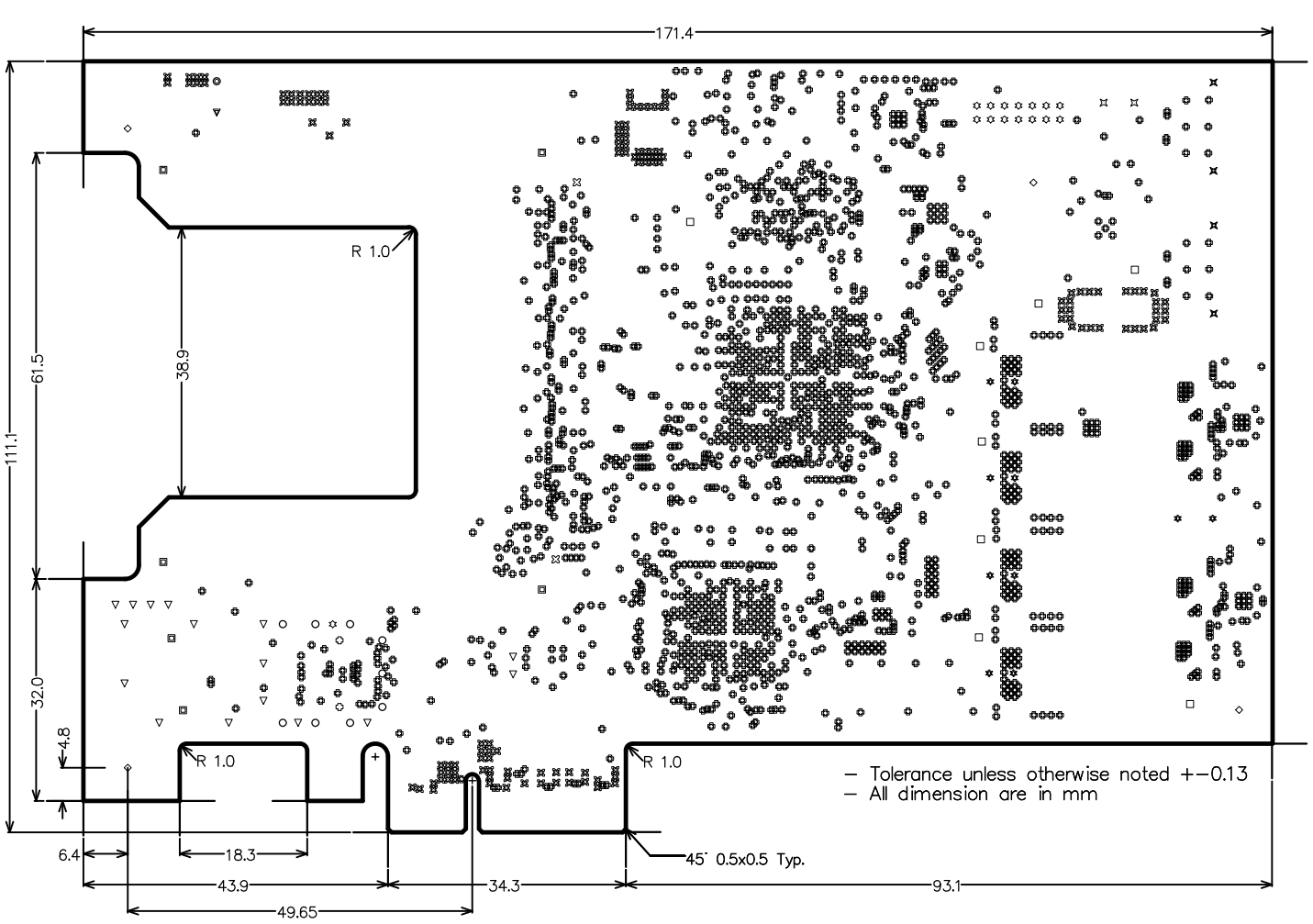
Bottom Paste

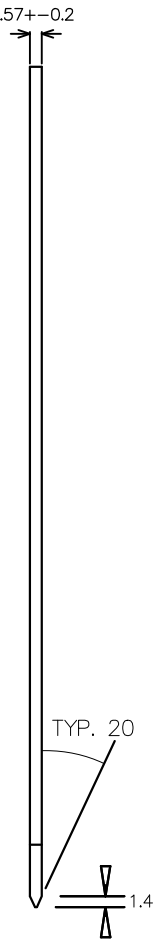


Symbol	Hit Count	Tool Size	Plated	Hole Type
⌖	2006	7.874mil (0.2mm)	PTH	Round
⊠	125	13.78mil (0.35mm)	PTH	Round
✡	10	31.496mil (0.8mm)	PTH	Round
⌑	2	35.433mil (0.9mm)	NPTH	Round
✡	15	35.433mil (0.9mm)	PTH	Round
○	9	37.402mil (0.95mm)	NPTH	Round
▽	16	41.339mil (1.05mm)	PTH	Round
□	8	51.181mil (1.3mm)	PTH	Round
⊠	2	51.181mil (1.3mm)	NPTH	Round
⌘	4	59.055mil (1.5mm)	PTH	Round
⌖	2	61.024mil (1.55mm)	NPTH	Round
◎	1	62.992mil (1.6mm)	NPTH	Round
▽	1	70.866mil (1.8mm)	NPTH	Round
⧈	6	106.299mil (2.7mm)	PTH	Round
◇	4	125.984mil (3.2mm)	PTH	Round
	2211 Total			

LAYER IDENTIFICATION		LINE WIDTH/GAP (Mils)	FINISHED COPPER THICKNESS	CONTROLLED IMPEDANCE +/−10%	DIELECTRIC THICKNESS *=CRITICAL
TOP LAYER	SIGNAL LAYER	5 / 5	35 um	50/*100 Ohms	
					* 0.1mm
LAYER 2	GND PLANE	–	35 um		
					* 0.1mm
LAYER 3	SIGNAL LAYER	5 / 5	35 um	50/*100 Ohms	
					1.0mm
LAYER 4	SIGNAL LAYER	5 / 5	35 um	50/*100 Ohms	
					* 0.1mm
LAYER 5	POWER PLANE	–	35 um		
					* 0.1mm
BOTTOM LAYER	SIGNAL LAYER	5 / 5	35 um	50/*100 Ohms	
FINISHED BOARD THICKNESS +/−10%					1.58 mm

*100 Ohms = Differential Impedance





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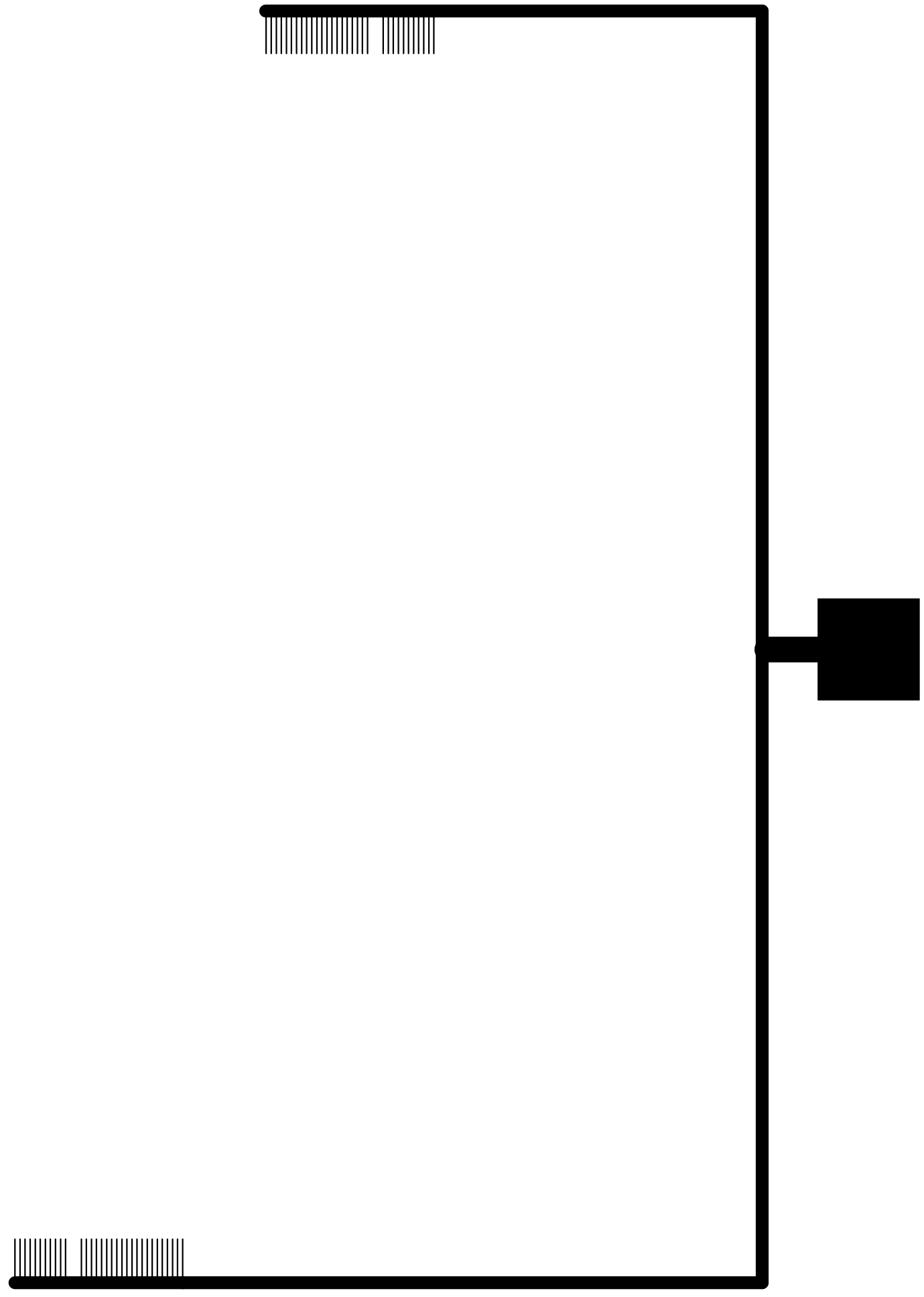
SCALE: 1.00 Print Date 23/08/2011 14:54:59

Components	667
Pins	2704
Vias	2131
Holes	2211
Nets	467



Number	EDA-02189-V4
Drawn By	GK/TW
DATE	18-07-2011
CHECK BY	CEGELEC NL (23-08-2011)

SCALE: 1.00 Print Date 23/08/2011 15:05:19