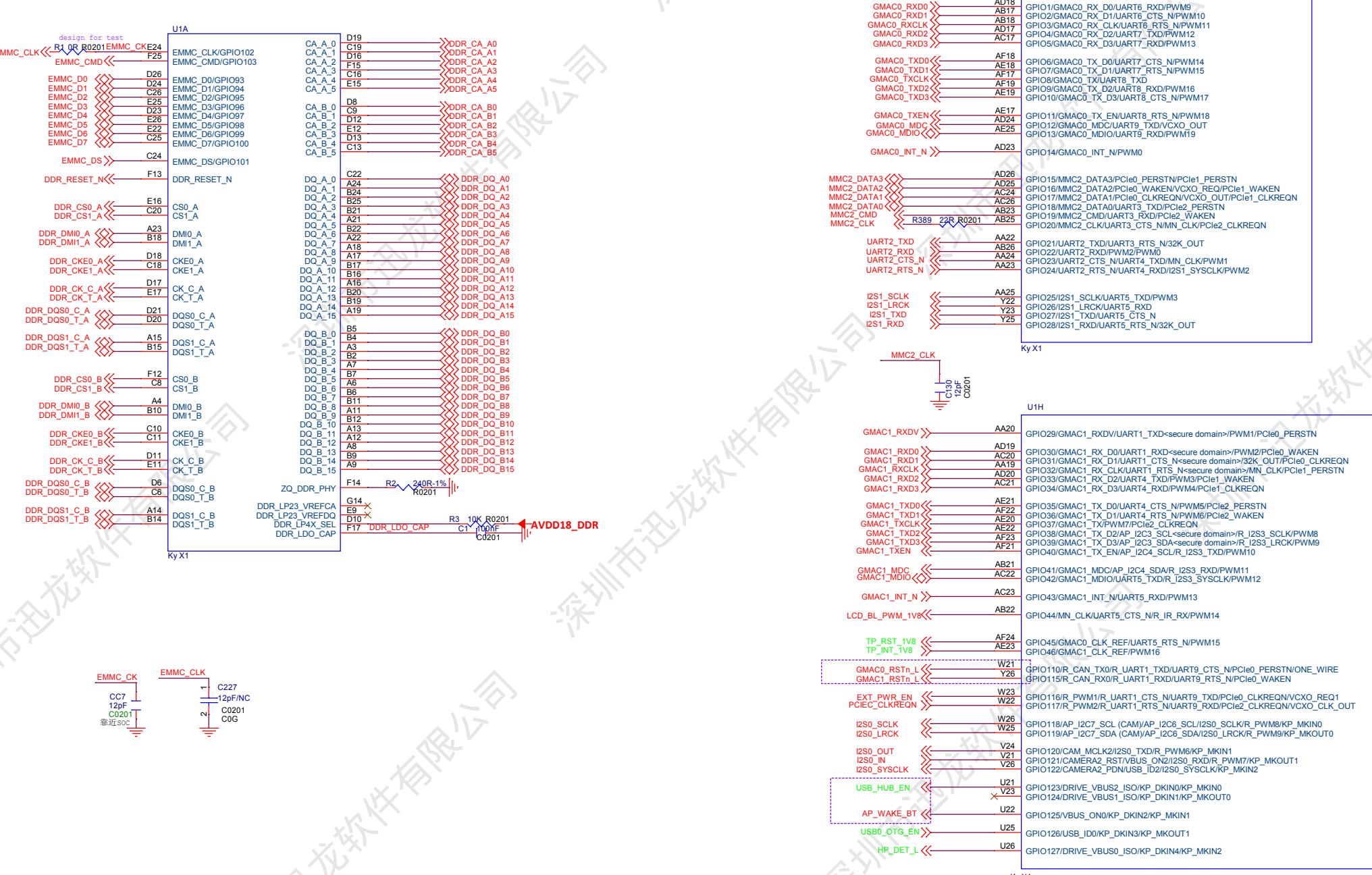
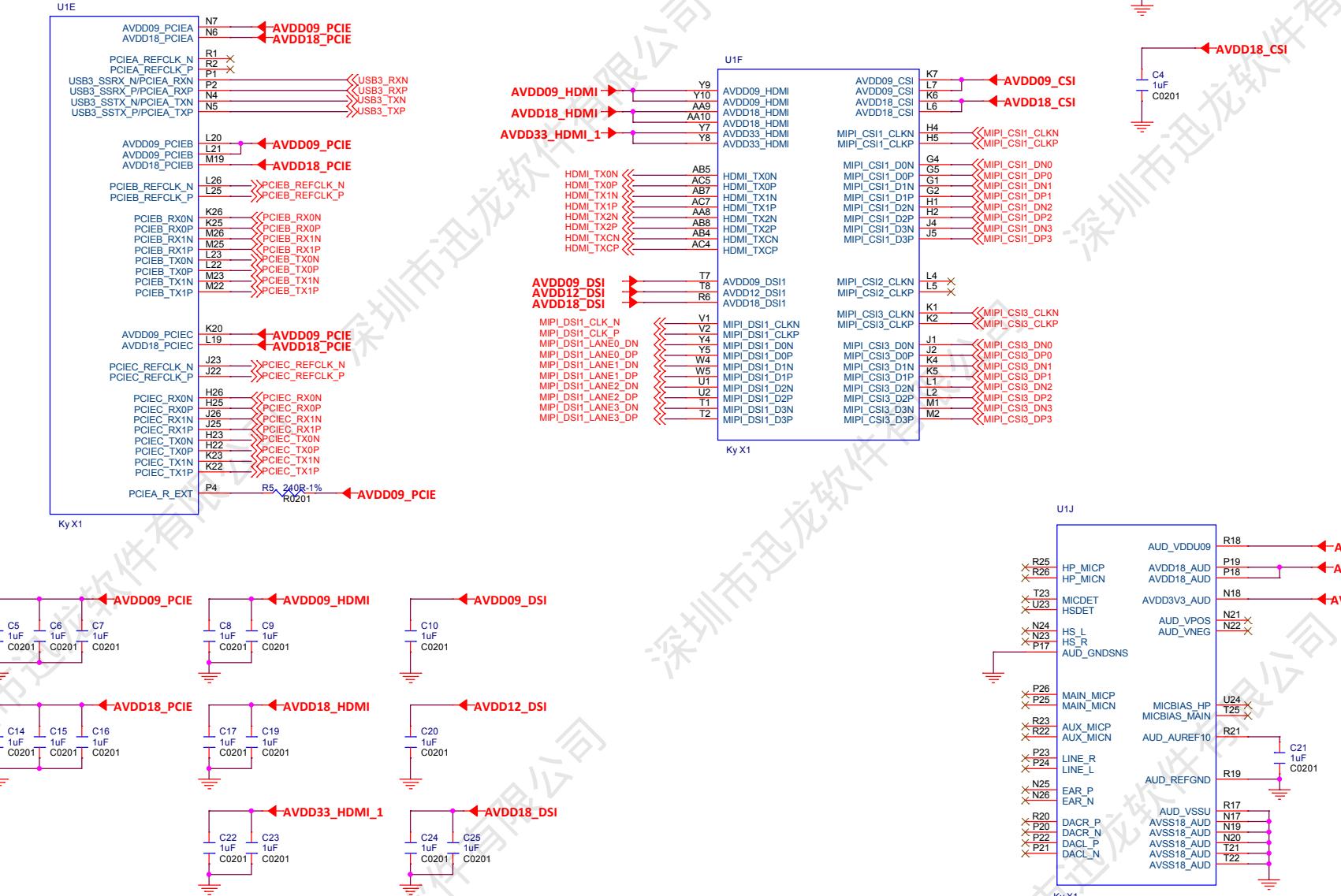


eMMC+DDR

GPIO

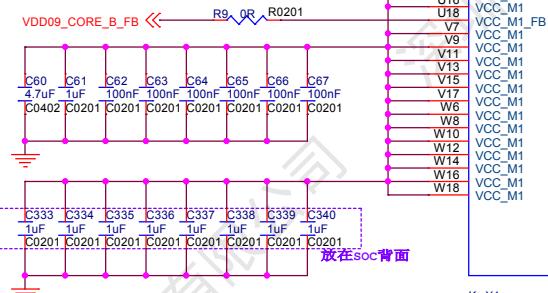
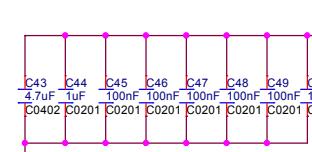
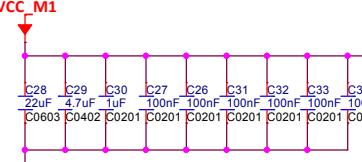


Periphery

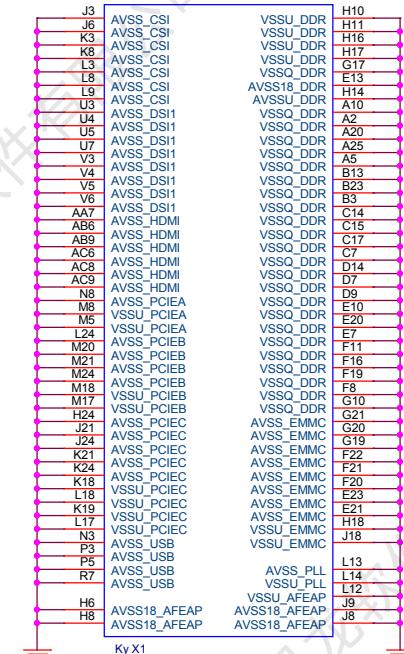
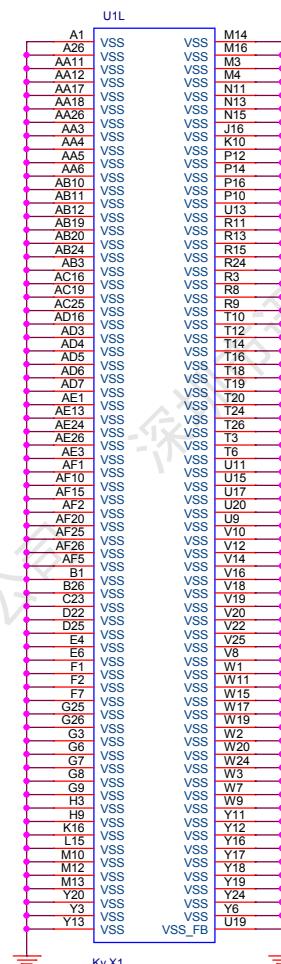
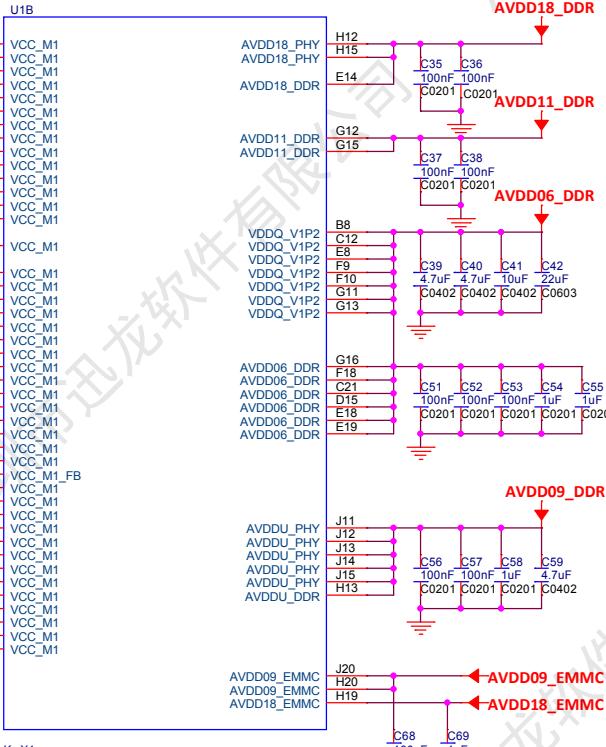


Shenzhen Xunlong Software Co., Ltd				
Project:	OPI RV2			
File:	02 CPU-Periphery			
Date:	Wednesday, May 07, 2025	Rev:	V1.1	
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CPU-PWR

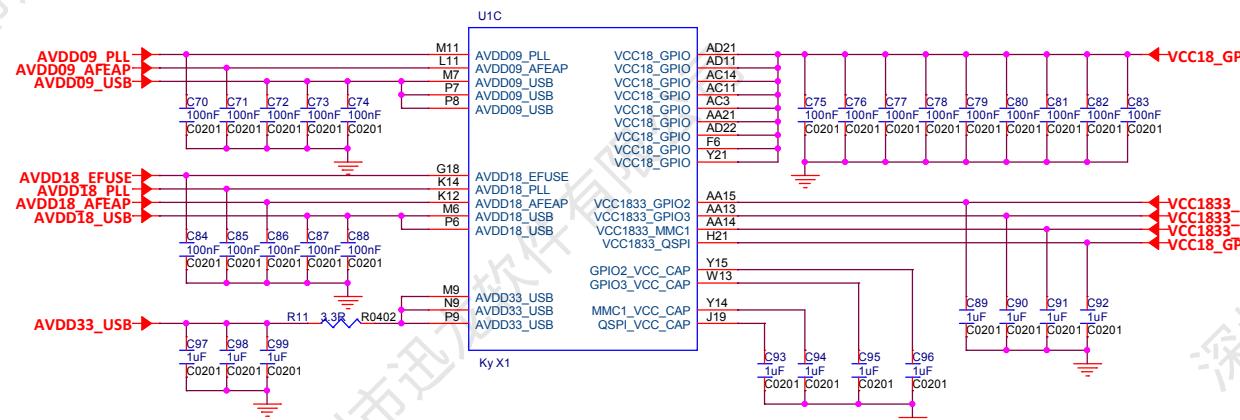


放在SOC背面



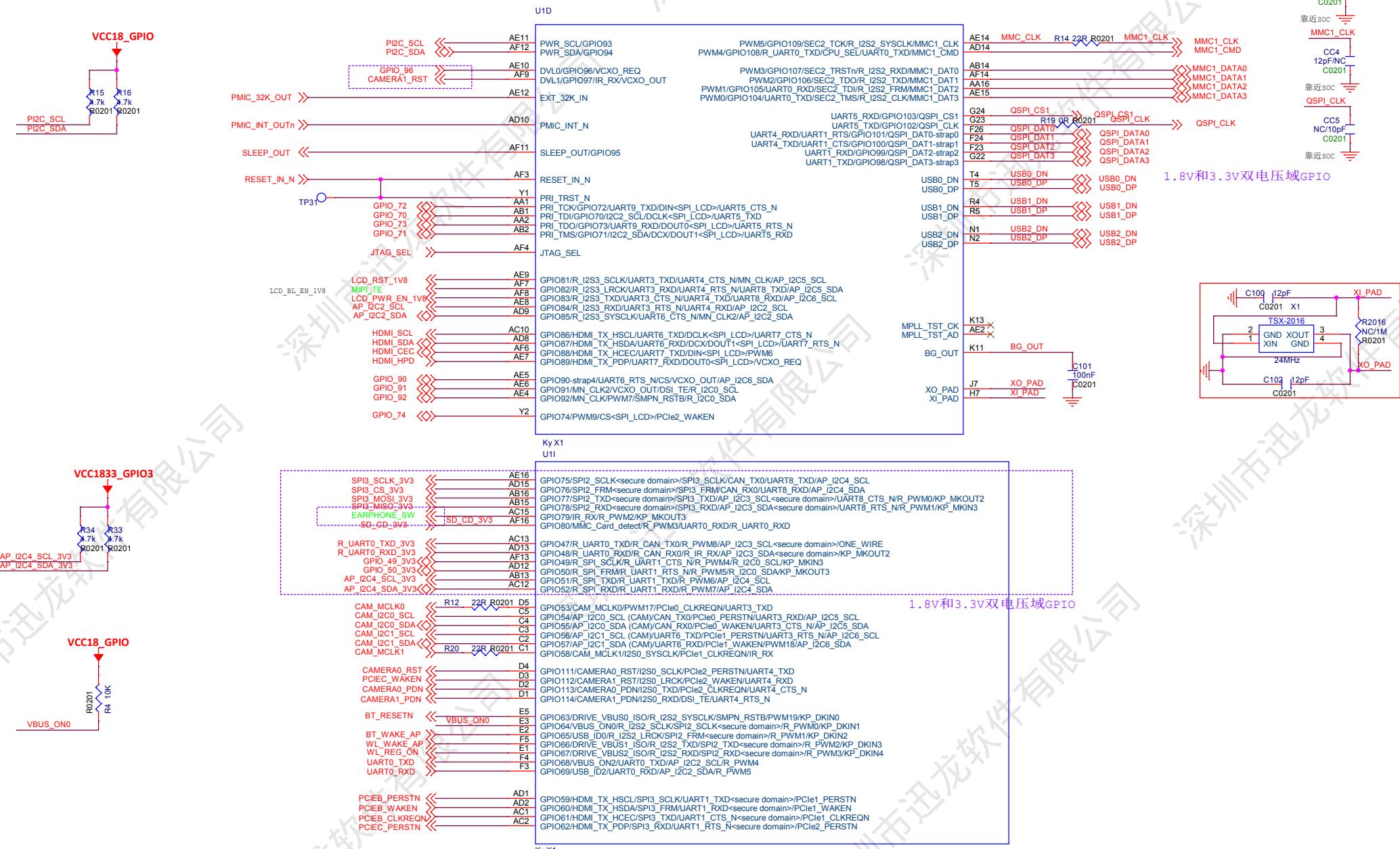
Ky X1

Ky X1



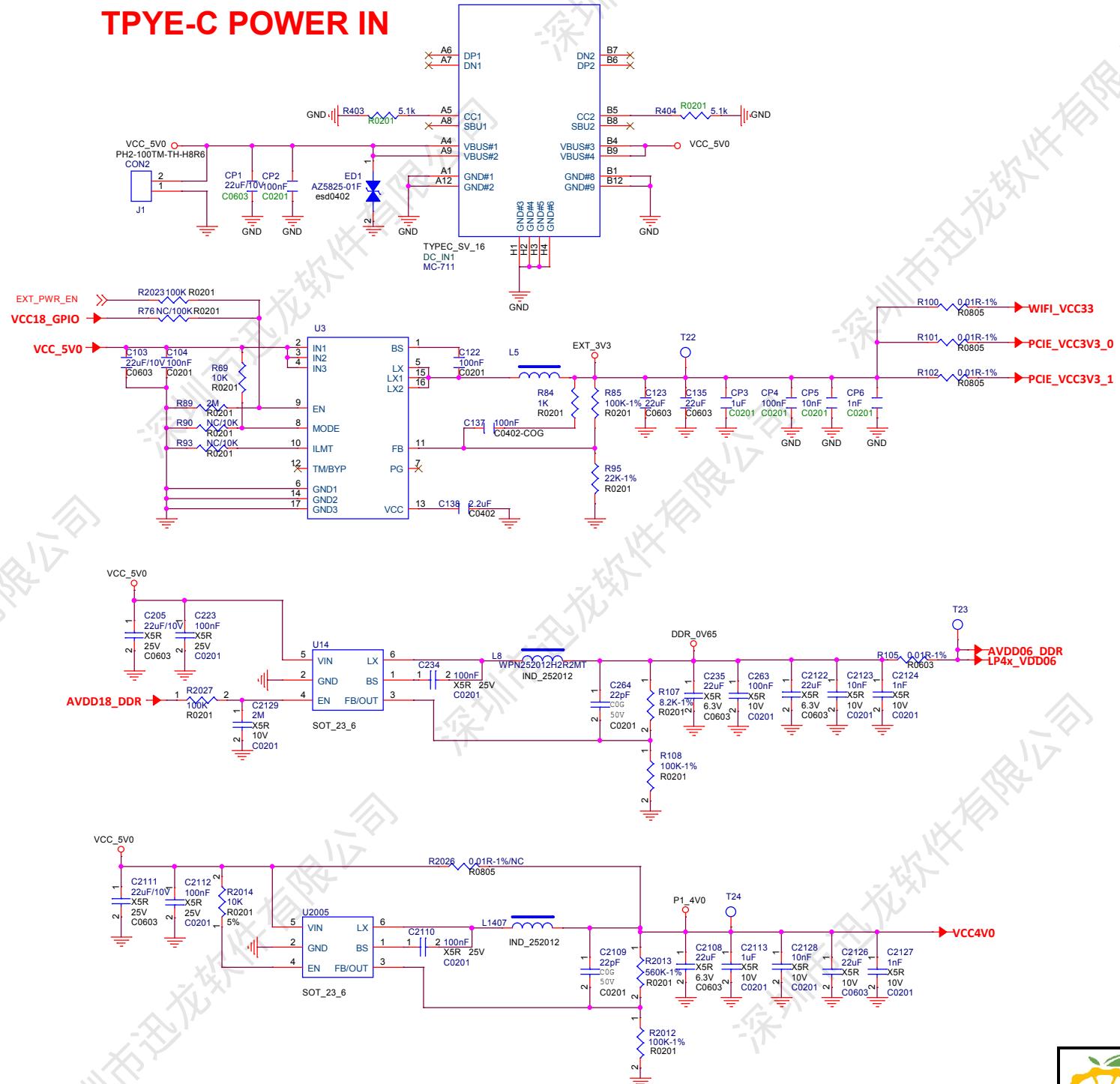
Shenzhen Xunlong Software Co., Ltd	
Project:	OPI RV2
File:	03 CPU-PWR
Date:	Wednesday, May 07, 2025
Size:	A3 Sheet: 3 Of: 20

SYS-GPIO

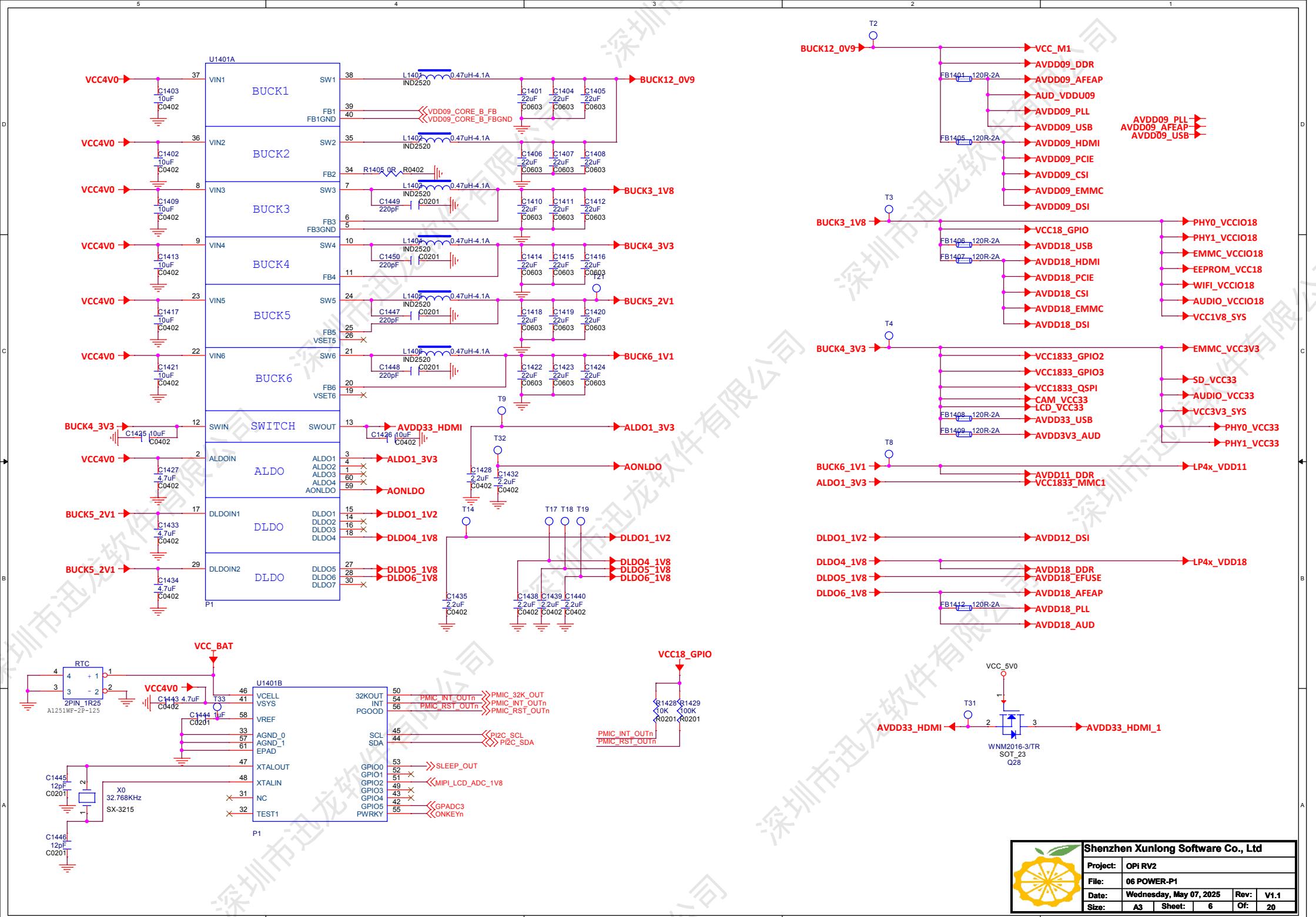


Shenzhen Xunlong Software Co., Ltd					
Project:	OPI RV2				
File:	04 CPU-SYS-GPIO				
Date:	Wednesday, May 07, 2025			Rev:	V
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TPYE-C POWER IN

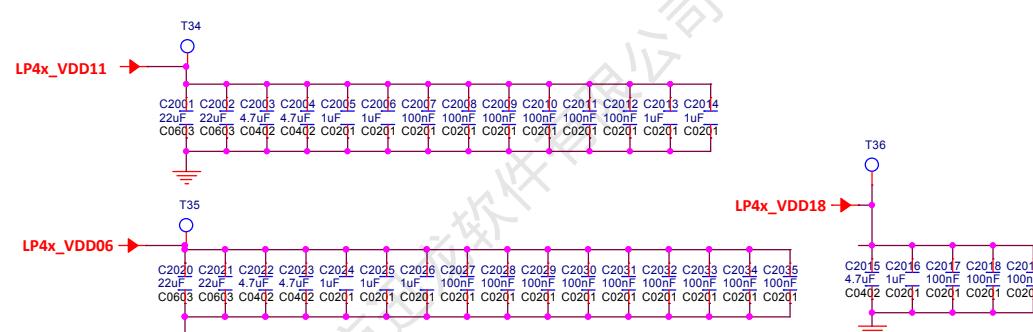
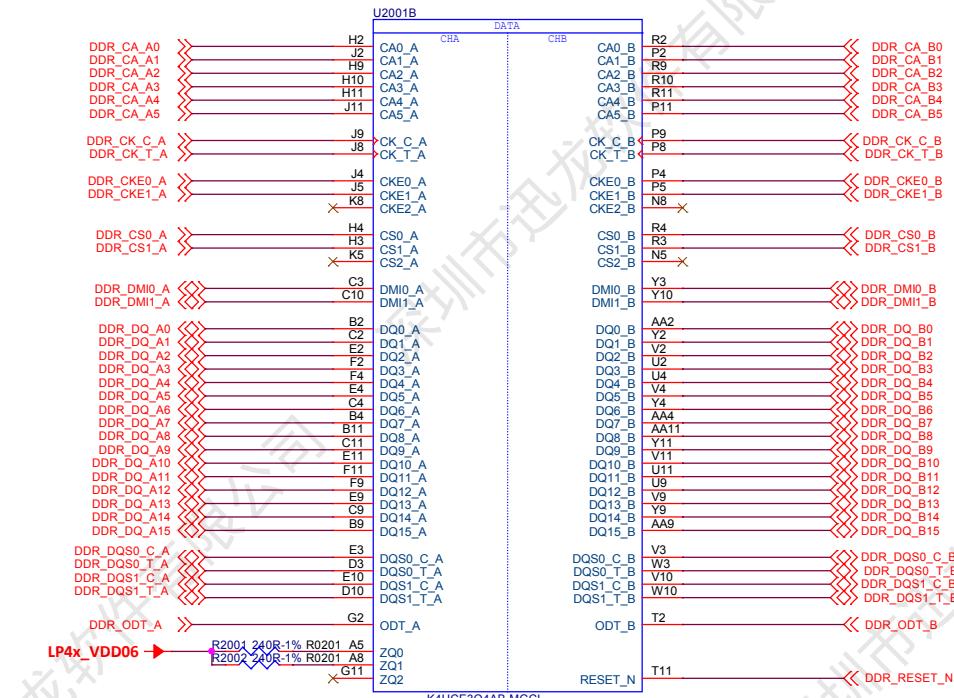
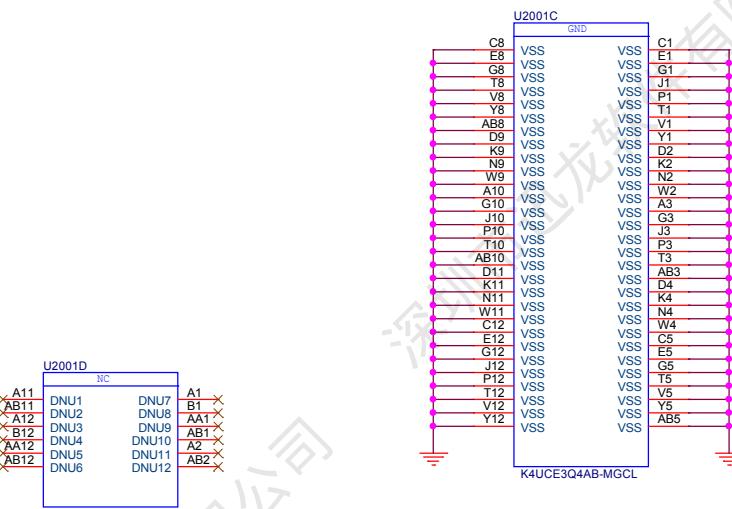


Shenzhen Xunlong Software Co., Ltd	
Project:	OPI RV2
File:	05 POWER-EXT DCDC
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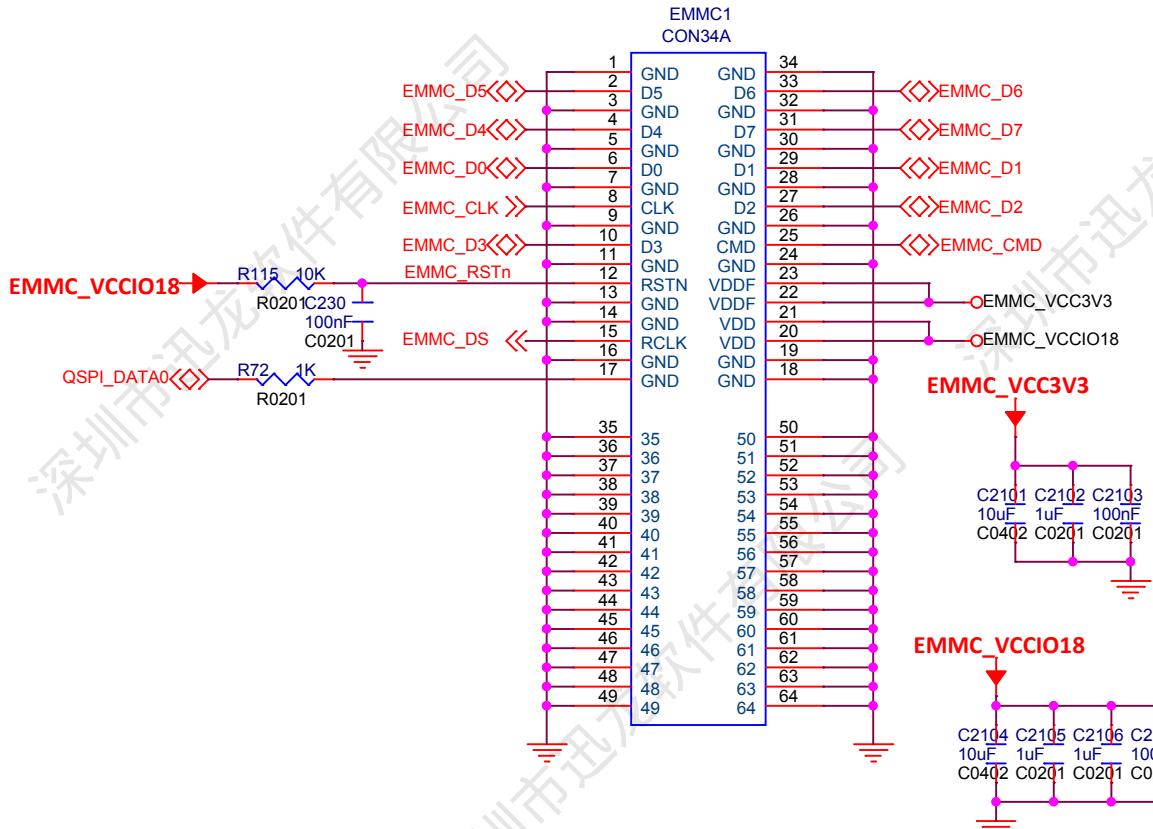
LPDDR4X

NOTE : CKE2 / CS2 / ZO2 NC IN SAMSUNG

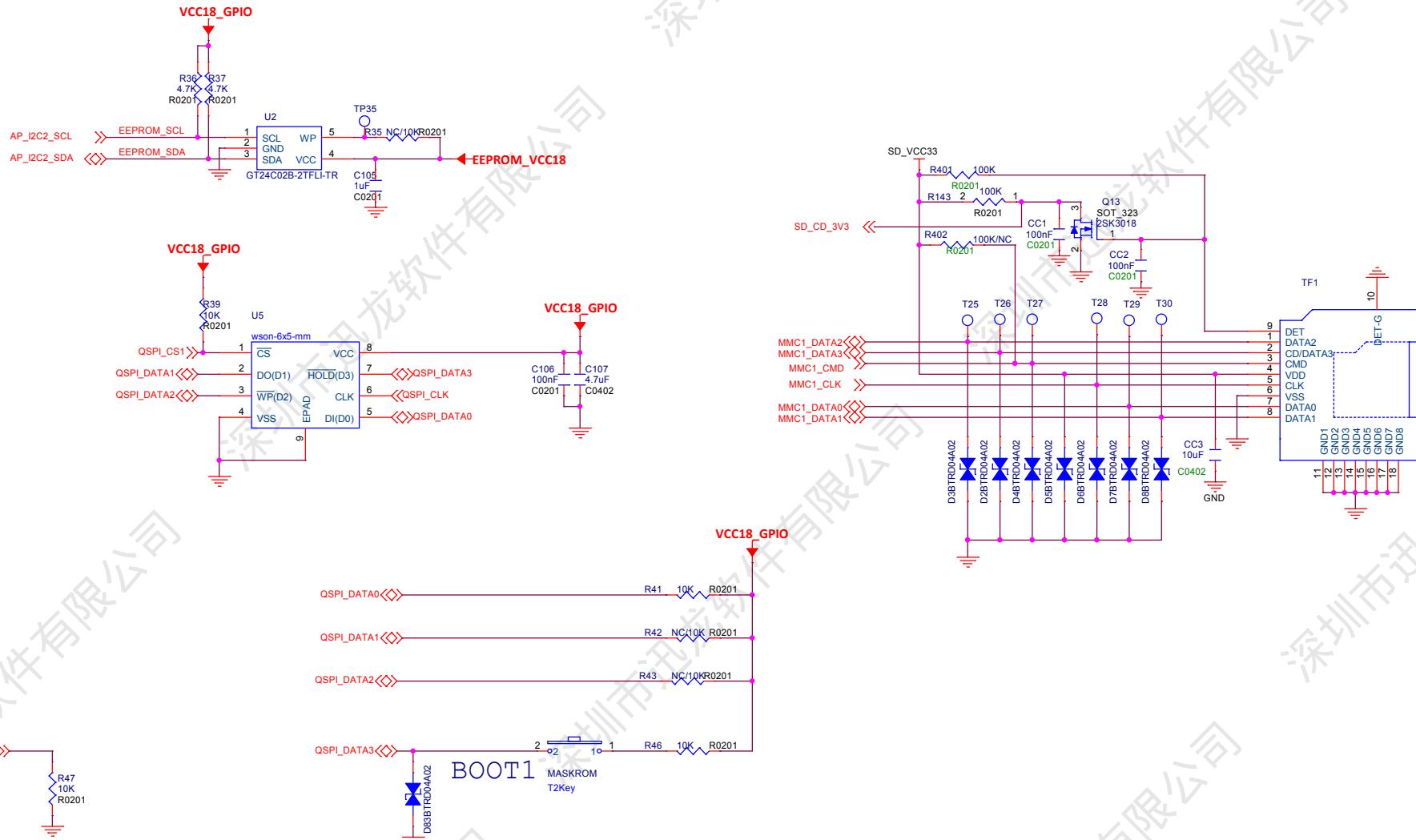


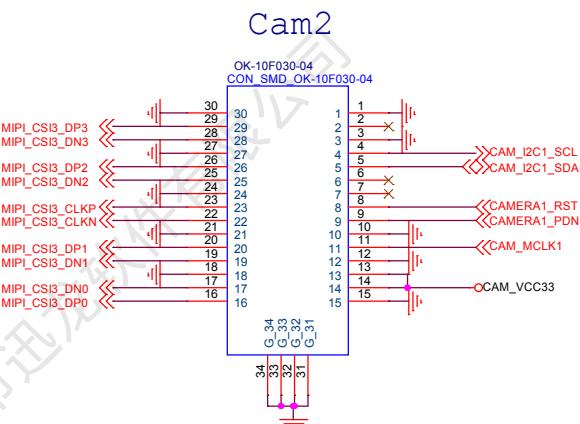
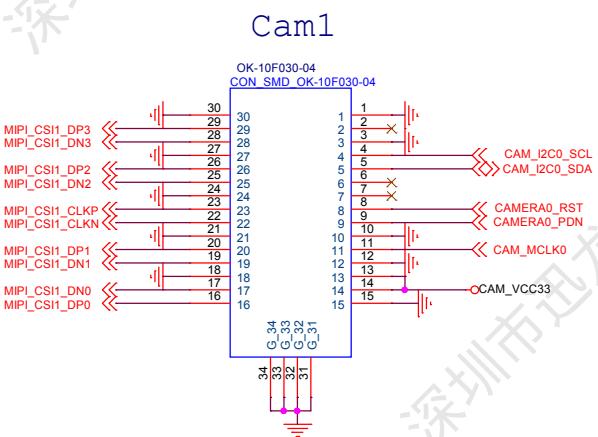
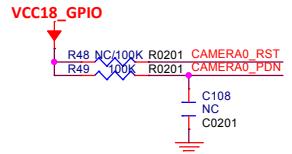
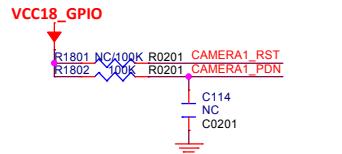
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Project:	OPI RV2				
File:	07 Memory-LPDDR4x				
Date:	Wednesday, May 07, 2025		Rev:	V1.1	
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EMMC

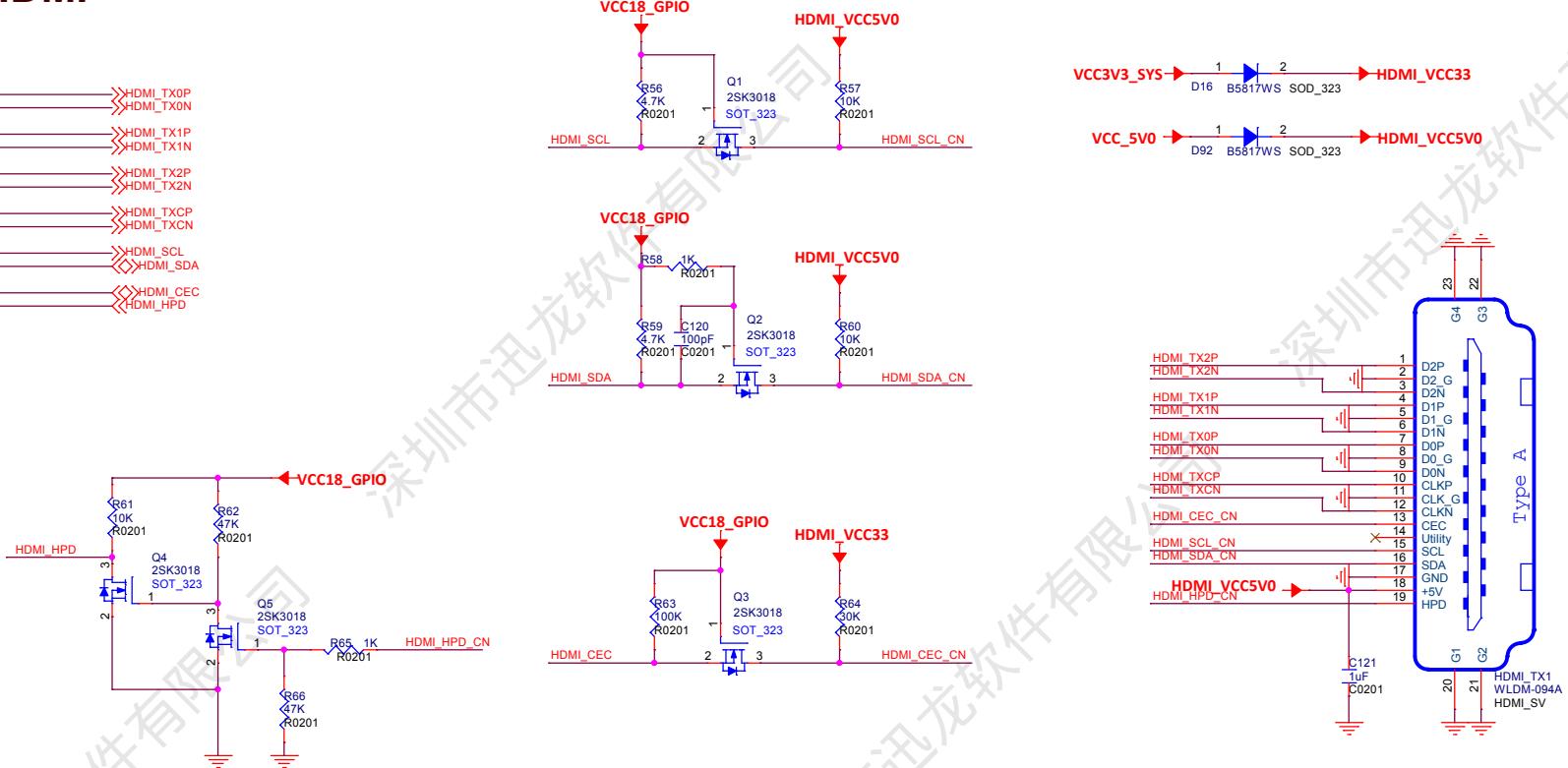


Shenzhen Xunlong Software Co., Ltd					
Project:		OPI RV2			
File:		08 Storage-eMMC			
Date:	Wednesday, May 07, 2025	Rev:	V1.1		
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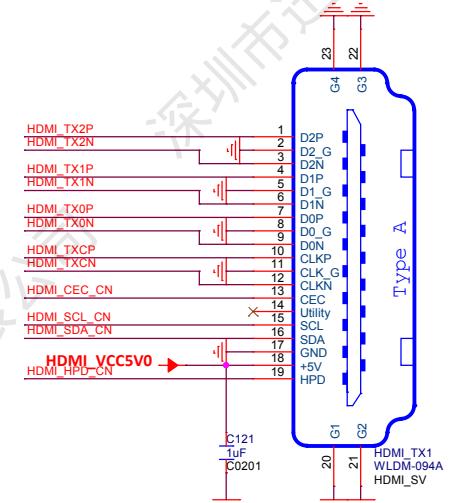


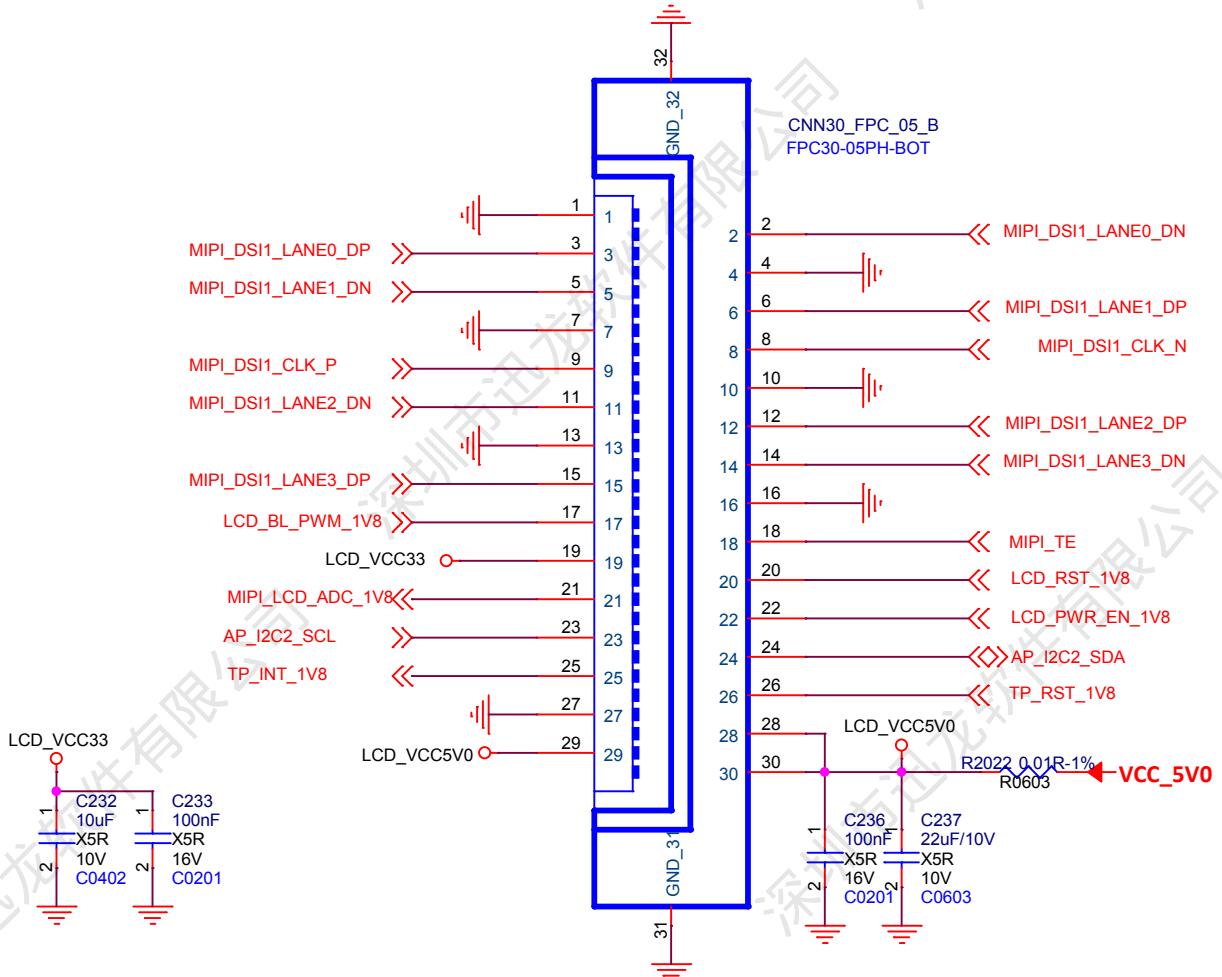
HDMI



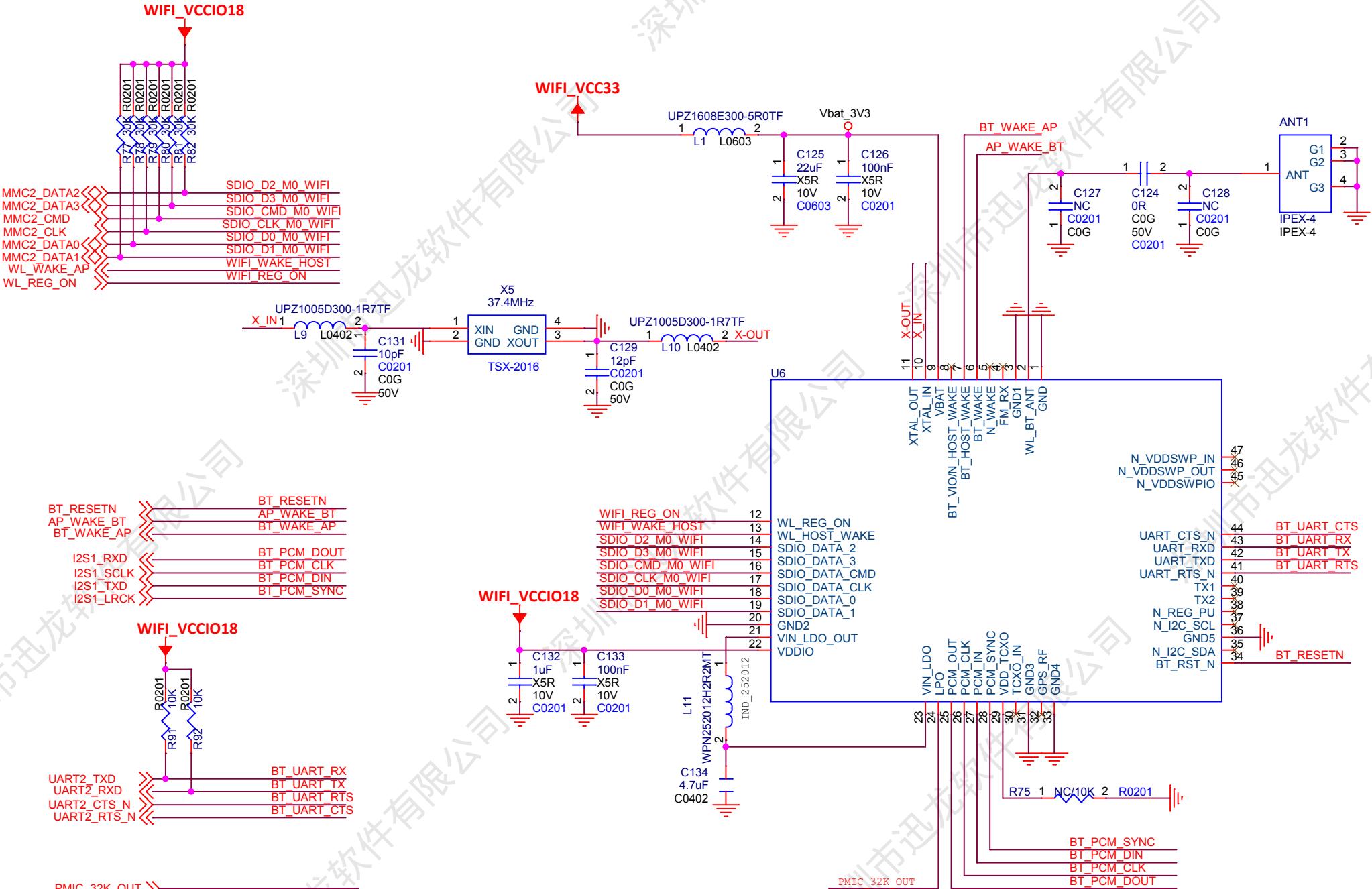
VCC3V3_SYS → 1 D16 B5817WS SOD_323 → HDMI_VCC33

VCC_5V0 → 1 D92 B5817WS SOD_323 → HDMI_VCC5V0

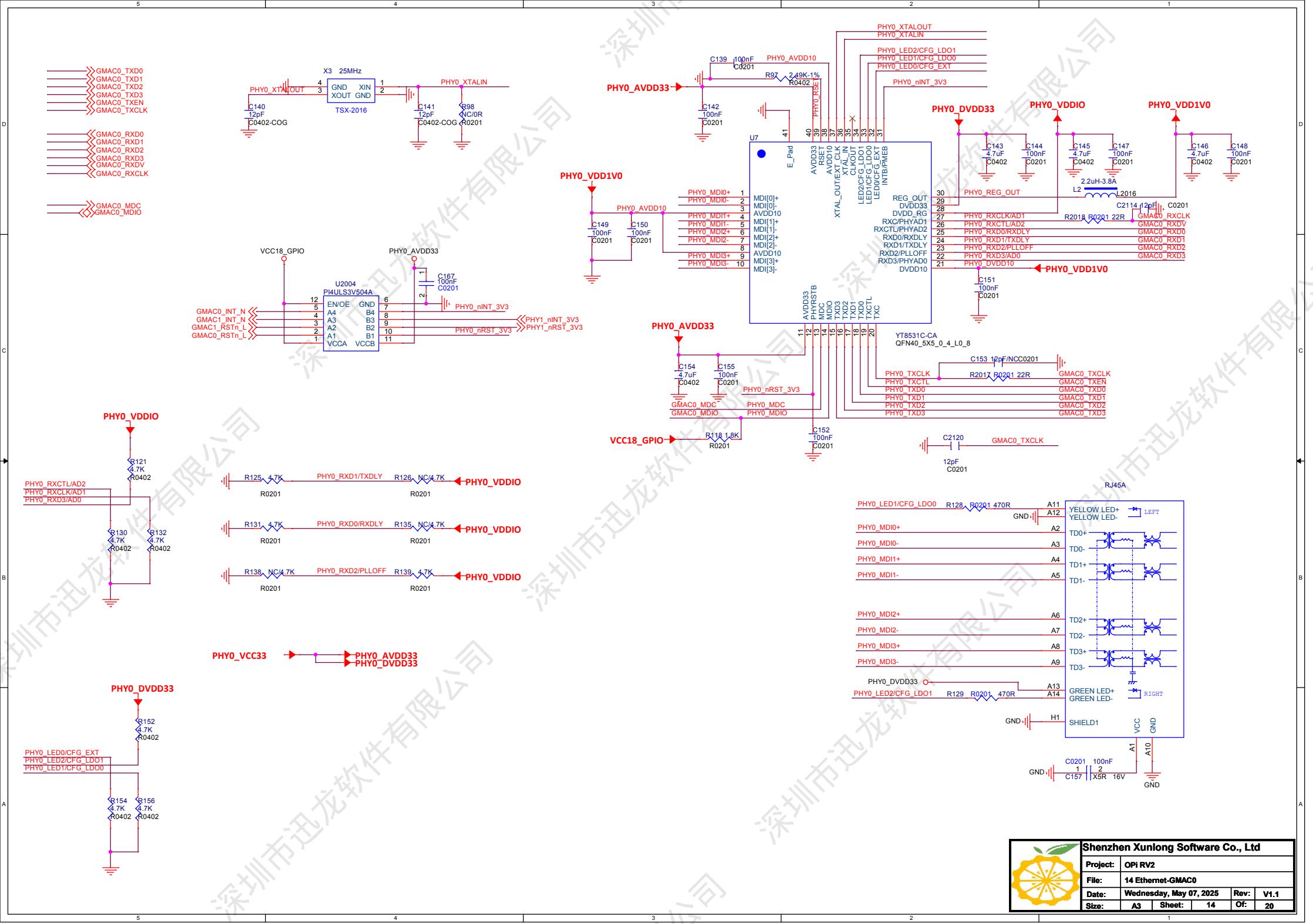


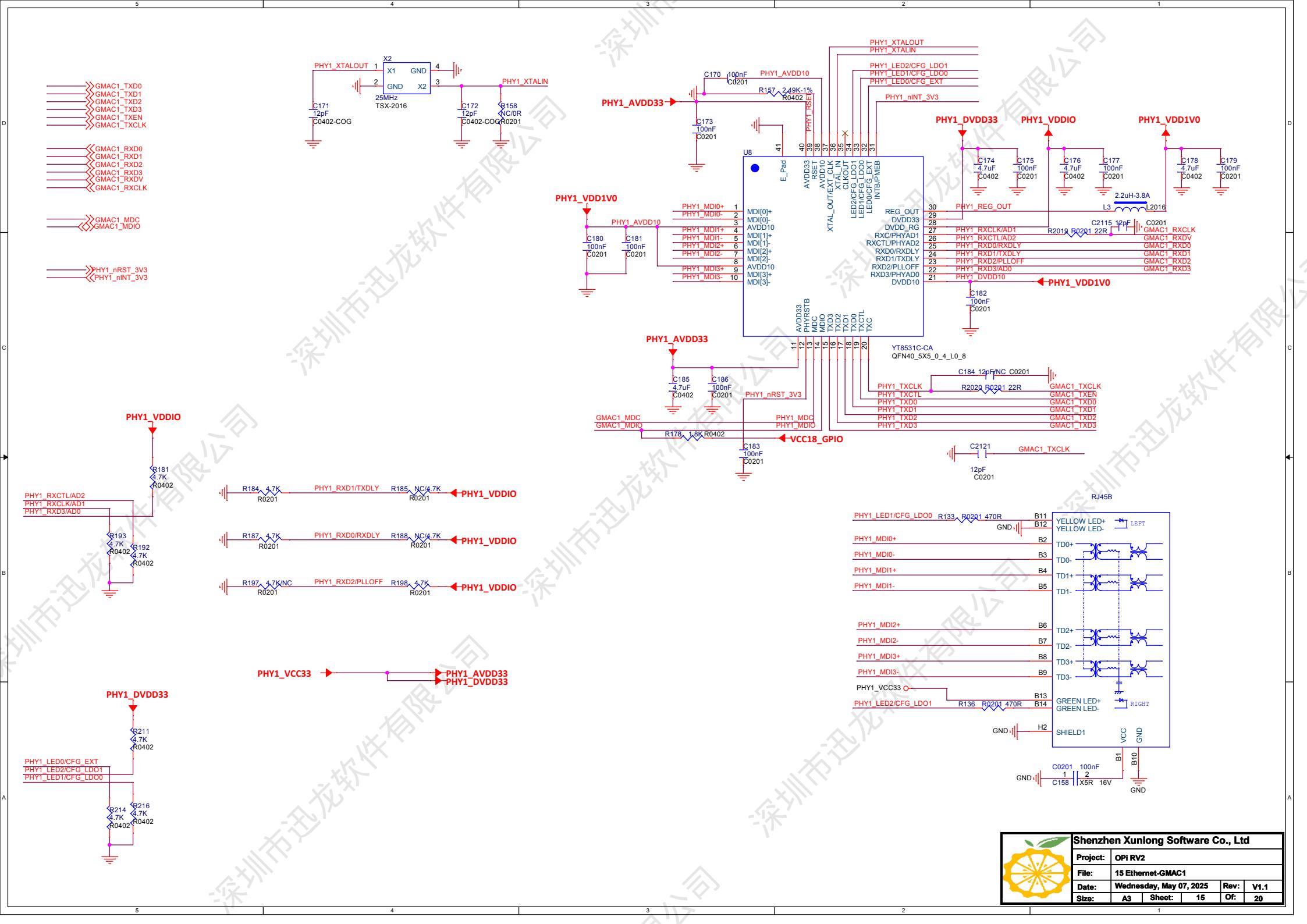


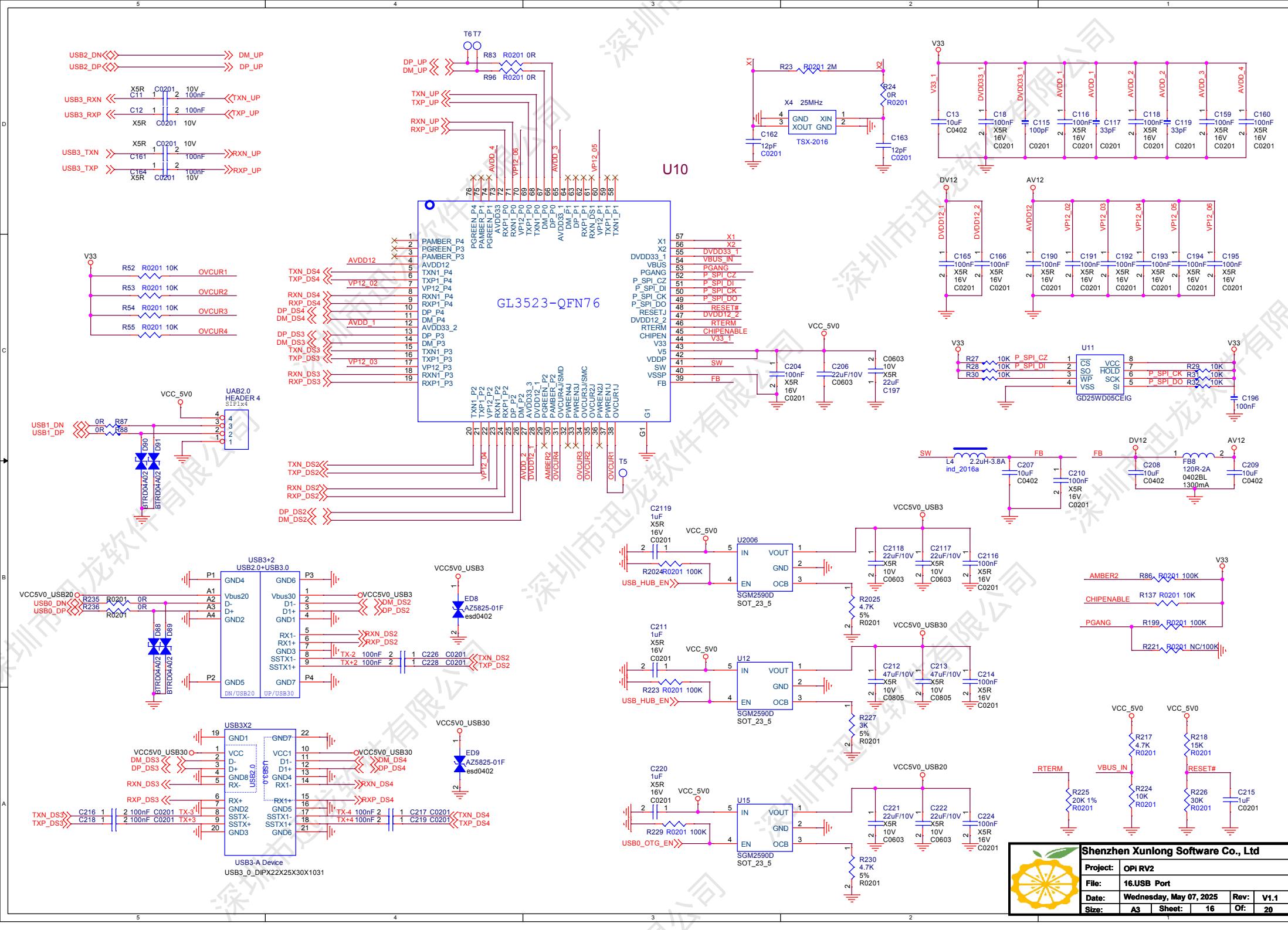
Shenzhen Xunlong Software Co., Ltd			
Project:	OPI RV2		
File:	12 VO-MIPI DSI		
Date:	Wednesday, May 07, 2025	Rev:	V1.1
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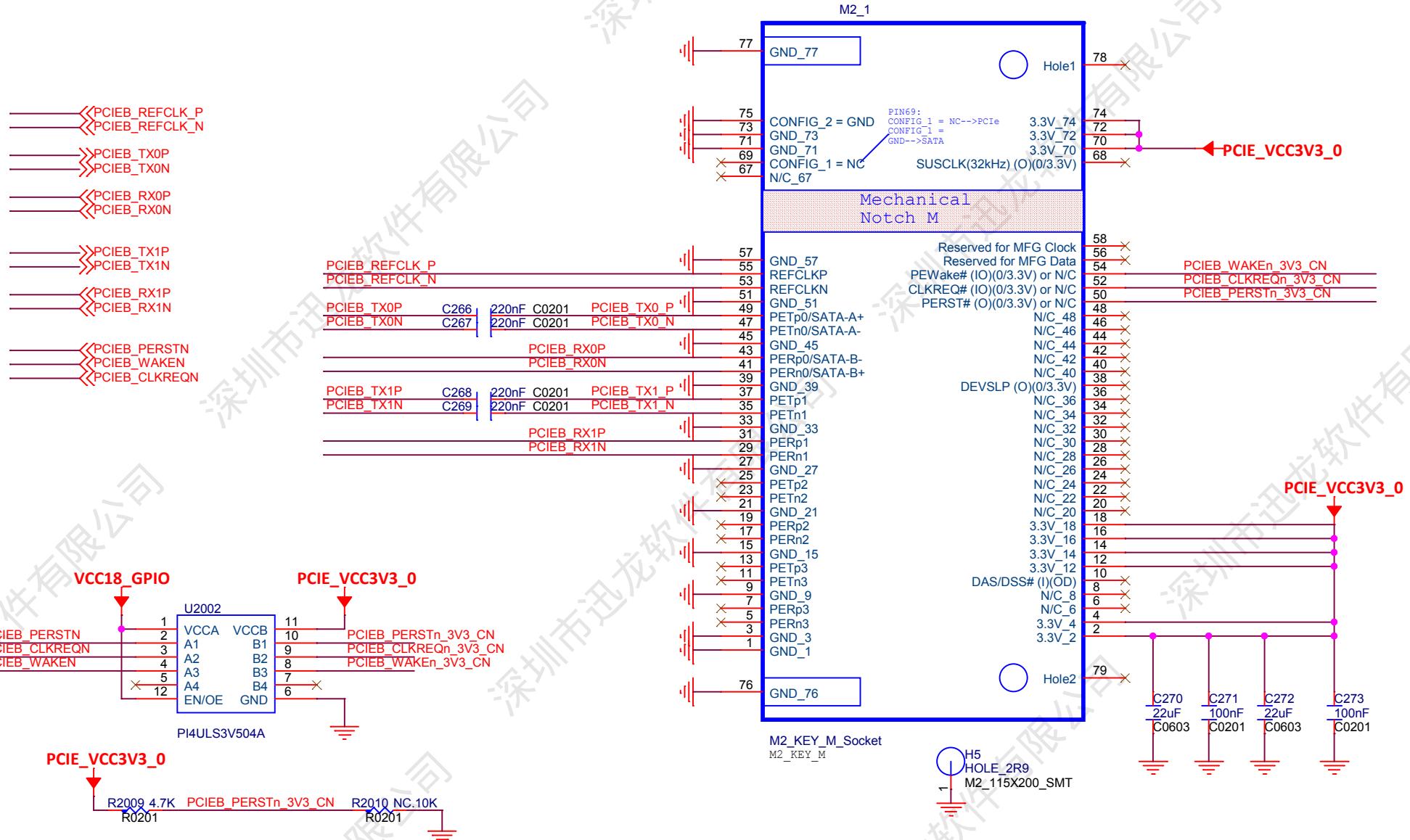


Shenzhen Xunlong Software Co., Ltd			
Project:	OPI RV2		
File:	13 WiFi/BT-SDIO_2T2R/UART		
Date:	Wednesday, May 07, 2025	Rev:	V1.1
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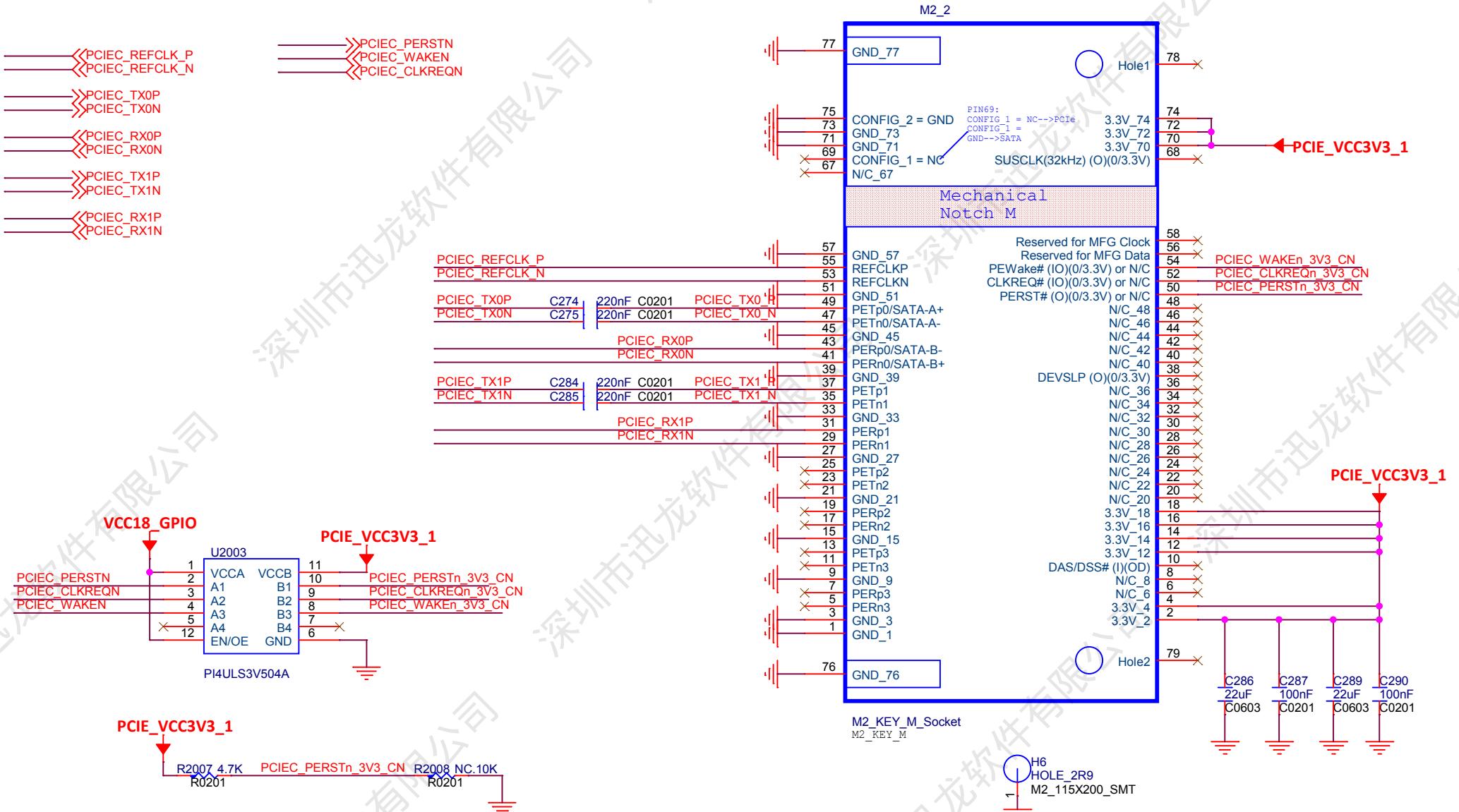








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File:	17 PCIEB M.2 KEY M				
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Shenzhen Xunlong Software Co., Ltd				
Project:	OPI RV2			
File:	18 PCIE M.2 KEY M			
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CODEC ES8388

