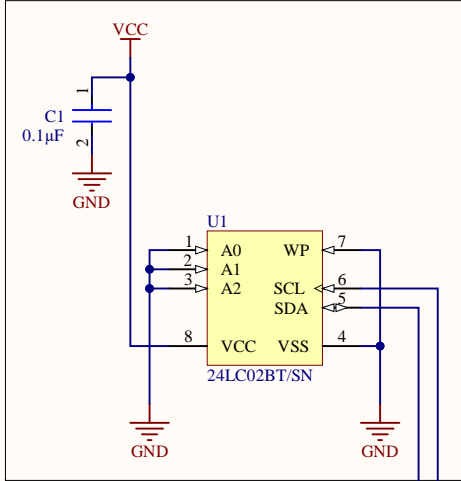


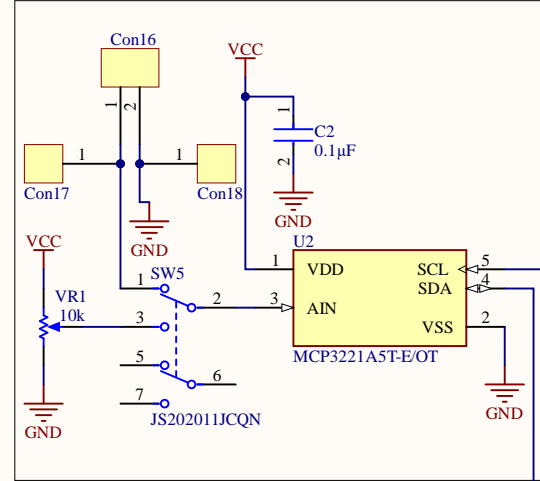
I2C-EEPROM

(Slave Address = 0xA0)



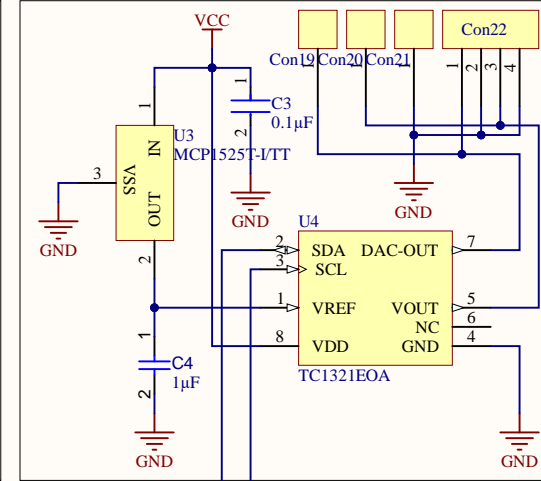
I2C-12-Bit-ADC

(Slave Address = 0x9A)

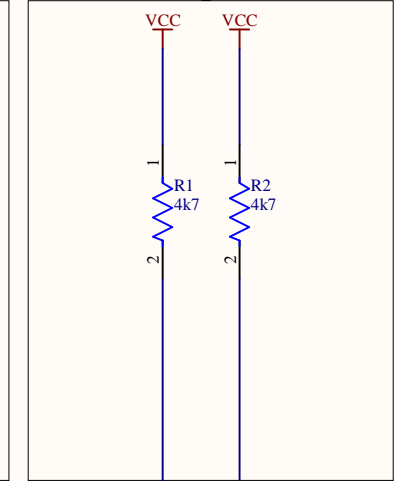


I2C-10-Bit-DAC

(Slave Address = 0x90)

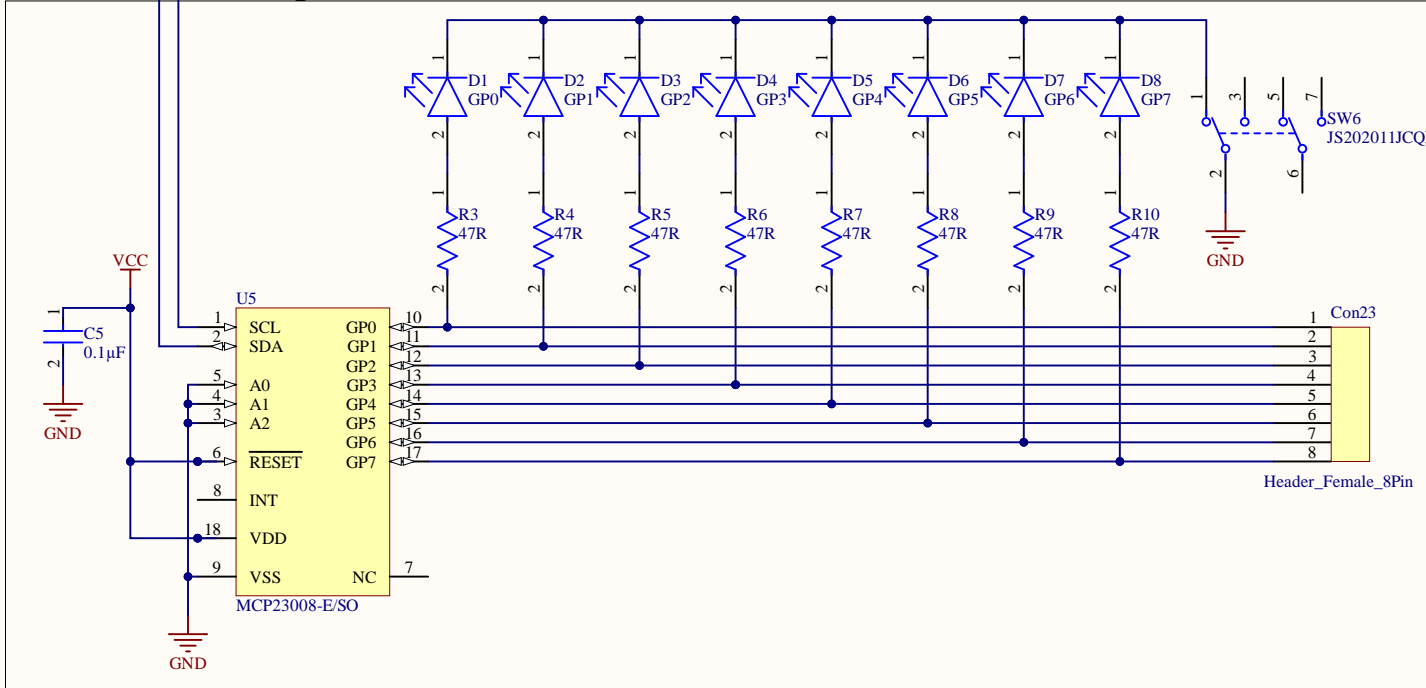


I2C-PullUps



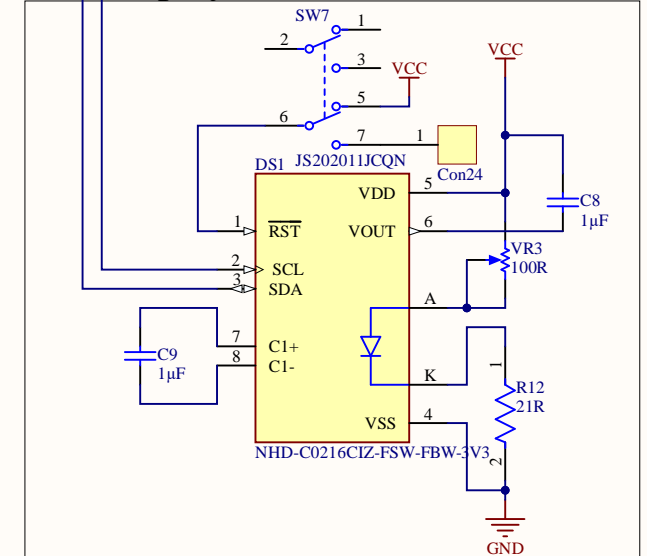
I2C-8-Bit-I/O-Expander

(Slave Address = 0x40)



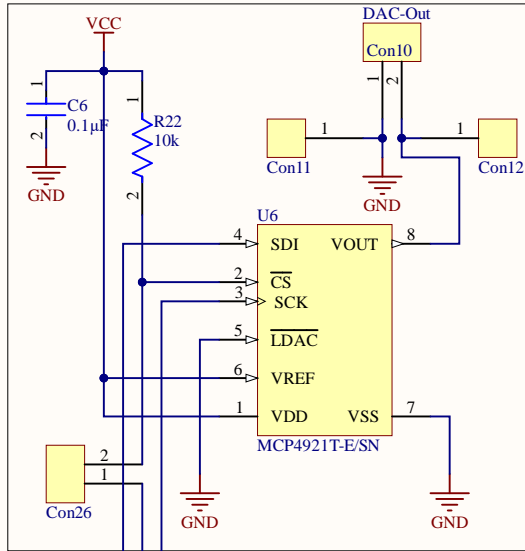
I2C-Display

(Slave Address = 0x7C)

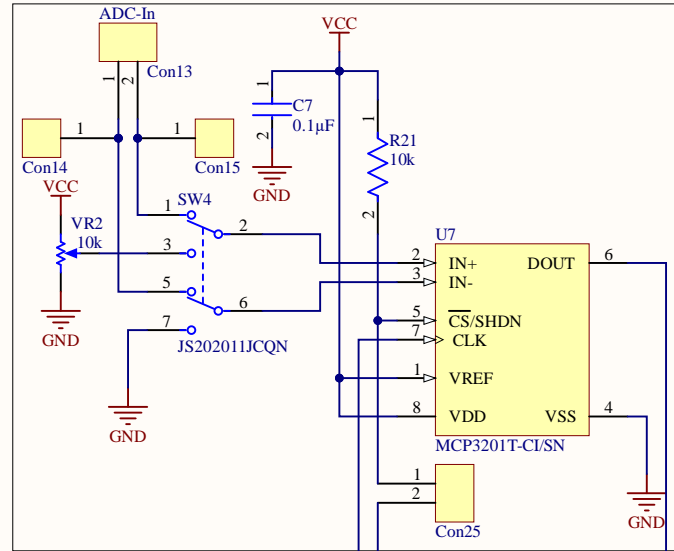


Title			
ESP32-Devboard			
Size	Number	Revision	
A4	I2C	V1	
Date:	6.13.2023	Sheet 2 of	4
File:	S:\Schule\...\ESP32-HWE-Dev-I2C.SchDpDrawn By: J.M.		

SPI-12-Bit-DAC



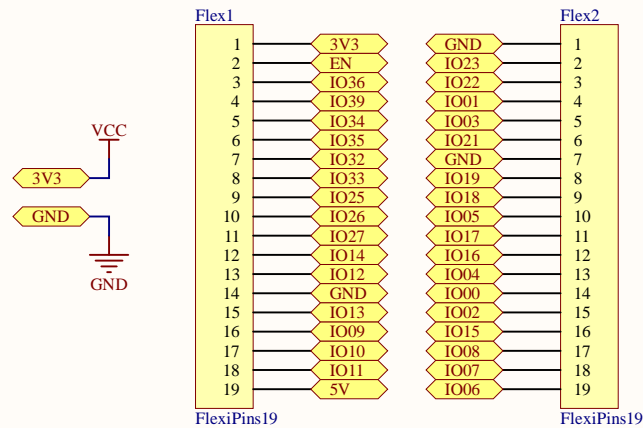
SPI-12-Bit-ADC



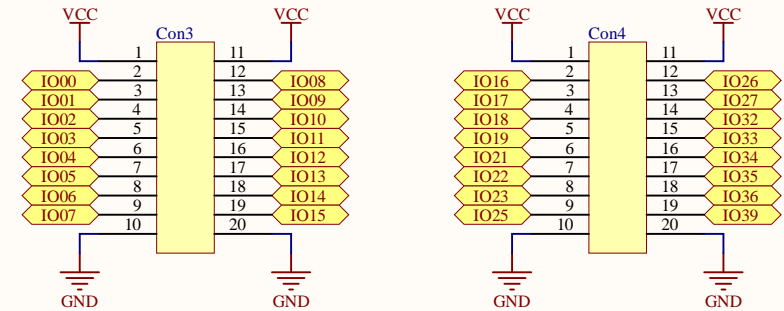
MISO > ESP32-HWE-Dev[2C]
 CLK > ESP32-HWE-Dev[2D]
 CS > ESP32-HWE-Dev[2D]
 MOSI > ESP32-HWE-Dev[2D]

Title		
ESP32-Devboard		
Size	Number	Revision
A4	SPI	V1
Date:	6.13.2023	Sheet 3 of 4
File:	S:\Schule\...\ESP32-HWE-Dev-SPI.SchDpDrawn By: J.M.	

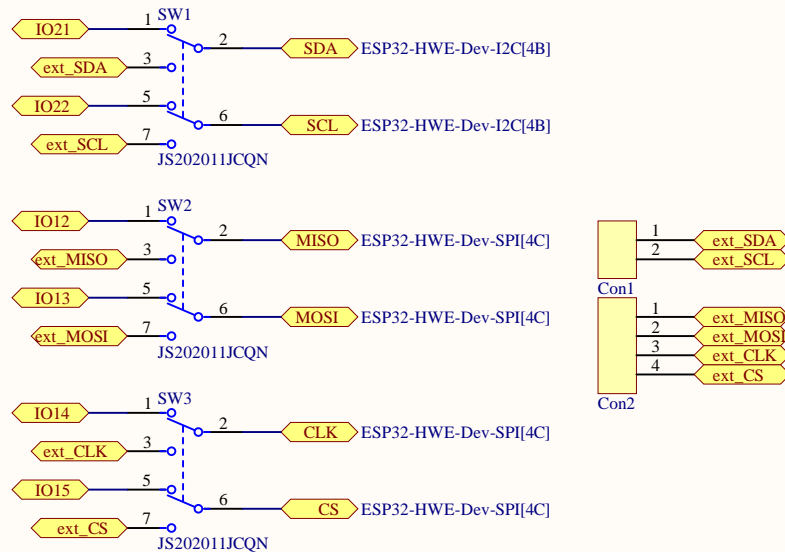
ESP32 Connection



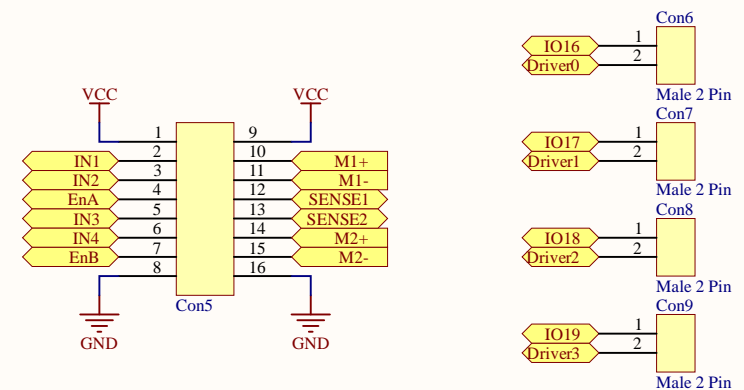
GPIO-Header



HW-Interface Connections

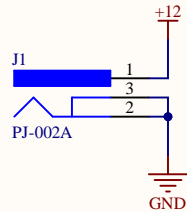


Driver Connections

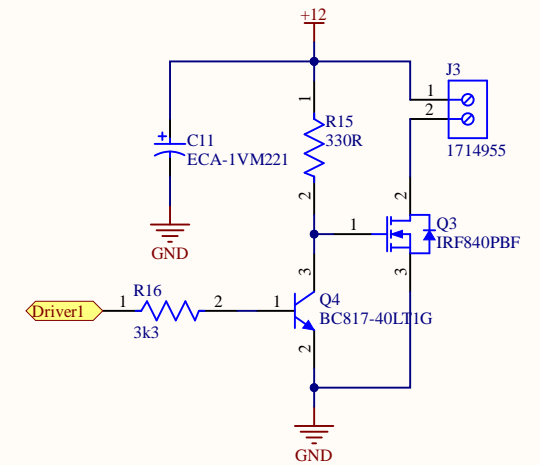
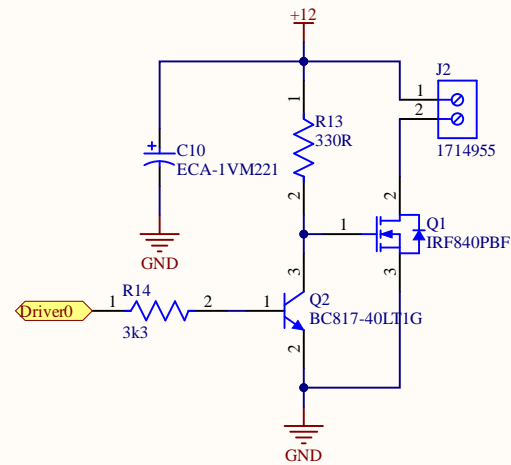


Title			
ESP32-Devboard			
Size	Number	Revision	
A4	Connections	V1	
Date:	6.13.2023	Sheet 1 of	4
File:	S:\Schule\...\ESP32-HWE-Dev.SchDoc	Drawn By:	J.M.

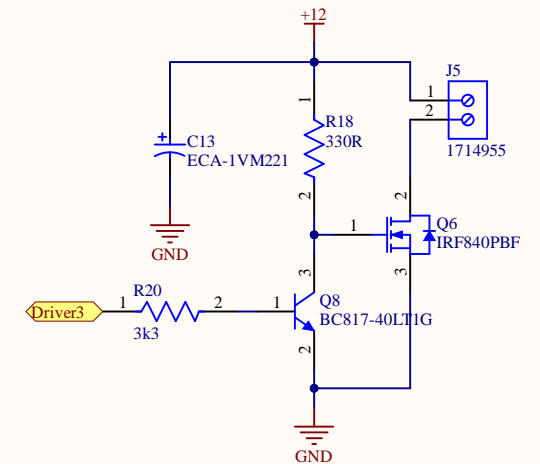
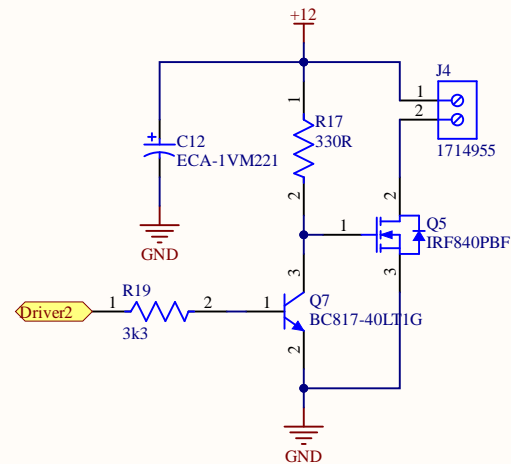
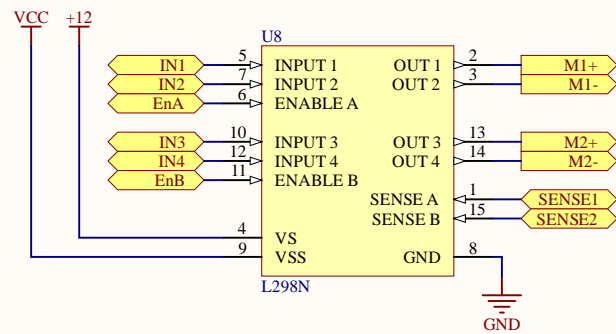
12V-Supply



4x inverted FET-Driver



Double Fullbridge-Driver (L298N)



Title		
ESP32-Devboard		
Size	Number	Revision
A4	Power	V1
Date:	6.13.2023	Sheet 4 of 4
File:	S:\Schule\...ESP32-HWE-Dev-Power.Sch	Drawn By: J.M.

