

650V 0.96Ω Super Junction Power MOSFET

Description

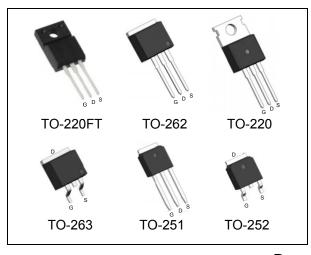
WMOSTM C4 is Wayon's 4th generation super junction MOSFET family that is utilizing charge balance technology for extremely low on-resistance and low gate charge performance. WMOSTM C4 is suitable for applications which require superior power density and outstanding efficiency.

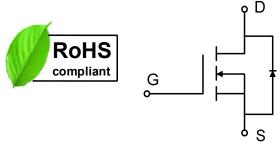
Features

- V_{DS} =700V @ T_{j,max}
- Typ. $R_{DS(on)} = 0.96\Omega$
- 100% UIS tested
- Pb-free plating, Halogen free



LED Lighting, Charger, Adapter, PC, LCD TV, Server





Absolute Maximum Ratings

Parameter	Symbol	WMK/WMM/WMN/WMP/WMO	WML	Unit
Drain-source voltage	V_{DSS}	650		V
Continuous drain current ¹⁾ $(T_C = 25^{\circ}C)$	I _D	5		Α
(T _C = 100°C)		2.8		Α
Pulsed drain current ²⁾	I _{DM}	9		Α
Gate-source voltage	V_{GS}	±30		V
Avalanche energy, single pulse ³⁾	E _{AS}	15		mJ
Avalanche energy, repetitive ²⁾	E _{AR}	0.1		mJ
Avalanche current, repetitive ²⁾	I _{AR}			Α
Power dissipation (T _C = 25°C)	P _D	42	23	W
- Derate above 25°C		0.34	0.18	W/°C
Operating and storage temperature range	T _j , T _{stg}	T _i , T _{stg} -55 to +150		°C
Continuous diode forward current	I _S	5		А
Diode pulse current	I _{S,pulse}	e 9		А

Thermal Characteristics

Parameter	Symbol	WMK/WMM/WMN/WMP/WMO	WML	Unit
Thermal resistance, junction-to-case	R _{eJC}	3	5.4	°C/W
Thermal resistance, junction-to-ambient	$R_{ heta JA}$	62	80	°C/W



Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics		T		T	T	T
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	650	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =0.25mA	2	3	4	V
Drain cut-off current	I_{DSS}	V _{DS} =650 V, V _{GS} =0V,				μA
		T _j = 25°C	-	-	1	
		T _j = 125°C	-	30	-	
Gate leakage current, forward	I _{GSSF}	V _{GS} =20V, V _{DS} =0V	-	-	100	nA
Gate leakage current, reverse	I_{GSSR}	V _{GS} =-20V, V _{DS} =0V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =1A	-			
		T _j = 25°C	-	0.96	1.14	Ω
Dynamic characteristics						
Input capacitance	C_iss	V _{DS} = 100V, V _{GS} = 0V,	-	230	-	
Output capacitance	Coss	f = 1 MHz	-	9.9	-	pF
Reverse transfer capacitance	C_{rss}		-	0.8	_	
Turn-on delay time	$t_{d(on)}$	V _{DD} = 300V, I _D = 2A	-	7	-	
Rise time	t _r	$R_G = 25\Omega$, $V_{GS}=10V$	-	7	-	ns
Turn-off delay time	t _{d(off)}		-	34	-	
Fall time	t _f		-	8	-	
Gate charge characteristics						
Gate to source charge	Q_{qs}	V _{DD} =480V, I _D =2A,	-	1.2	-	
Gate to drain charge	Q_{gd}	V _{GS} =0 to 10V	-	2.2	-	nC
Gate charge total	Q_g		-	5.2	-	
Gate plateau voltage	V _{plateau}		-	5.2	-	V
Reverse diode characteristics			<u>.</u>			
Diode forward voltage	V_{SD}	V _{GS} =0 V, I _F =1A	-	_	1.2	V
Reverse recovery time	t _{rr}	V _R =50V, I _F =2A,	_	82	-	ns
Reverse recovery charge	Q _{rr}	dl _F /dt=100A/μs	-	0.4	-	μC
Peak reverse recovery current	I _{rrm}	· ·	_	7.5	_	Α

Notes:

- 1. Limited by $T_{j\,max}$. Maximum duty cycle D=0.5.
- 2. Repetitive rating: pulse width limited by maximum junction temperature.
- 3. I_{AS} = 0.7A, V_{DD} = 50V, R_{G} = 25 Ω , starting T_{j} = 25 $^{\circ}$ C.



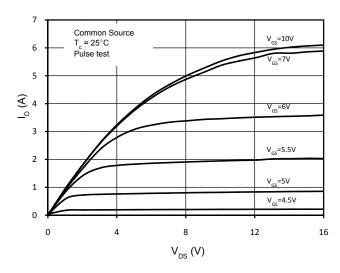


Figure 1.On-Region Characteristics

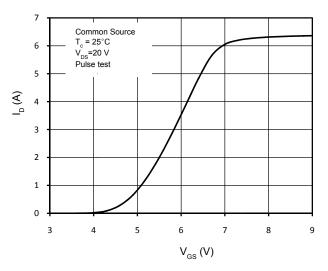


Figure 2. Transfer Characteristics

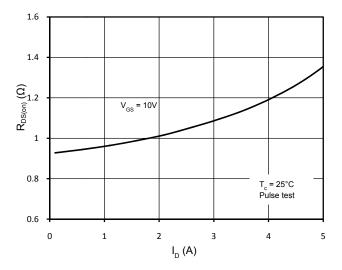


Figure 3. Static Drain-Source On Resistance

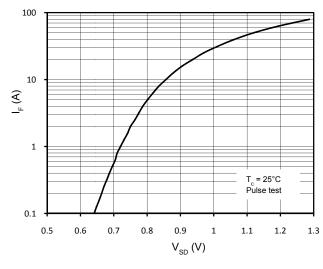


Figure 4. Body- Diode Forward Characteristics

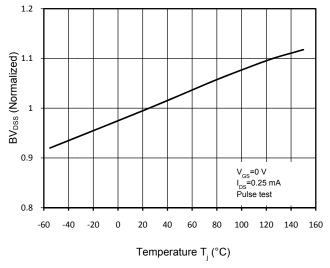


Figure 5. Normalized BV_{DSS} vs. Temperature

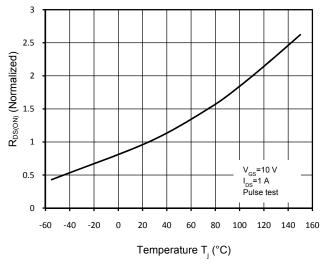


Figure 6. Normalized R_{DS(on)} vs. Temperature



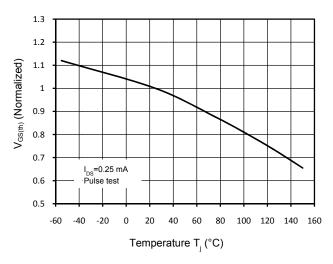


Figure 7. Threshold Voltage vs. Temperature

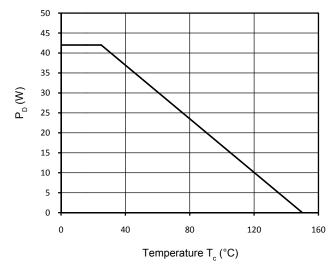


Figure 9. Power Dissipation

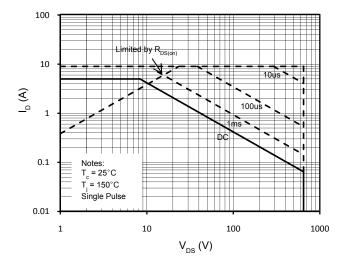


Figure 11. Maximum Safe Operating Area

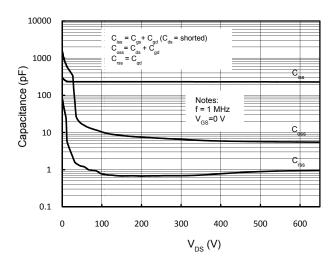


Figure 8. Capacitance Characteristics

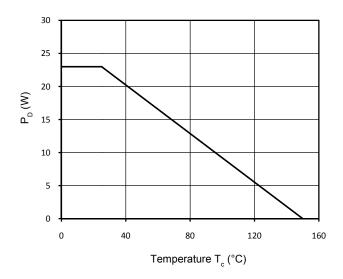


Figure 10. Power Dissipation (TO-220FT)

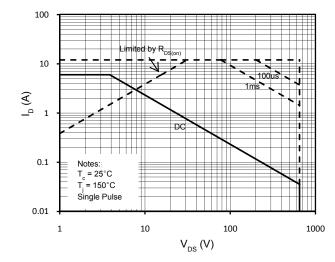


Figure 12. Maximum Safe Operating Area(TO-220FT)



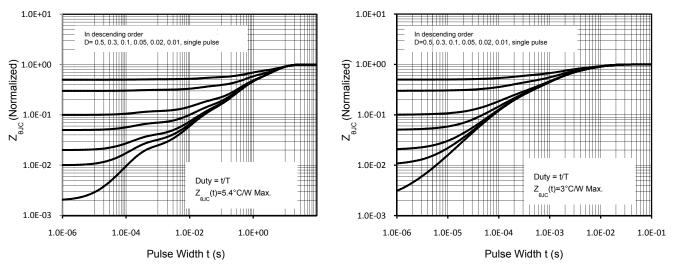


Figure 13. Transient Thermal Response Curve (TO-220FT) Figure 14. Transient Thermal Response Curve

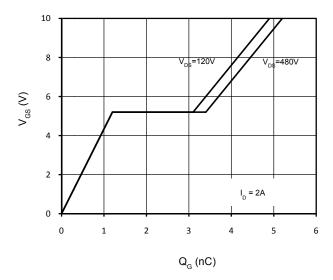
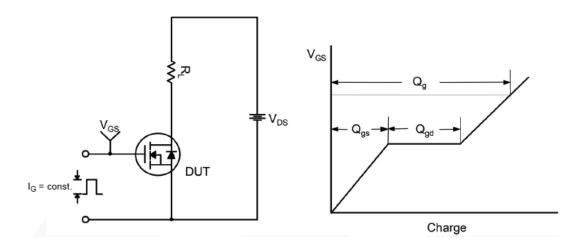


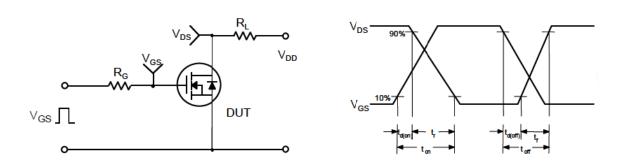
Figure 15. Gate Charge Characteristics



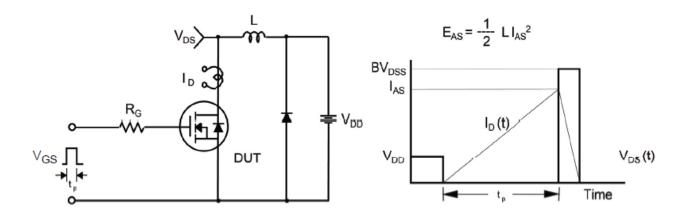
Gate Charge Test Circuit & Waveform



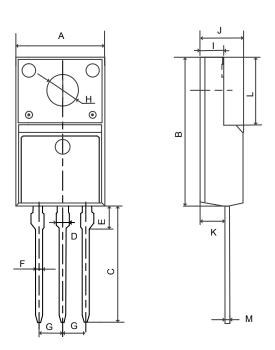
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

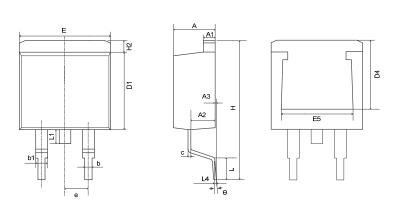






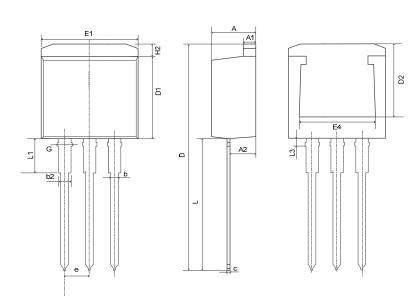
	MM		
SYMBOL	MIN	MAX	
Α	9.96	10.36	
В	15.67	16.07	
С	12.70	13.30	
D	1.12	1.32	
Е	1.85	2.15	
F	0.59	0.79	
G	2.39	2.69	
Н	3.08	3.29	
I	2.34	2.74	
J	4.50	4.90	
K	2.61	2.91	
L	6.50	6.90	
М	0.40	0.60	





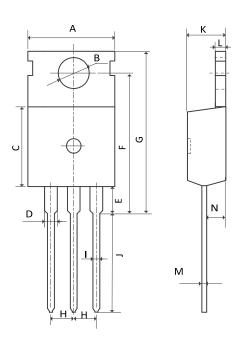
SYMBOL	MM		
STIVIBOL	MIN	MAX	
Α	4.37	4.89	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.70	0.96	
b1	1.17	1.47	
С	0.30	0.53	
D1	8.45	8.90	
D4	6.60		
Е	9.86	10.40	
E5	7.06	_	
е	2.54	BSC	
Н	14.70	15.50	
H2	1.07	1.47	
L	2.00	2.70	
L1	1.40	1.70	
L4	0.25BSC		
θ	0°	9°	





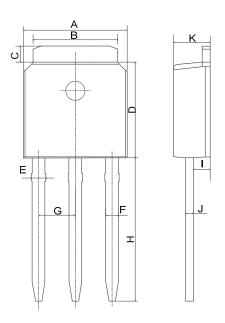
	MM		
SYMBOL	MIN	MAX	
Α	4.37	4.90	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.71	0.96	
b2	1.07	1.47	
С	0.28	0.53	
D	23.20	24.02	
D1	8.45	8.90	
D2	6.00	_	
E1	9.86	10.40	
E4	7.06	_	
е	2.54BSC		
G	1.25	1.50	
H2	_	1.50	
L	13.33	14.16	
L1	3.50	4.00	
L3	1.28	1.58	





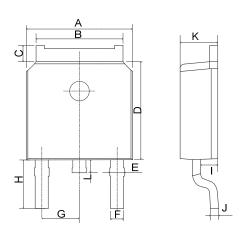
	MM		
SYMBOL	MIN	MAX	
А	9.70	10.20	
В	3.40	3.80	
С	8.90	9.40	
D	1.17	1.47	
Е	2.60	3.40	
F	15.10	16.70	
G	19.55MAX		
Н	2.54	REF	
I	0.70	0.95	
J	9.35	11.00	
K	4.30	4.77	
L	1.20	1.45	
М	0.40	0.65	
N	2.20	2.60	





	MM		
SYMBOL	MIN	MAX	
А	6.40	6.80	
В	5.13	5.50	
С	0.88	1.28	
D	5.90	6.22	
E	0.68	1.10	
F	0.68	0.91	
G	2.29REF		
Н	9.00	9.65	
I	0.85	1.17	
J	0.40	0.61	
К	2.10	2.50	





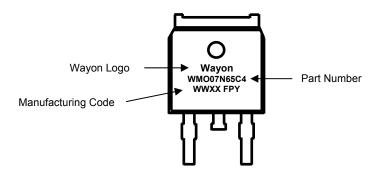
	ММ		
SYMBOL	MIN	MAX	
Α	6.40	6.80	
В	5.13	5.50	
С	0.88	1.28	
D	5.90	6.22	
E	0.68	1.10	
F	0.68	0.91	
G	2.29REF		
Н	2.90REF		
1	0.85	1.17	
J	0.51REF		
K	2.10	2.50	
L	0.40	1.00	



Ordering Information

Part	Package	Marking	Packing method
WML07N65C4	TO-220FT	WML07N65C4	Tube
WMK07N65C4	TO-220	WMK07N65C4	Tube
WMN07N65C4	TO-262	WMN07N65C4	Tube
WMM07N65C4	TO-263	WMM07N65C4	Tape and Reel
WMO07N65C4	TO-252	WMO07N65C4	Tape and Reel
WMP07N65C4	TO-251	WMP07N65C4	Tube

Marking Information



Contact Information

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WAYON website: http://www.way-on.com

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