

800V 1.2 Ω Super Junction Power MOSFET

Description

WMOS[™] M3 is Wayon's 3rd generation 800V super junction MOSFET family that is utilizing charge balance technology for extremely low on-resistance and low gate charge performance. WMOS[™] M3 is suitable for applications which require superior power density and outstanding efficiency.

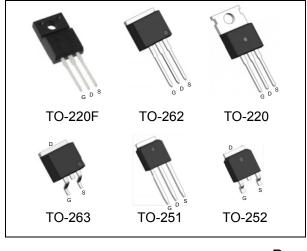
Features

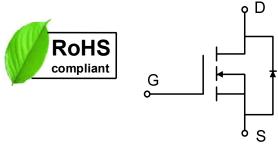
- V_{DS} =850V @ T_{j,max}
- Typ. $R_{DS(on)} = 1.2\Omega$
- 100% UIS tested
- Pb-free plating, Halogen free

Absolute Maximum Ratings



LED Lighting, Charger, Adapter, PC, LCD TV, Server





Parameter	Symbol	WMN/WMM/WMO/WMP/WMK	WML	Unit
Drain-source voltage	V _{DSS}	800		V
Continuous drain current ¹⁾ ($T_C = 25^{\circ}C$)	I _D	7		А
$(T_C = 100^{\circ}C)$		4		Α
Pulsed drain current ²⁾	I _{DM}	21		Α
Gate-source voltage	V_{GS}	±30		V
Avalanche energy, single pulse ³⁾	E _{AS}	56		mJ
Avalanche energy, repetitive ²⁾	E _{AR}	0.1		mJ
Avalanche current, repetitive ²⁾	I _{AR}	1.3		Α
Power dissipation (T _C = 25°C)	P_{D}	70	29	w
- Derate above 25°C		0.56	0.23	W/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to +150		°C
Continuous diode forward current	I _S	7		А
Diode pulse current	I _{S,pulse}	21		А

Thermal Characteristics

Parameter	Symbol	WMN/WMM/WMO/WMP/WMK	WML	Unit
Thermal resistance, junction-to-case	R _{eJC}	2	4	°C/W
Thermal resistance, junction-to-ambient	$R_{\theta JA}$	62	80	°C/W



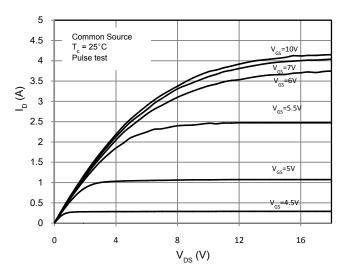
Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	800	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =0.25mA	2.5	3.3	4.5	V
Drain cut-off current	I _{DSS}	V _{DS} =800 V, V _{GS} =0 V,				μΑ
		T _j = 25°C	-	-	1	
		T _j = 125°C	-	10	-	
Gate leakage current, forward	I_{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	100	nA
Gate leakage current, reverse	I_{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =1A	-			
		T _j = 25°C	-	1.2	1.38	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	V _{DS} = 25 V, V _{GS} = 0 V,	-	465	-	
Output capacitance	Coss	f = 1 MHz	-	35	-	pF
Reverse transfer capacitance	C_{rss}		-	5.4	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 300V, I _D = 1A	-	21	-	
Rise time	t _r	$R_G = 25\Omega$, $V_{GS}=10V$	-	13	-	ns
Turn-off delay time	$t_{d(off)}$		-	50	-	
Fall time	t _f		-	20	-	
Gate charge characteristics						
Gate to source charge	Q_gs	V _{DD} =480 V, I _D =1A,	-	2.1	-	
Gate to drain charge	Q_gd	V _{GS} =0 to 10 V	-	7.5	-	nC
Gate charge total	Q_g		-	13.3	-	
Gate plateau voltage	V _{plateau}		_	5	_	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	V _{GS} =0 V, I _F =2A	-	-	1.2	V
Reverse recovery time	t _{rr}	V _R =50 V, I _F =1A,	-	165	-	ns
Reverse recovery charge	Q_{rr}	dl _F /dt=100 A/μs	-	0.64	-	μC
Peak reverse recovery current	I _{rrm}		-	7.5	-	Α

Notes:

- 1. Limited by $T_{j\,max}$. Maximum duty cycle D=0.5.
- 2. Repetitive rating: pulse width limited by maximum junction temperature
- 3. I_{AS} = 1.3A, V_{DD} = 50V, R_G = 25 Ω , starting T_j = 25 $^{\circ}C$





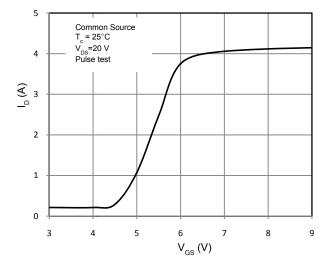
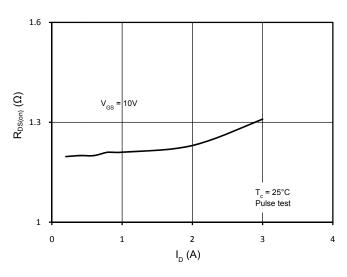


Figure 1.On-Region Characteristics

Figure 2. Transfer Characteristics



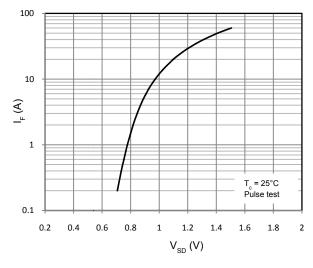
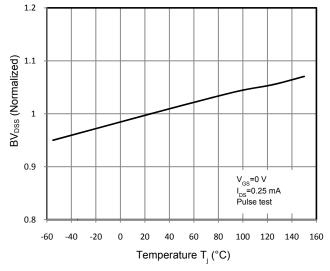


Figure 3. Static Drain-Source On Resistance

Figure 4. Body- Diode Forward Characteristics



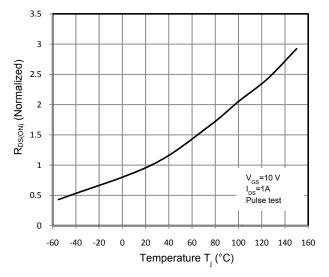


Figure 5. Normalized BV_{DSS} vs. Temperature

Figure 6. Normalized R_{DS(on)} vs. Temperature



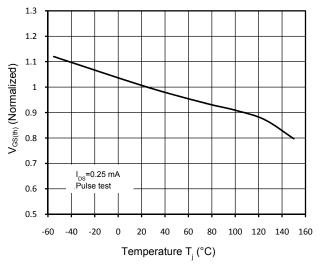


Figure 7. Threshold Voltage vs. Temperature

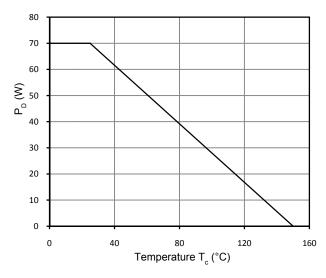


Figure 9. Power Dissipation

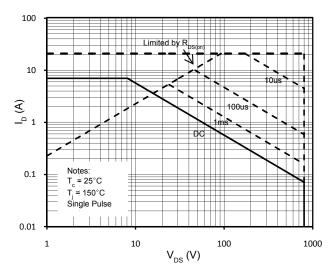


Figure 11. Maximum Safe Operating Area

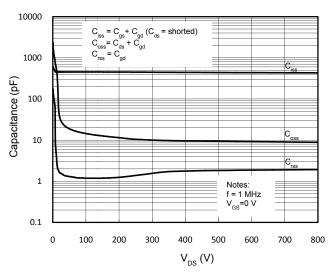


Figure 8. Capacitance Characteristics

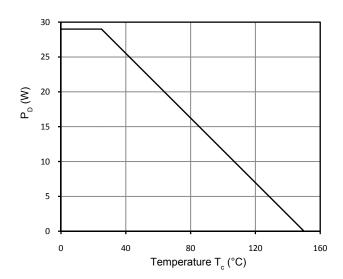


Figure 10. Power Dissipation (TO-220F)

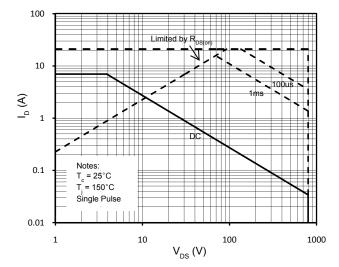


Figure 12. Maximum Safe Operating Area(TO-220F)



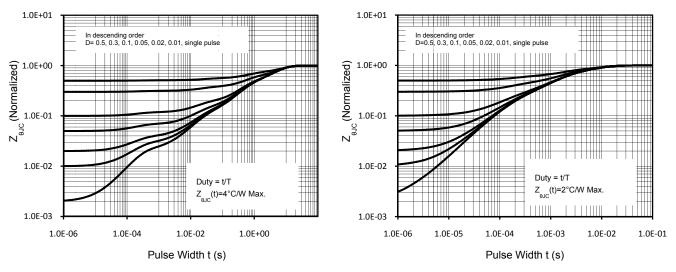


Figure 13. Transient Thermal Response Curve (TO-220F) Figure 14. Transient Thermal Response Curve

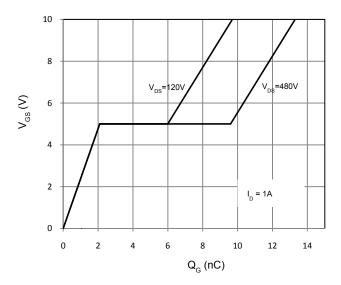
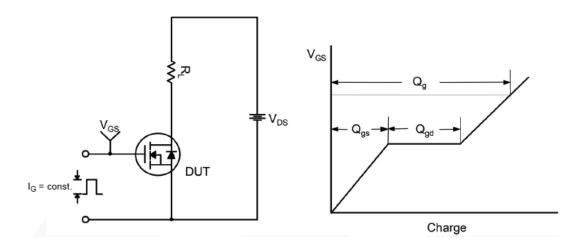


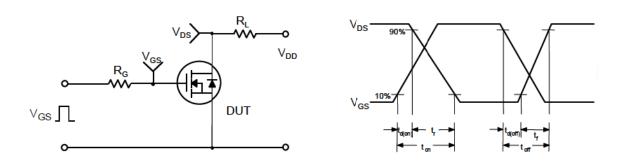
Figure 15. Gate Charge Characteristics



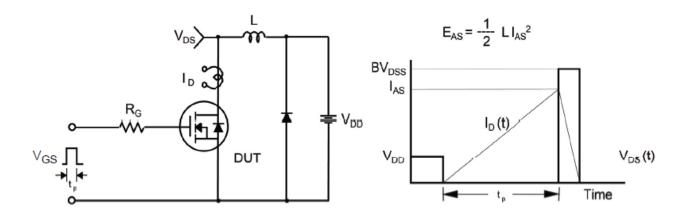
Gate Charge Test Circuit & Waveform



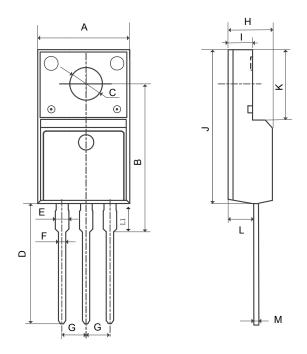
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

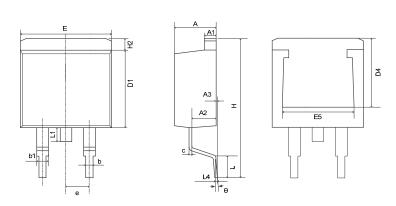






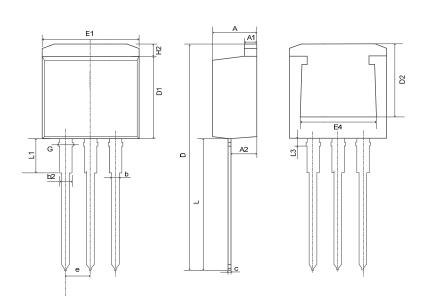
	MM		
SYMBOL	MIN	MAX	
Α	9.96	10.36	
В	15.10	16.10	
С	3.03	3.38	
D	12.64	13.28	
Е	1.18	1.58	
F	0.70	0.95	
G	2.54REF		
Н	4.50	4.90	
I	2.34	2.74	
J	15.57	16.17	
К	6.70REF		
L	2.56	2.96	
М	0.40	0.65	
L1	2.85	3.45	





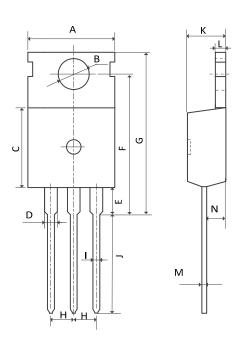
	MM		
SYMBOL	MIN	MAX	
Α	4.37	4.89	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.70	0.96	
b1	1.17	1.47	
С	0.30	0.53	
D1	8.45	8.90	
D4	6.60		
E	9.86	10.40	
E5	7.06	_	
е	2.54BSC		
Н	14.70	15.50	
H2	1.07	1.47	
L	2.00	2.70	
L1	1.40	1.70	
L4	0.25BSC		
θ	0°	9°	





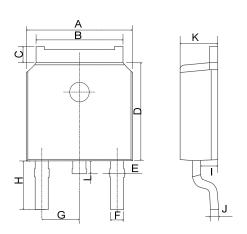
	MM		
SYMBOL	MIN	MAX	
Α	4.37	4.90	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.71	0.96	
b2	1.07	1.47	
С	0.28	0.53	
D	23.20	24.02	
D1	8.45	8.90	
D2	6.00		
E1	9.86	10.40	
E4	7.06	_	
е	2.54BSC		
G	1.25	1.50	
H2	_	1.50	
L	13.33	14.16	
L1	3.50	4.00	
L3	1.28	1.58	





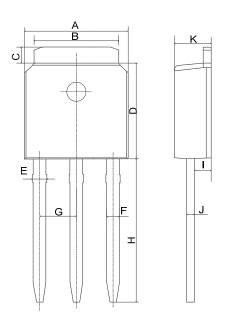
	MM		
SYMBOL	MIN	MAX	
А	9.70	10.20	
В	3.40	3.80	
С	8.90	9.40	
D	1.17	1.47	
Е	2.60	3.40	
F	15.10	16.70	
G	19.55MAX		
Н	2.54	REF	
I	0.70	0.95	
J	9.35	11.00	
K	4.30	4.77	
L	1.20	1.45	
М	0.40	0.65	
N	2.20	2.60	





	MM		
SYMBOL	MIN	MAX	
Α	6.40	6.80	
В	5.13	5.50	
С	0.88	1.28	
D	5.90	6.22	
Е	0.68	1.10	
F	0.68	0.91	
G	2.29REF		
Н	2.90REF		
I	0.85	1.17	
J	0.51REF		
K	2.10	2.50	
L	0.40	1.00	





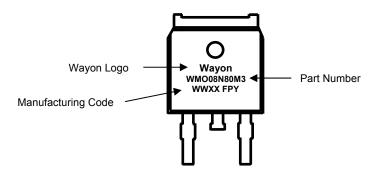
	MM		
SYMBOL	MIN	MAX	
Α	6.40	6.80	
В	5.13	5.50	
С	0.88	1.28	
D	5.90	6.22	
E	0.68	1.10	
F	0.68	0.91	
G	2.29REF		
Н	9.00	9.65	
I	0.85	1.17	
J	0.40	0.61	
K	2.10	2.50	



Ordering Information

Part	Package	Marking	Packing method
WML08N80M3	TO-220F	WML08N80M3	Tube
WMM08N80M3	TO-263	WMM08N80M3	Tape and Reel
WMO08N80M3	TO-252	WMO08N80M3	Tape and Reel
WMP08N80M3	TO-251	WMP08N80M3	Tube
WMK08N80M3	TO-220	WMK08N80M3	Tube
WMN08N80M3	TO-262	WMN08N80M3	Tube

Marking Information



Contact Information

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WAYON website: http://www.way-on.com

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