

500V 0.1Ω Super Junction Power MOSFET

Description

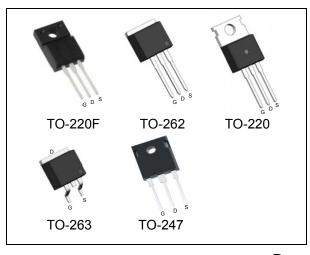
WMOSTM C4 is Wayon's 4th generation super junction MOSFET family that is utilizing charge balance technology for extremely low on-resistance and low gate charge performance. WMOSTM C4 is suitable for applications which require superior power density and outstanding efficiency.

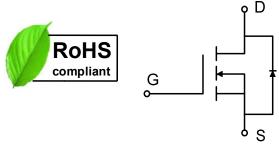
Features

- V_{DS} =600V @ $T_{j,max}$
- Typ. $R_{DS(on)} = 0.1\Omega$
- 100% UIS tested
- Pb-free plating, Halogen free



LED Lighting, Charger, Adapter, PC, LCD TV, Server





Absolute Maximum Ratings

Parameter	Symbol	WMK/WMM/WMN/WMJ	WML	Unit
Drain-source voltage	V_{DSS}	500		V
Continuous drain current ¹⁾ $(T_C = 25^{\circ}C)$	I _D	28		Α
(T _C = 100°C)		16		Α
Pulsed drain current ²⁾	I _{DM}	85		Α
Gate-source voltage	V_{GS}	±30		V
Avalanche energy, single pulse ³⁾	E _{AS}	210		mJ
Avalanche energy, repetitive ²⁾	E _{AR}	0.3		mJ
Avalanche current, repetitive ²⁾	I _{AR}	AR 2.5		Α
Power dissipation (T _C = 25°C)	P _D	160	31	W
- Derate above 25°C		1.28	0.25	W/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to +150		°C
Continuous diode forward current	I _S	28		Α
Diode pulse current	I _{S,pulse}	I _{S,pulse} 85		Α

Thermal Characteristics

Parameter	Symbol	WMK/WMM/WMN/WMJ	WML	Unit
Thermal resistance, junction-to-case	$R_{ heta JC}$	0.9	4	°C/W
Thermal resistance, junction-to-ambient	$R_{ heta JA}$	62	80	°C/W



Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	500	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=0.25$ mA	2	3	4	V
Drain cut-off current	I_{DSS}	V _{DS} =500 V, V _{GS} =0V,				μΑ
		T _j = 25°C	-	-	1	
		T _j = 125°C	-	30	-	
Gate leakage current, forward	I_{GSSF}	V _{GS} =20V, V _{DS} =0V	-	-	100	nA
Gate leakage current, reverse	I_{GSSR}	V _{GS} =-20V, V _{DS} =0V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =6A	-			
		T _j = 25°C	-	0.1	0.125	Ω
Dynamic characteristics		<u>, </u>				
Input capacitance	C _{iss}	V _{DS} = 100V, V _{GS} = 0V,	-	1770	-	
Output capacitance	Coss	f = 1 MHz	-	65	-	pF
Reverse transfer capacitance	C_{rss}		-	1.8	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 300V, I _D = 15A	-	47	-	
Rise time	t _r	$R_G = 25\Omega$, $V_{GS}=10V$	-	48	-	ns
Turn-off delay time	$t_{d(off)}$		-	106	-	
Fall time	t _f		-	26	-	
Gate charge characteristics						
Gate to source charge	Q_gs	V _{DD} =480V, I _D =15A,	-	9.7	-	
Gate to drain charge	Q_gd	V _{GS} =0 to 10V	-	7.3	-	nC
Gate charge total	Q_{g}		-	27.3	_	
Gate plateau voltage	$V_{plateau}$		_	5.6	_	٧
Reverse diode characteristics						
Diode forward voltage	V_{SD}	V _{GS} =0 V, I _F =6A	-	-	1.2	V
Reverse recovery time	t _{rr}	V _R =50V, I _F =15A,	-	255	_	ns
Reverse recovery charge	Q _{rr}	dI _F /dt=100A/μs	-	3.8	-	μC
Peak reverse recovery current	I _{rrm}		-	27.8	-	А

Notes:

- 1. Limited by $T_{j\,max}$. Maximum duty cycle D=0.5.
- 2. Repetitive rating: pulse width limited by maximum junction temperature.
- 3. I_{AS} = 2.5 A, V_{DD} = 50V, R_G = 25 Ω , starting T_j = 25 $^{\circ}$ C.



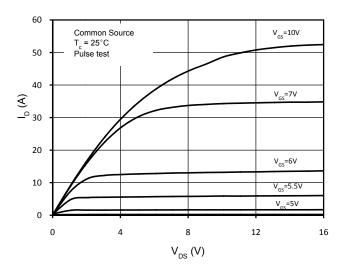


Figure 1.On-Region Characteristics

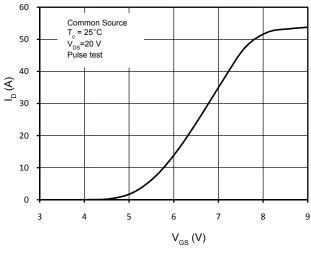


Figure 2. Transfer Characteristics

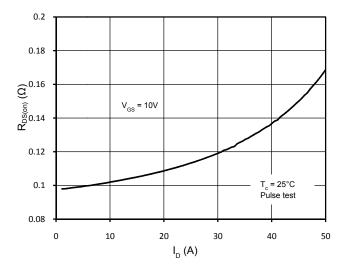


Figure 3. Static Drain-Source On Resistance

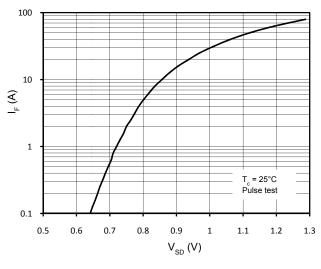


Figure 4. Body- Diode Forward Characteristics

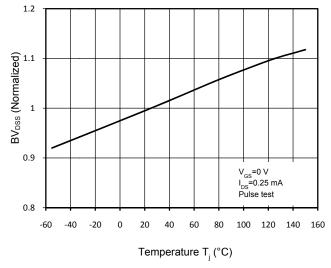


Figure 5. Normalized BV_{DSS} vs. Temperature

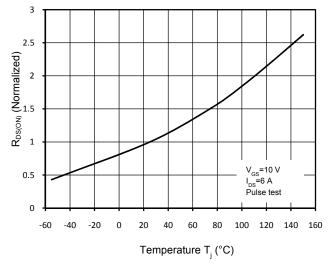
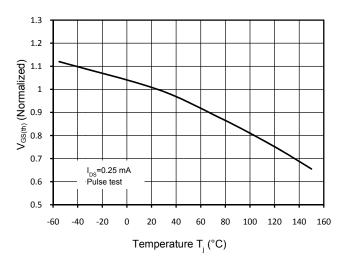


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature





10000

1000

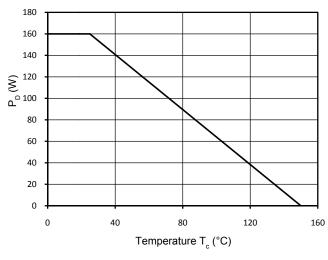
1000

C iss = C + C or (C iss = shorted)

C iss = C or + C or c iss = c or c iss = c or + C or c iss = c or c or c iss = c or c is

Figure 7. Threshold Voltage vs. Temperature

Figure 8. Capacitance Characteristics



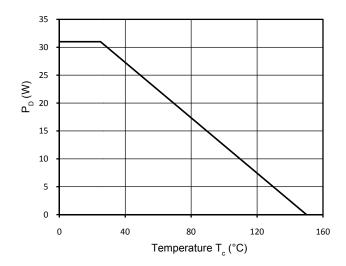
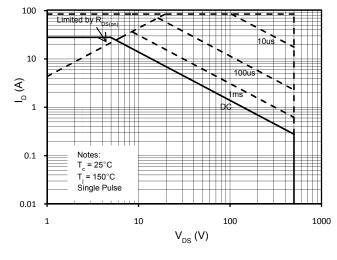


Figure 9. Power Dissipation

Figure 10. Power Dissipation (TO-220F)



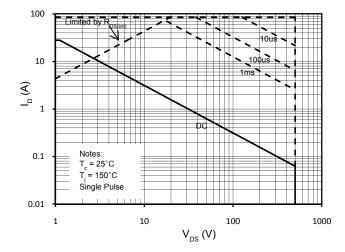


Figure 11. Maximum Safe Operating Area

Figure 12. Maximum Safe Operating Area(TO-220F)



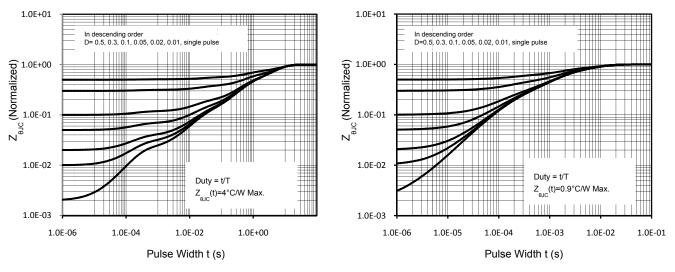


Figure 13. Transient Thermal Response Curve (TO-220F) Figure 14. Transient Thermal Response Curve

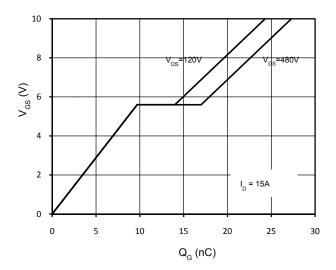
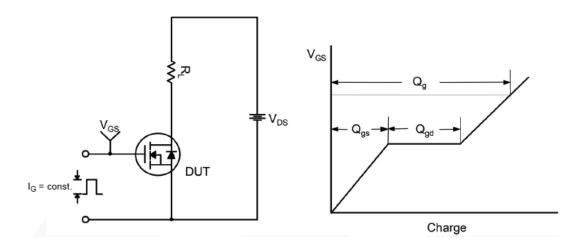


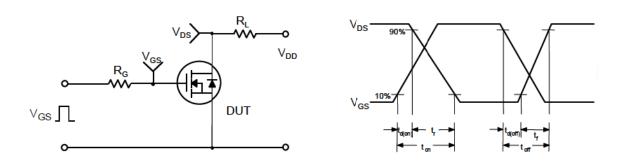
Figure 15. Gate Charge Characteristics



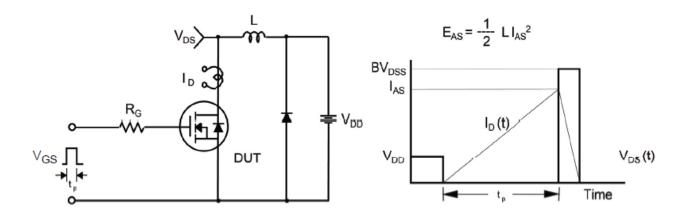
Gate Charge Test Circuit & Waveform



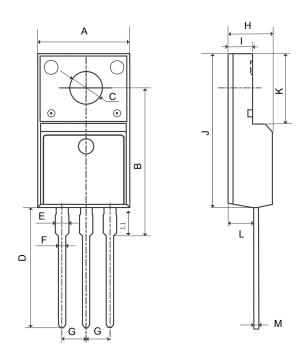
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

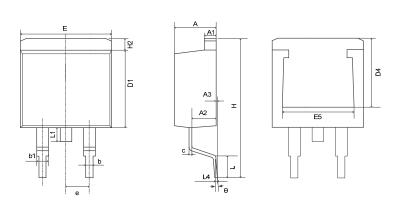






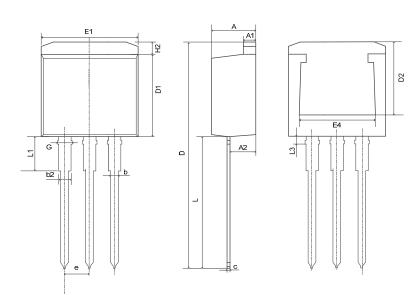
	MM		
SYMBOL	MIN	MAX	
А	9.96	10.36	
В	15.10	16.10	
С	3.03	3.38	
D	12.64	13.28	
Е	1.18	1.58	
F	0.70	0.95	
G	2.54	REF	
Н	4.50	4.90	
I	2.34	2.74	
J	15.57	16.17	
К	6.70REF		
L	2.56	2.96	
М	0.40	0.65	
L1	2.85	3.45	





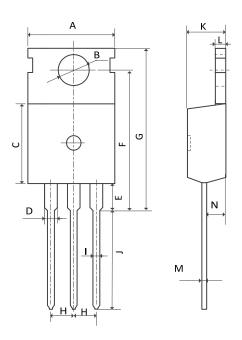
	MM		
SYMBOL	MIN	MAX	
Α	4.37	4.89	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.70	0.96	
b1	1.17	1.47	
С	0.30	0.53	
D1	8.45	8.90	
D4	6.60		
E	9.86	10.40	
E5	7.06	_	
е	2.54	BSC	
Н	14.70	15.50	
H2	1.07	1.47	
L	2.00	2.70	
L1	1.40	1.70	
L4	0.25BSC		
θ	0°	9°	





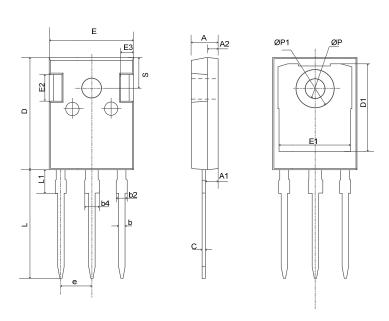
	MM		
SYMBOL	MIN	MAX	
Α	4.37	4.90	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.71	0.96	
b2	1.07	1.47	
С	0.28	0.53	
D	23.20	24.02	
D1	8.45	8.90	
D2	6.00		
E1	9.86	10.40	
E4	7.06	_	
е	2.54	BSC	
G	1.25	1.50	
H2	_	1.50	
L	13.33	14.16	
L1	3.50	4.00	
L3	1.28	1.58	





	MM		
SYMBOL	MIN	MAX	
Α	9.70	10.20	
В	3.40	3.80	
С	8.90	9.40	
D	1.17	1.47	
Е	2.60	3.40	
F	15.10	16.70	
G	19.55MAX		
Н	2.54	IREF	
I	0.70	0.95	
J	9.35	11.00	
K	4.30	4.77	
L	1.20	1.45	
М	0.40	0.65	
N	2.20	2.60	





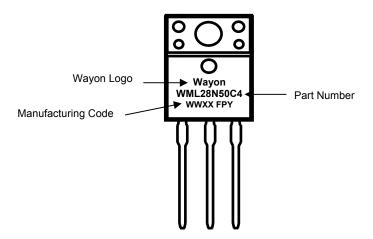
	MM		
SYMBOL	MIN	MAX	
Α	4.80	5.20	
A1	2.21	2.61	
A2	1.85	2.15	
b	1.11	1.36	
b2	1.91	2.21	
b4	2.91	3.21	
С	0.51	0.75	
D	20.70	21.30	
D1	16.25	16.85	
Е	15.50	16.10	
E1	13.00	13.60	
E2	4.80	5.20	
E3	2.30	2.70	
е	5.44BSC		
L	19.62	20.22	
L1	_	4.30	
ØP	3.40	3.80	
ØP1	_	7.30	
S	6.15BSC		



Ordering Information

Part	Package	Marking	Packing method
WML28N50C4	TO-220F	WML28N50C4	Tube
WMK28N50C4	TO-220	WMK28N50C4	Tube
WMN28N50C4	TO-262	WMN28N50C4	Tube
WMM28N50C4	TO-263	WMM28N50C4	Tape and Reel
WMJ28N50C4	TO-247	WMJ28N50C4	Tube

Marking Information



Contact Information

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WAYON website: http://www.way-on.com

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