REMOVE PHANTOM POWER

#### **GENERAL DESCRIPTION**

The CM03 family is designed to improve no-load consumption IC controller.

The CM03(A)/CM03-2/CM03A1 provides excellent  $R_{DS\ (ON)}$  and low gate charge by using advanced BiCMOS technology.

The CM03(A)/CM03-2/CM03A1 is designed to reduce the no load consumption or so called Phantom power for AC Adapter, Desk Top PC power supply, TV Power Supply and others.

#### **FEATURES**

- No load consumption can be reduced ~180mw for EPA/Climate Saver Application to reduce the phantom power.
- ◆ CM03(A)/CM03-2

 $R_{DS (ON)} = 900 \Omega (typ.) @V_{GS} = 5V / ID=1mA$ 

 $R_{DS (ON)} = 1K \Omega (typ.) @V_{GS} = 2.5V / ID=1mA$ 

◆ CM03A1

 $R_{DS (ON)} = 700 \Omega (typ.) @V_{GS} = 10V / ID=1mA$ 

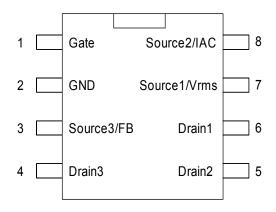
- ◆ Reliable and rugged
- ◆ Package DIP-8 / SOP-8 / SOT23-6 / TO-92A / TO-92B / SOT23 / SOT23-5
- ◆ No VCC

◆ CM03/CM03A: 3 MOSFET

◆ CM03-2 : 2 MOSFET
 ◆ CM03A1 : 1 MOSFET

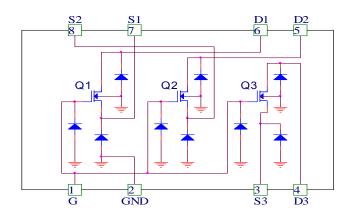
#### **PIN CONFIGURATION**

#### CM03A DIP/SOP 8 Pin Top View

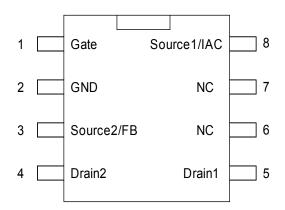


#### **SYMBOL**

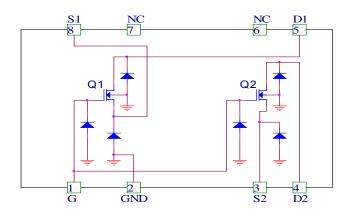
#### CM03A N-Channel (3 MOSFET)



#### CM03-2 DIP/SOP 8 Pin Top View



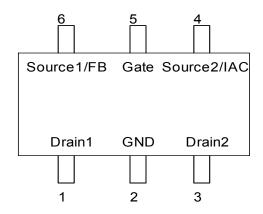
CM03-2 N-Channel (2 MOSFET)



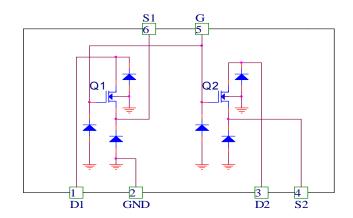




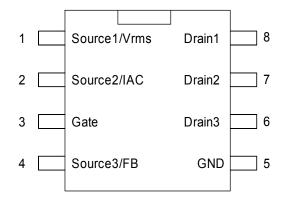
#### CM03-2 SOT23-6 Top View



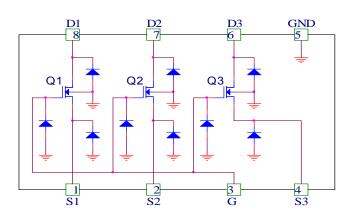
#### CM03-2 N-Channel (2 MOSFET)



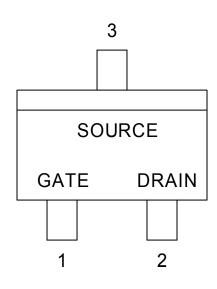
#### CM03 DIP/SOP 8 Pin Top View



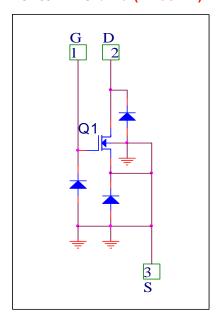
CM03 N-Channel (3 MOSFET)



#### CM03A1 Sot-23 Top View



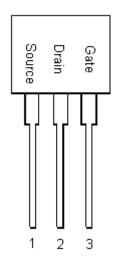
#### CM03A1 N-Channel (1 MOSFET)



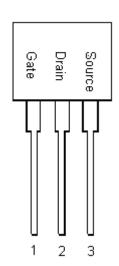




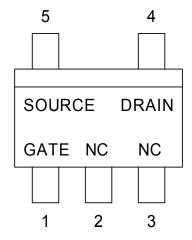
CM03 TO-92A Top View



CM03A1 TO-92B Top View



CM03A1 Sot23-5 Top View



#### **ORDERING INFORMATION**

Part Number	Temperature Range	Package
CM03AXIP*	-55°C to 150°C	8-Pin DIP
CM03AXIS*	-55°C to 150°C	8-Pin SOP
CM03-2XIP*	-55°C to 150°C	8-Pin DIP
CM03-2XIS*	-55°C to 150°C	8-Pin SOP
CM03-2XIM26*	-40°C ~ +85°C	SOT-23-6
CM03XIP*	-55°C to 150°C	8-Pin DIP
CM03XIS*	-55°C to 150°C	8-Pin SOP
CM03XCN*	-65°C to 150°C	TO-92A
CM03A1XIM	-40°C to +85°C	Sot-23
CM03A1XIM25	-40°C to +85°C	Sot23-5
CM03A1XCN*	-65°C to 150°C	TO-92B

\*Note: X : Suffix for Halogen Free and PB Free Product

## ABSOLUTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified)

PARAMETER	Symbol	RATINGS	Unit
Drain-Source Voltage	V <sub>DSS</sub>	800	V
Gate-Source Voltage	V <sub>GSS</sub>	+20/-0.3	V
Source Pin Voltage	Vsb	< 8	V
Continuous Drain Current *	I <sub>D</sub>	25	mA
Pulsed Continuous Drain Current *	I <sub>DM</sub>	200	mA
Power Dissipation	P <sub>D (MAX)</sub>	1.3	W
Junction Temperature	TJ	+150	$^{\circ}\!\mathbb{C}$
Storage Temperature (SOP8/DIP8)	T <sub>STG</sub>	-55 ~ +150	$^{\circ}$
Storage Temperature (SOT23)	T <sub>STG</sub>	-40 ~ +85	$^{\circ}$
Storage Temperature (TO-92A)	T <sub>STG</sub>	-65 ~ +150	$^{\circ}\!\mathbb{C}$
Storage Temperature (TO-92B)	T <sub>STG</sub>	-65 ~ +150	$^{\circ}\!\mathbb{C}$

<sup>\*:</sup> Surface Mounted on  $1\text{in}^2$  pad area,  $t \leq 10\text{sec}$ 

#### THERMAL DATA

PARAMETER	Symbol	Min	TYP	MAX	Unit
Junction to Ambient *	$\theta_{JA}$		74	110	°C/W

<sup>\* :</sup> Surface Mounted on 1in² pad area, t≦10sec

# CM03(A)/CM03-2/CM03A1 REMOVE PHANTOM POWER

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_A = 25^{\circ}C$ .

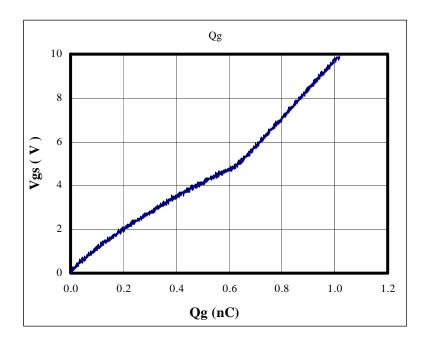
			CM03(A)/CM03-2/CM03A1			
PARAMETER	SYMBOL	TEST CONDITIONS	Min	Тур	Max	Unit
OFF CHARACTERISTICS			•	•	•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =40uA	800			٧
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =500V, V <sub>GS</sub> =0V			0.1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±0.1	uA
ON CHARACTERISTICS (CM03/A)				ı	I	
Gate Threshold Voltage	V <sub>GS (TH)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250uA	0.9	1.1	1.3	٧
		V <sub>GS</sub> =5V, I <sub>D</sub> =1mA		330	1K	Ω
Drain-Source On-State Resistance (Note 1)	R <sub>DS (ON)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =1mA		350	1K	Ω
ON CHARACTERISTICS (CM03A1)			l	l		
Gate Threshold Voltage	V <sub>GS (TH)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250uA	3	4	5	V
Drain-Source On-State Resistance (Note 1)	R <sub>DS (ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =1mA		700	1.1K	Ω
SWITCHING CHARACTERISTICS			ı	ı	I	
Turn-On Delay Time (Note 1)	t <sub>D(ON)</sub>			20		ns
Turn-On Rise Time	t <sub>R</sub>	$V_{DS}$ =50V, $V_{GS}$ =5V, $R_{G}$ =3 $\Omega$ ,		16		ns
Turn-Off Delay Time	t <sub>D (OFF)</sub>	$R_L=2.7\Omega$		4		us
Turn-Off Fall Time	t <sub>F</sub>			3.7		us
Gate-Source Charge	Q <sub>GS</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =25mA		1		nC
SOURCE-DRAIN DIODE RATINGS AND CHAR	ACTERISTI	cs	ı	ı	I	
Drain-Source Diode Forward Voltage (Note 1)	V <sub>SD</sub>	I <sub>S</sub> =25 mA, V <sub>GS</sub> =0V		0.76	1	V
Diode Continuous Forward Current (Note 2)	Is			25		mA
SOURCE CHARACTERISTICS	ı			1		
Normal Operating Voltage	Vsb		0		7	V
Breakdown Voltage	Vsb			10		٧

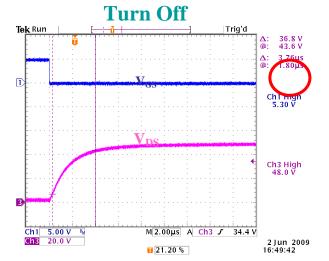
Note 1 : Pulse width  $\leq$ 300us, duty cycle  $\leq$ 2%.

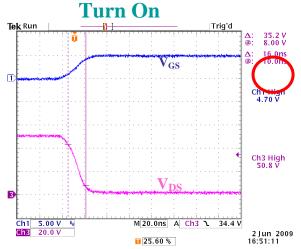
Note 2 : Surface Mounted on  $1in^2$  pad area,  $t \le 10sec$ 



### TYPICAL ELECTRICAL CHARACTERISTICS

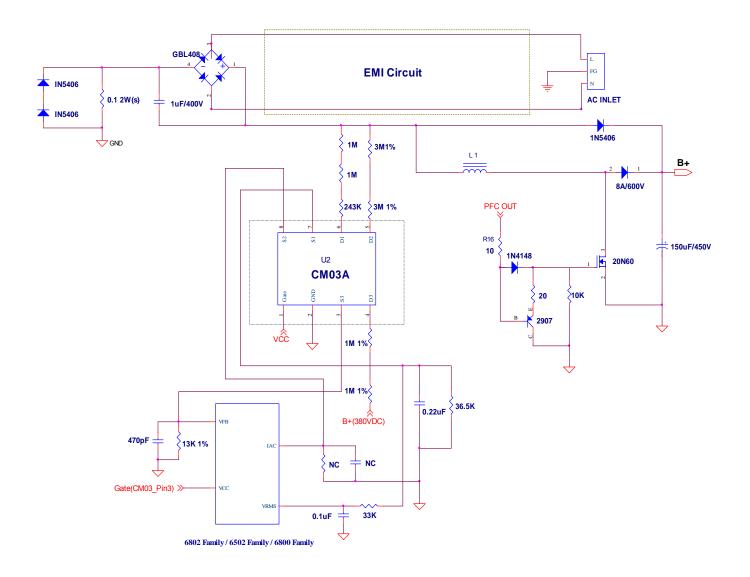






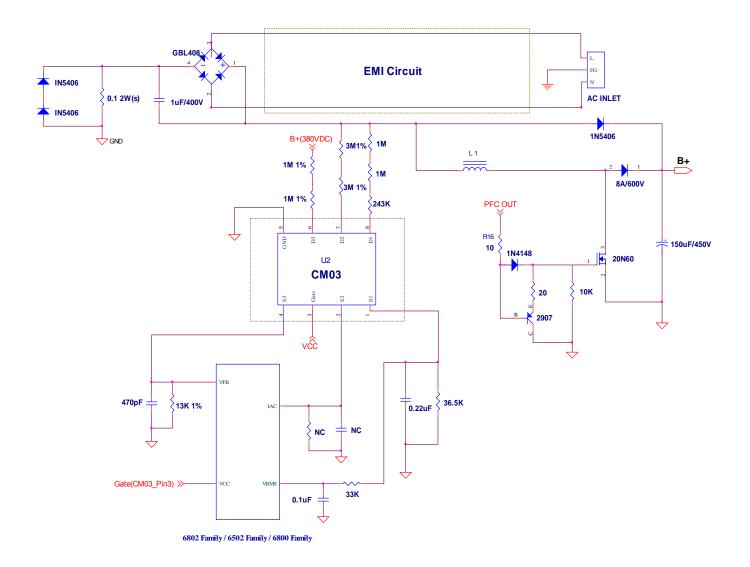


### **CM03A APPLICATION CIRCUIT**

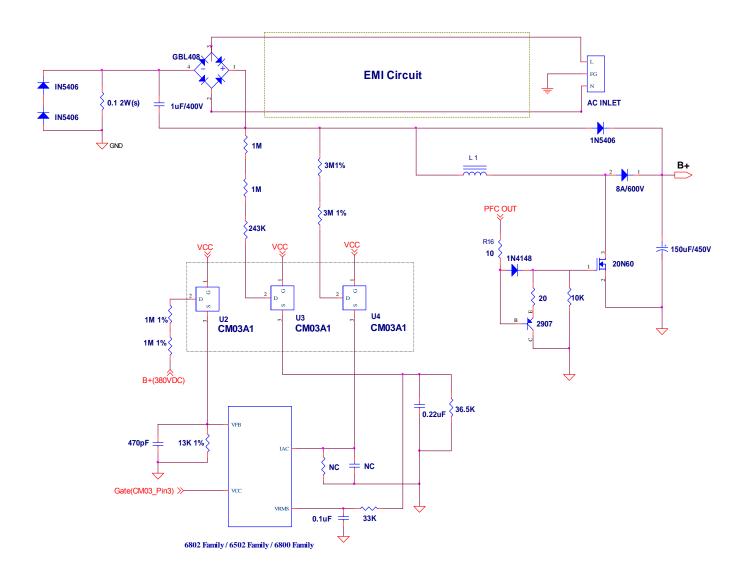




#### **CM03 APPLICATION CIRCUIT**



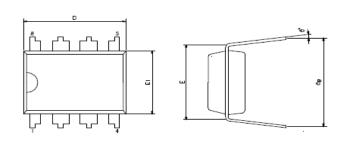
#### **CM03A1 APPLICATION CIRCUIT**



#### **PACKAGE DIMENSION**

0.018ty

#### **8-PIN PDIP**



SYMBOLS	MIN.	NOR.	MAX.
Α	ı	_	0.210
A1	0.015	_	_
A2	0.125	0.130	0.135
D	0.355	0.365	0.400
E		0.300 BSC	,
E1	0.245	0.250	0.255
L	0.115	0.130	0.150
€B	0.335	0.355	0.375
¢°	0	7	15
			TIMIT + IMPU

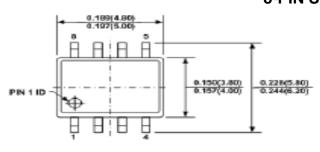
UNIT: INCH

- NOTES: 1.JEDEC OUTLINE : MS-001 BA
- 2."D","EI\* DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH. 3.6B IS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.
- POINTED OR ROUNDED LEAD TIPS ARE PREFERRED TO EASE INSERTION.
- EASE INSERTION.

  5.DISTANCE BETWEEN LEADS INCLUDING DAM BAR PROTRUSIONS TO BE .005 INCH MININUM.

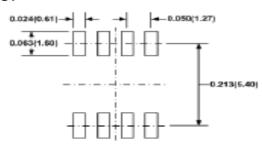
  6.DATUM PLANE IT COINCIDENT WITH THE BOTTOM OF LEAD, WHERE LEAD EXITS BODY.

#### 8-PIN SOP

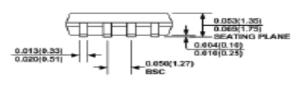


TOP VIEW

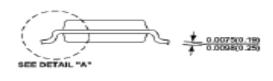
0.100typ 0.060typ.



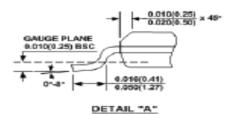
#### RECOMMENDED LAND PATTERN



FRONT VIEW



SIDE VIEW

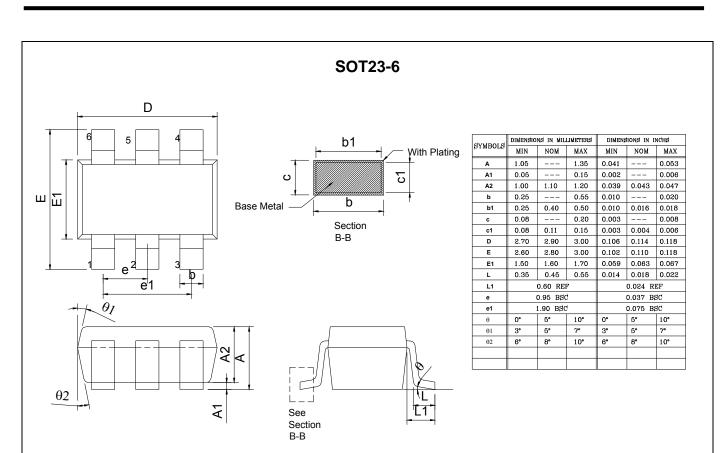


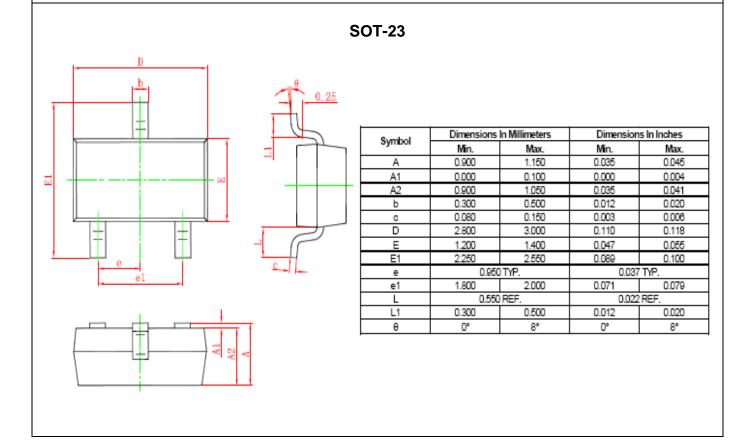
#### NOTE:

- CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
   PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
   PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH.
- 3) PACKAGE WIDTH DOILS NOT INCLUDE INTERLEAD FLASH OR PROTRUGIONS.
  4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
  5) DRAWING CONFORMS TO JEDEC MS-012, VARIATION AA.
  6) DRAWING IS NOT TO SCALE.



## REMOVE PHANTOM POWER



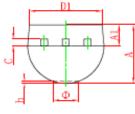


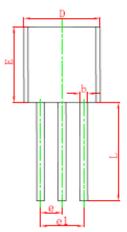
# CM03(A)/CM03-2/CM03A1 REMOVE PHANTOM POWER





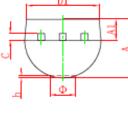
#### **TO-92A**

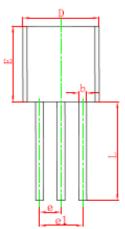




Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	3.300	3.700	0.130	0.146	
A1	1.100	1.400	0.043	0.055	
b	0.380	0.550	0.015	0.022	
С	0.360	0.510	0.014	0.020	
D	4.400	4.700	0.173	0.185	
D1	3.430		0.135		
E	4.300	4.700	0.169	0.185	
e	1.270 TYP		0.050 TYP		
e1	2.440	2.640	0.096	0.104	
L	14.100	14.500	0.555	0.571	
θ		1.600		0.063	
h	0.000	0.380	0.000	0.015	

#### **TO-92B**

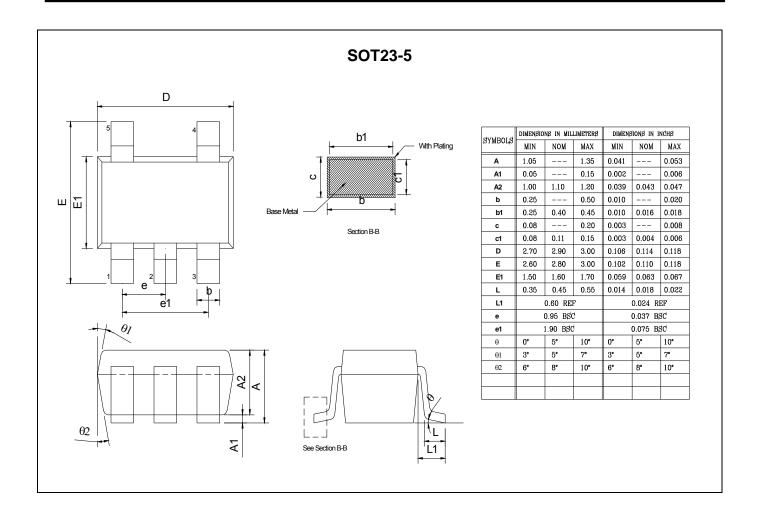




Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	3.300	3.700	0.130	0.146	
A1	1.100	1.400	0.043	0.055	
Ь	0.380	0.550	0.015	0.022	
С	0.360	0.510	0.014	0.020	
D	4.400	4.700	0.173	0.185	
D1	3.430		0.135		
E	4.300	4.700	0.169	0.185	
e	1.27	1.270 TYP		TYP	
e1	2.440	2.640	0.096	0.104	
L	14.100	14.500	0.555	0.571	
θ		1.600		0.063	
h	0.000	0.380	0.000	0.015	



## REMOVE PHANTOM POWER





REMOVE PHANTOM POWER

#### **IMPORTANT NOTICE**

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A few applications using integrated circuit products may involve potential risks of death, personal injury, or severe property or environmental damage. CMC integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life-support applications, devices or systems or other critical applications. Use of CMC products in such applications is understood to be fully at the risk of the customer. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.

#### HsinChu Headquarter

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