

600V 0.062Ω Super Junction Power MOSFET

Description

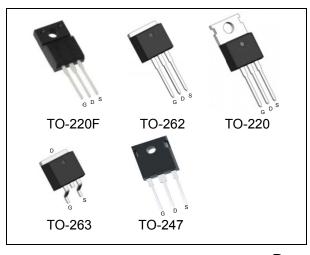
WMOSTM F2 is Wayon's 2nd generation super junction MOSFET family with fast body diode. F2 series provide all benefits of a fast switching SJ-MOSFET while offering an extremely fast body diode. WMOSTM F2 makes especially resonant switching applications more reliable.

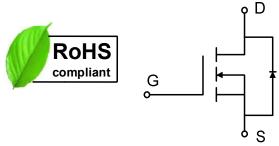
Features

- V_{DS} =650V @ T_{j,max}
- Typ. $R_{DS(on)} = 0.062\Omega$
- 100% UIS tested
- Pb-free plating, Halogen free



LED Lighting, Charger, Adapter, PC, LCD TV, Server





Absolute Maximum Ratings Parameter

Parameter	Symbol	WMK/WMM/WMN/WMJ	WML	Unit
Drain-source voltage	V _{DSS}	600		V
Continuous drain current ¹⁾ (T _C = 25°C)	I _D	50		
(T _C = 100°C)		26		Α
Pulsed drain current ²⁾	I _{DM}	90		Α
Gate-source voltage	V_{GS}	±30		V
Avalanche energy, single pulse ³⁾	E _{AS}	s 940		mJ
Avalanche energy, repetitive ²⁾	E _{AR}	Ear 1.3		mJ
Avalanche current, repetitive ²⁾	I _{AR}	5		А
Power dissipation (T _C = 25°C)	P _D	350	34	W
- Derate above 25°C		2.8	0.27	W/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to +150		°C
Continuous diode forward current ¹⁾	Is	50		Α
Diode pulse current ²⁾	I _{S.pulse}	90		Α

Thermal Characteristics

Parameter	Symbol	WMK/WMM/WMN/WMJ	WML	Unit
Thermal resistance, junction-to-case	$R_{ heta JC}$	0.36	3.6	°C/W
Thermal resistance, junction-to-ambient	$R_{ heta JA}$	62	80	°C/W



Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =1 mA	600	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25mA	2	3	4.5	V
Drain cut-off current	I _{DSS}	V _{DS} =600 V, V _{GS} =0V,				μΑ
		T _j = 25°C	-	-	50	
		T _j = 125°C	-	200	-	
Gate leakage current, forward	I _{GSSF}	V _{GS} =20V, V _{DS} =0V	-	-	300	nA
Gate leakage current, reverse	I _{GSSR}	V _{GS} =-20V, V _{DS} =0V	-	-	-300	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =15A	-			
		T _j = 25°C	_	0.062	0.078	Ω
Dynamic characteristics						
Input capacitance	C _{iss}	V _{DS} = 100V, V _{GS} = 0V,	_	3400	-	
Output capacitance	Coss	f = 1 MHz	-	115	-	pF
Reverse transfer capacitance	C _{rss}		-	2.7	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 300V, I _D = 15A	-	53	-	
Rise time	t _r	$R_G = 25\Omega$, $V_{GS}=10V$	-	40	-	ns
Turn-off delay time	$t_{d(off)}$		-	214	-	
Fall time	t _f		-	28	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	V _{DD} =480V, I _D =15A,	-	15.0	-	
Gate to drain charge	Q_{gd}	V _{GS} =0 to 10V	-	15.6	-	nC
Gate charge total	Q_{g}		_	58.0	-	
Gate plateau voltage	V _{plateau}		-	5.0	-	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	V _{GS} =0 V, I _F =15A	-	-	1.2	V
Reverse recovery time	t _{rr}	V _R =50V, I _F =15A,	-	160	-	ns
Reverse recovery charge	Q _{rr}	dl _F /dt=100A/μs	-	2.8	-	μC
Peak reverse recovery current	I _{rrm}		-	29	-	Α

Notes:

- 1. Limited by $T_{j\,max}$. Maximum duty cycle D=0.5.
- 2. Pulse width limited by maximum junction temperature.
- 3. I_{AS} = 5A, V_{DD} = 50V, R_G = 25 Ω , starting T_j = 25 $^{\circ}$ C.



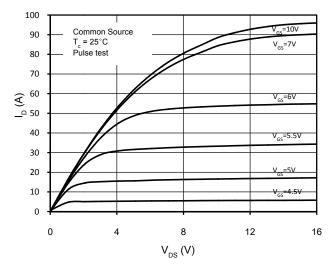


Figure 1.On-Region Characteristics

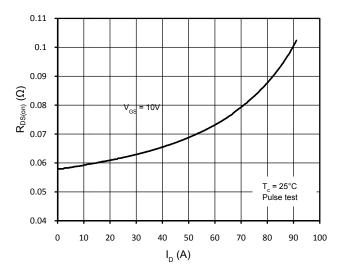


Figure 3. Static Drain-Source On Resistance

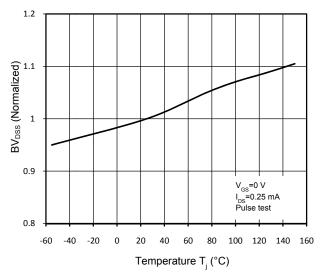


Figure 5. Normalized BV_{DSS} vs. Temperature

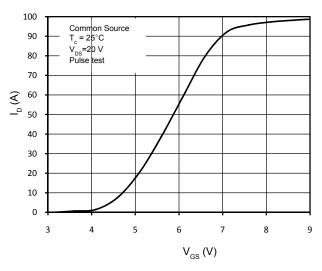


Figure 2. Transfer Characteristics

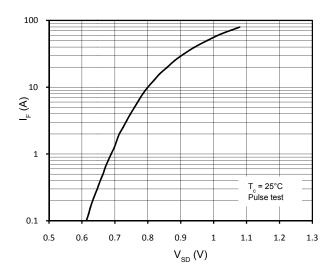


Figure 4. Body- Diode Forward Characteristics

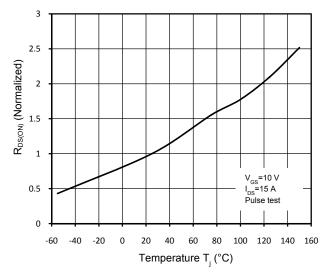


Figure 6. Normalized R_{DS(on)} vs. Temperature



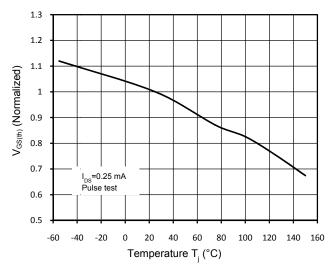


Figure 7. Threshold Voltage vs. Temperature

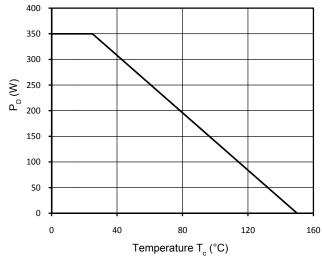


Figure 9. Power Dissipation

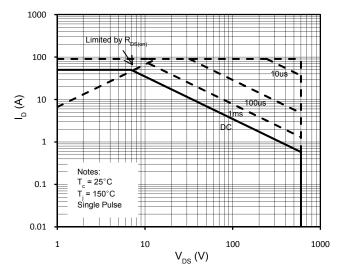


Figure 11. Maximum Safe Operating Area

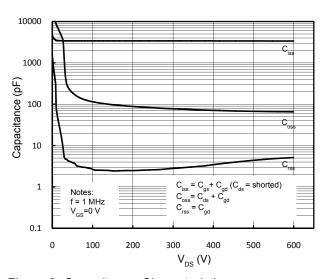


Figure 8. Capacitance Characteristics

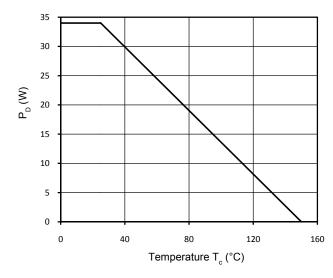


Figure 10. Power Dissipation (TO-220F)

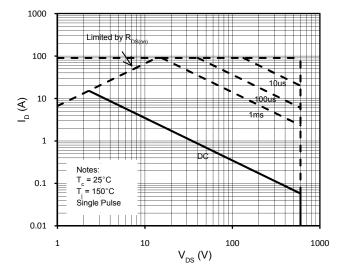


Figure 12. Maximum Safe Operating Area(TO-220F)



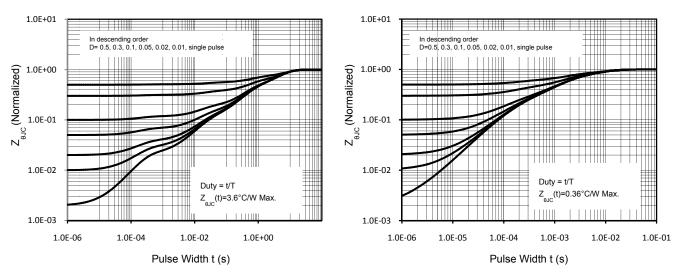


Figure 13. Transient Thermal Response Curve (TO-220F) Figure 14. Transient Thermal Response Curve

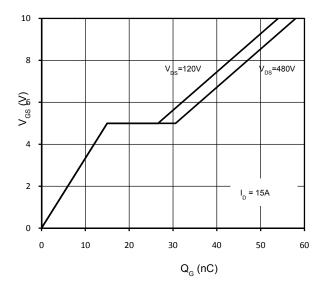
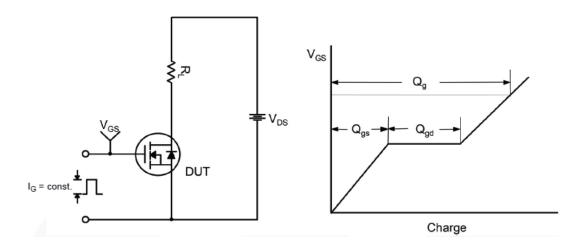


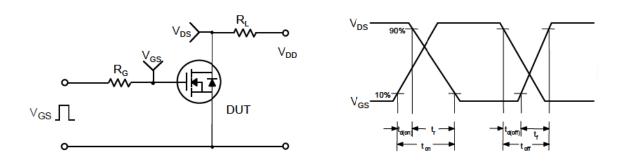
Figure 15. Gate Charge Characteristics



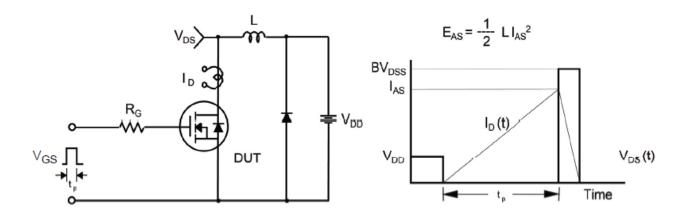
Gate Charge Test Circuit & Waveform



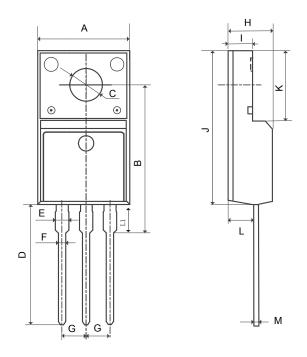
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

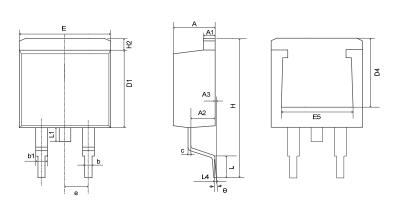






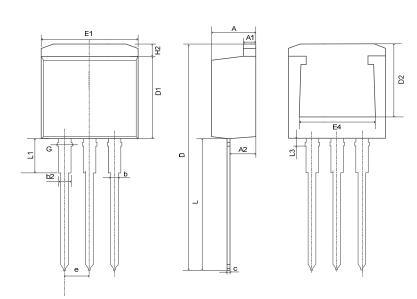
	MM		
SYMBOL	MIN	MAX	
Α	9.96	10.36	
В	15.10	16.10	
С	3.03	3.38	
D	12.64	13.28	
Е	1.18	1.58	
F	0.70	0.95	
G	2.54	REF	
Н	4.50	4.90	
I	2.34	2.74	
J	15.57	16.17	
K	6.70REF		
L	2.56	2.96	
М	0.40	0.65	
L1	2.85	3.45	





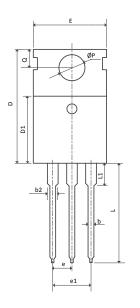
SYMBOL	MM		
	MIN	MAX	
А	4.37	4.89	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.70	0.96	
b1	1.17	1.47	
С	0.30	0.53	
D1	8.45	8.90	
D4	6.60		
E	9.86	10.40	
E5	7.06	_	
е	2.54BSC		
Н	14.70	15.50	
H2	1.07	1.47	
L	2.00	2.70	
L1	1.40	1.70	
L4	0.25BSC		
θ	0°	9°	

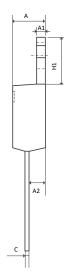


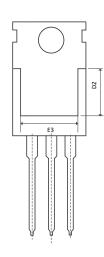


SYMBOL	MM		
	MIN	MAX	
Α	4.37	4.90	
A1	1.17	1.42	
A2	2.49	2.89	
b	0.71	0.96	
b2	1.07	1.47	
С	0.28	0.53	
D	23.20	24.02	
D1	8.45	8.90	
D2	6.00		
E1	9.86	10.40	
E4	7.06	_	
е	2.54BSC		
G	1.25	1.50	
H2	_	1.50	
L	13.33	14.16	
L1	3.50	4.00	
L3	1.28	1.58	



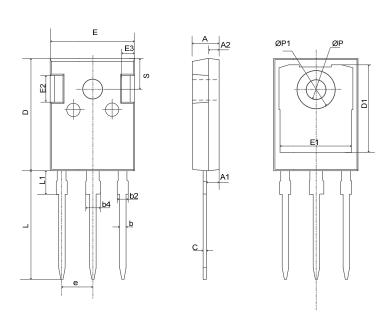






0.4.50	MM		
SYMBOL	MIN	MAX	
Α	4.37	4.70	
A1	1.25	1.40	
A2	2.20	2.60	
b	0.70	0.95	
b2	1.17	1.47	
С	0.45	0.60	
D	15.10	16.10	
D1	8.80	9.40	
D2	5.50	_	
E	9.70	10.30	
E3	7.00	_	
е	2.54BSC		
e1	5.08BSC		
H1	6.25	6.85	
L	12.75	13.80	
L1	_	3.40	
ØP	3.40	3.80	
Q	2.60	3.00	





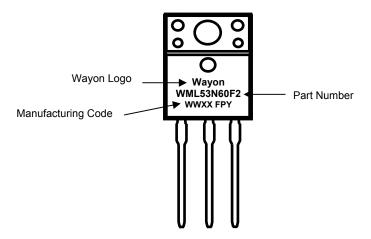
	N	IM	
SYMBOL	MIN	MAX	
А	4.80	5.21	
A1	2.21	2.61	
A2	1.85	2.16	
b	1.07	1.36	
b2	1.91	2.41	
b4	2.87	3.38	
С	0.51	0.75	
D	20.70	21.30	
D1	16.25	17.65	
Е	15.50	16.13	
E1	12.38	13.60	
E2	3.68	5.20	
E3	1.00	2.70	
е	5.44BSC		
L	19.62	20.32	
L1	_	4.40	
ØP	3.40	3.80	
ØP1	_	7.30	
S	6.15BSC		



Ordering Information

Part	Package	Marking	Packing method
WML53N60F2	TO-220F	WML53N60F2	Tube
WMK53N60F2	TO-220	WMK53N60F2	Tube
WMN53N60F2	TO-262	WMN53N60F2	Tube
WMM53N60F2	TO-263	WMM53N60F2	Tape and Reel
WMJ53N60F2	TO-247	WMJ53N60F2	Tube

Marking Information



Contact Information

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WAYON website: http://www.way-on.com

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