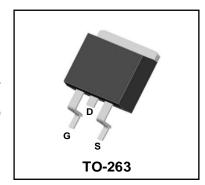


# 100V N-Channel Enhancement Mode Power MOSFET

# **Description**

WMM043N10HGS uses Wayon's advanced power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.



#### **Features**

- $V_{DS} = 100V, I_D = 145A$
- $R_{DS(on)} < 5m\Omega$  @  $V_{GS} = 10V$
- Green Device Available
- Low Gate Charge
- 100% EAS Guaranteed

# **Applications**

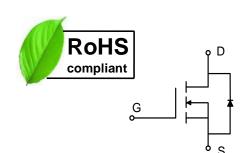
- Power Management Switches
- DC/DC Converter
- LED Backlighting

# Absolute Maximum Ratings (Tc = 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100	V	
Gate-Source Voltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current	T <sub>C</sub> =25°C		145	Δ.	
	Tc=100°C	lο	92	А	
Pulsed Drain Current <sup>4</sup>		Ірм	580	А	
Single Pulse Avalanche Energy <sup>3</sup>		EAS	320	mJ	
Total Power Dissipation	T <sub>C</sub> =25°C	<b>P</b> <sub>D</sub>	208	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C	

### **Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>1</sup>	Reja	38	°C/W
Thermal Resistance from Junction-to-Case	Rелс	0.6	°C/W





## Electrical Characteristics (Tc = 25°C, unless otherwise noted)

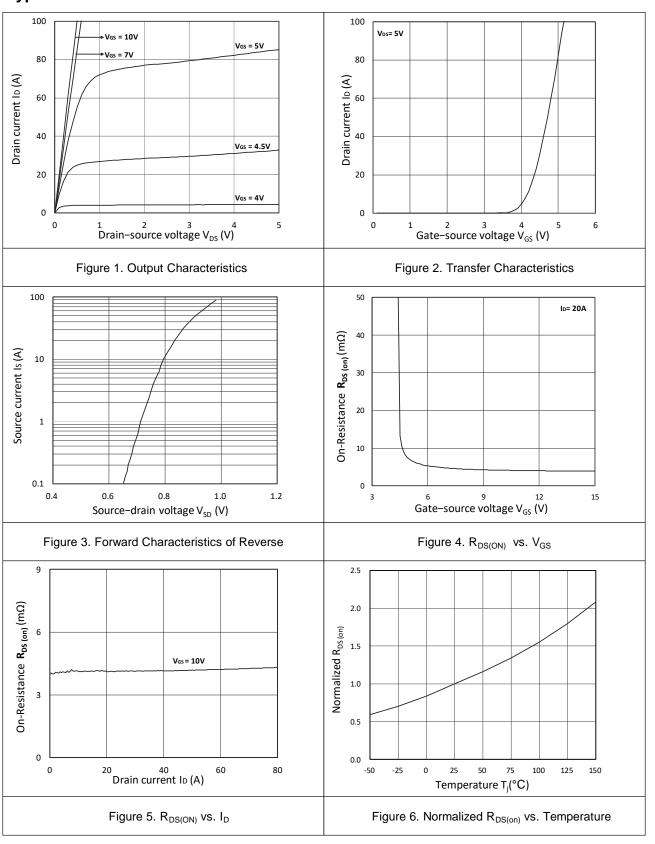
Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics								
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	V <sub>G</sub> S = 0V, I <sub>D</sub> = 250µA	100	-	-	V	
Gate-body Leakage current		Igss	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	IDSS	V <sub>DS</sub> =100V, V <sub>GS</sub> = 0V	-	-	1	μΑ	
	T <sub>J</sub> =100°C			-	-	100		
Gate-Threshold Voltage		V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_D=250\mu A$	2	3	4	V	
Drain-Source on-Resistance <sup>2</sup>		R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	4.1	5	mΩ	
Forward Transconductance <sup>2</sup>	Forward Transconductance <sup>2</sup>		V <sub>DS</sub> =10V, I <sub>D</sub> =20A	-	62	-	S	
Dynamic Characteristics		1	,	1				
Input Capacitance		Ciss		-	6868	-	pF	
Output Capacitance		Coss	V <sub>DS</sub> = 50V, V <sub>GS</sub> =0V, f =1MHz	-	740	-		
Reverse Transfer Capacitance	)	Crss		-	21	-		
Switching Characteristics								
Gate Resistance		Rg	$V_{GS} = 0V$ , $V_{DS} = 0V$ , $f = 1MHz$	-	1.3	-	Ω	
Total Gate Charge		Qg		-	112.5	-	nC	
Gate-Source Charge		Q <sub>gs</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> =20A	-	30.5	-		
Gate-Drain Charge		Q <sub>gd</sub>		-	27.3	-		
Turn-on Delay Time		t <sub>d(on)</sub>		-	33	-		
Rise Time Turn-off Delay Time		t <sub>r</sub>	$V_{GS}$ =10V, $V_{DS}$ =50V, $R_{G}$ = 3 $\Omega$ , $I_{D}$ = 20A	-	39	-	ns	
		t <sub>d(off)</sub>		-	67.1	-		
Fall Time		<b>t</b> f		-	32	-		
Drain-Source Body Diode Characteristics								
Diode Forward Voltage <sup>2</sup>		V <sub>SD</sub>	I <sub>F</sub> = 20A, V <sub>GS</sub> = 0V	-	-	1.2	V	
Continuous Source Current <sup>1,5</sup>		Is	Vg=Vp=0V , Force Current	-	-	145	Α	
Body Diode Reverse Recovery	y Time	t <sub>rr</sub>		-	58.7	-	ns	
Body Diode Reverse Recovery Charge		Q <sub>rr</sub>	l <sub>F</sub> = 20A, dl/dt=100A/μs	-	97.3	-	nC	

#### Notes:

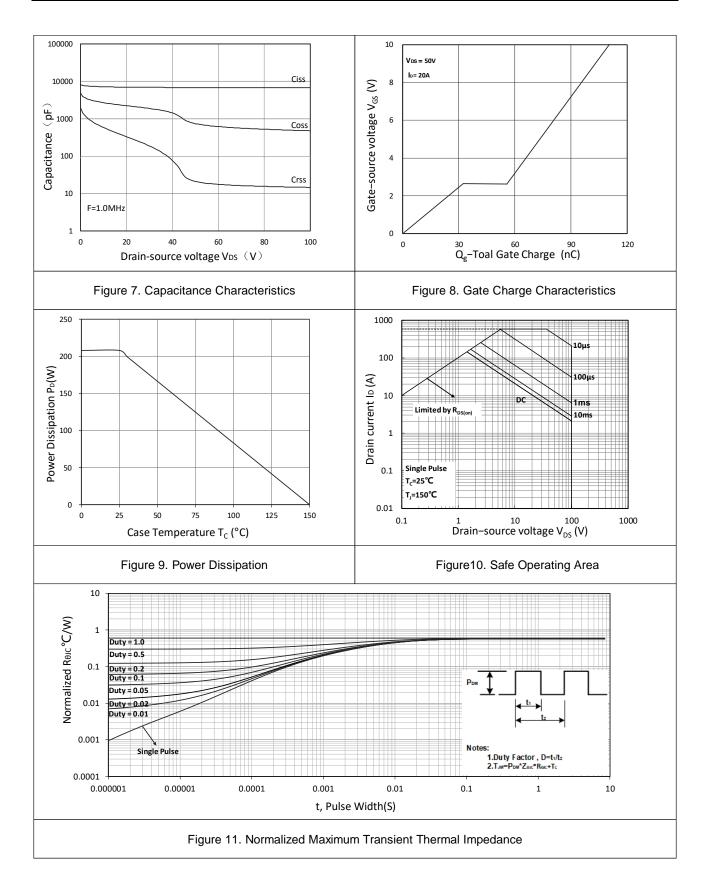
- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =35V,  $V_{GS}$ =10V, L=0.4mH,  $I_{AS}$ =40A
- 4. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}$ =150°C.
- 5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



# **Typical Characteristics**









# **Test Circuit**

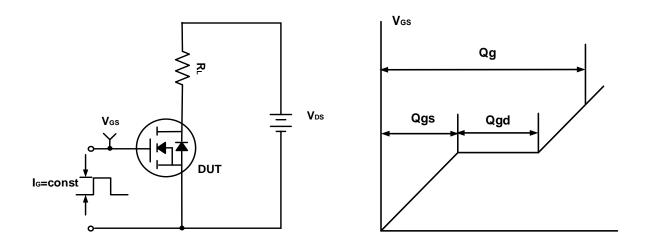


Figure A. Gate Charge Test Circuit & Waveforms

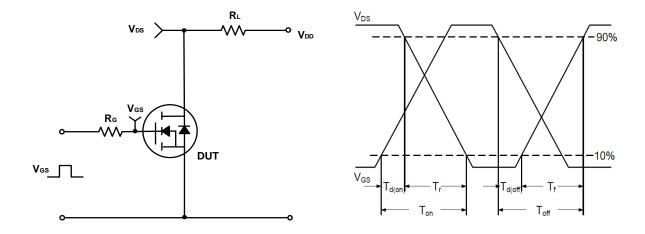


Figure B. Switching Test Circuit & Waveforms

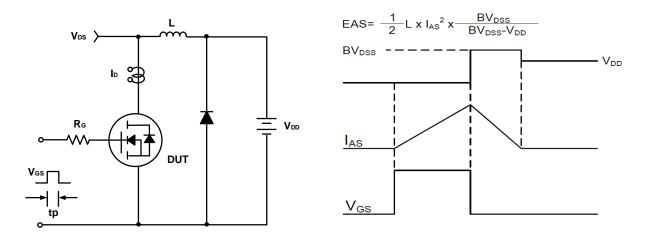
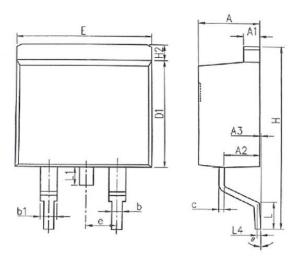
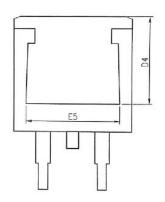


Figure C. Unclamped Inductive Switching Circuit & Waveforms



### **Mechanical Dimensions for TO-263**





### **COMMON DIMENSIONS**

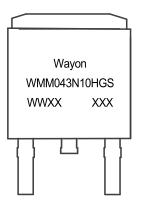
SYMBOL	MM			
STIVIDUL	MIN	MAX		
А	4.37	4.89		
A1	1.17	1.42		
A2	2.20	2.90		
A3	0.00	0.25		
b	0.70	0.96		
b1	1.17	1.47		
С	0.28	0.60		
D1	8.45	9.30		
D4	6.60	-		
Е	9.80	10.40		
E5	7.06	-		
е	2.54BSC			
Н	14.70	15.70		
H2	1.07	1.47		
L	2.00	2.80		
L1	-	1.75		
L4	0.254BSC			
θ	0°	9°		



## **Ordering Information**

Part	Package	Marking	Packing method	
WMM043N10HGS TO-263		WMM043N10HGS	Tape and Reel	

## **Marking Information**



WMM043N10HGS = Device code WWXX XXX= Date code

### **Contact Information**

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WAYON website: http://www.way-on.com

For additional information, please contact your local Sales Representative.

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