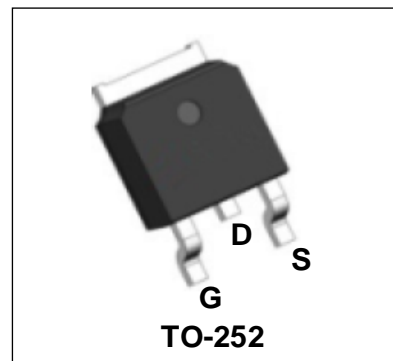


150V N-Channel Enhancement Mode Power MOSFET

Description

WMO690N15HG2 uses Wayon's 2nd generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.

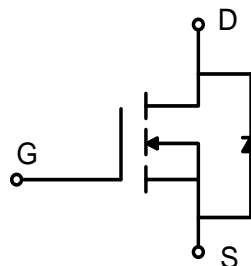


Features

- $V_{DS} = 150V$, $I_D = 20A$
 $R_{DS(on)} < 65m\Omega$ @ $V_{GS} = 10V$
- Green Device Available
- Low Gate Charge
- 100% EAS Guaranteed

Applications

- Synchronous Rectification
- LED Backlighting
- Motor Control

Absolute Maximum Ratings ($T_c = 25^\circ C$, unless otherwise noted)

| Parameter | | Symbol | Value | Unit |
|--|---------------------|----------------|------------|------------|
| Drain-Source Voltage | | V_{DS} | 150 | V |
| Gate-Source Voltage | | V_{GS} | ± 20 | V |
| Continuous Drain Current | $T_c = 25^\circ C$ | I_D | 20 | A |
| | $T_c = 100^\circ C$ | | 12.6 | |
| Pulsed Drain Current ⁴ | | I_{DM} | 80 | A |
| Single Pulse Avalanche Energy ³ | | EAS | 20 | mJ |
| Total Power Dissipation | $T_c = 25^\circ C$ | P_D | 56.8 | W |
| Operating Junction and Storage Temperature Range | | T_J, T_{STG} | -55 to 150 | $^\circ C$ |

Thermal Characteristics

| Parameter | Symbol | Value | Unit |
|--|-----------------|-------|--------------|
| Thermal Resistance from Junction-to-Ambient ¹ | $R_{\theta JA}$ | 52 | $^\circ C/W$ |
| Thermal Resistance from Junction-to-Case | $R_{\theta JC}$ | 2.2 | $^\circ C/W$ |

Electrical Characteristics (Tc = 25°C, unless otherwise noted)

| Parameter | | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
|--|-----------------------|----------------------|--|------|------|------|------|
| Static Characteristics | | | | | | | |
| Drain-Source Breakdown Voltage | | V _{(BR)DSS} | V _{GS} = 0V, I _D = 250μA | 150 | - | - | V |
| Gate-body Leakage current | | I _{GSS} | V _{DS} = 0V, V _{GS} = ±20V | - | - | ±100 | nA |
| Zero Gate Voltage Drain Current | T _J =25°C | I _{DSS} | V _{DS} =150V, V _{GS} = 0V | - | - | 1 | μA |
| | T _J =100°C | | | - | - | 100 | |
| Gate-Threshold Voltage | | V _{GS(th)} | V _{DS} = V _{GS} , I _D = 250μA | 2 | 3 | 4 | V |
| Drain-Source on-Resistance ² | | R _{DS(on)} | V _{GS} = 10V, I _D = 10A | - | 51 | 65 | mΩ |
| Forward Transconductance ² | | g _{fs} | V _{DS} = 5V, I _D = 10A | - | 19 | - | S |
| Dynamic Characteristics | | | | | | | |
| Input Capacitance | | C _{iss} | V _{DS} = 75V, V _{GS} =0V, f =1MHz | - | 655 | - | pF |
| Output Capacitance | | C _{oss} | | - | 45.5 | - | |
| Reverse Transfer Capacitance | | C _{rss} | | - | 2.7 | - | |
| Switching Characteristics | | | | | | | |
| Gate Resistance | | R _g | V _{GS} = 0V, V _{DS} = 0V, f =1MHz | - | 2 | - | Ω |
| Total Gate Charge | | Q _g | V _{GS} = 10V, V _{DS} = 75V, I _D =10A | - | 7.8 | - | nC |
| Gate-Source Charge | | Q _{gs} | | - | 2.1 | - | |
| Gate-Drain Charge | | Q _{gd} | | - | 0.6 | - | |
| Turn-on Delay Time | | t _{d(on)} | V _{GS} =10V, V _{DS} =75V, R _G = 10Ω, I _D = 10A | - | 7.5 | - | ns |
| Rise Time | | t _r | | - | 3.8 | - | |
| Turn-off Delay Time | | t _{d(off)} | | - | 10.5 | - | |
| Fall Time | | t _f | | - | 2.6 | - | |
| Drain-Source Body Diode Characteristics | | | | | | | |
| Diode Forward Voltage ² | | V _{SD} | I _S = 10A, V _{GS} = 0V | - | - | 1.2 | V |
| Continuous Source Current ^{1,5} | | I _S | V _G =V _D =0V , Force Current | - | - | 20 | A |
| Body Diode Reverse Recovery Time | | t _{rr} | I _F = 10A, dI/dt=100A/μs | - | 46 | - | ns |
| Body Diode Reverse Recovery Charge | | Q _{rr} | | - | 50 | - | nC |

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is $V_{DD} = 25V, V_{GS} = 10V, L = 0.4mH, I_{AS} = 10A$
4. Repetitive rating, pulse width limited by junction temperature $T_J(MAX) = 150^\circ C$
5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

Typical Characteristics

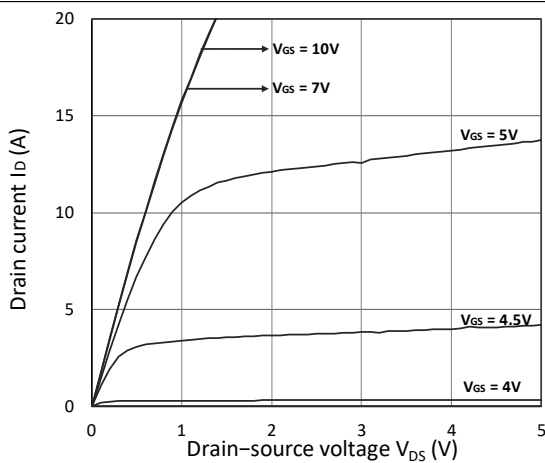


Figure 1. Output Characteristics

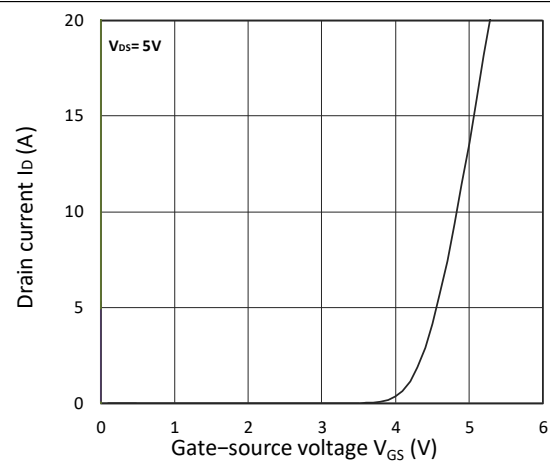


Figure 2. Transfer Characteristics

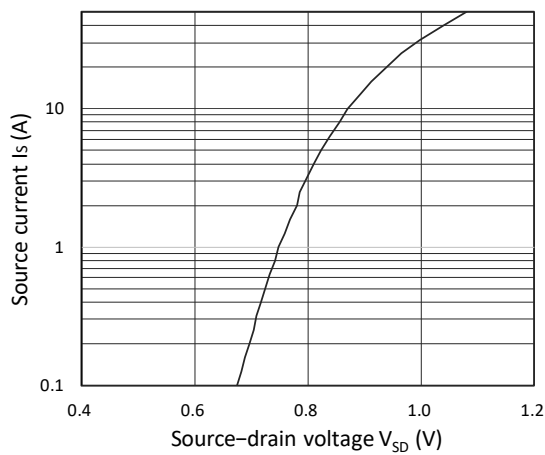
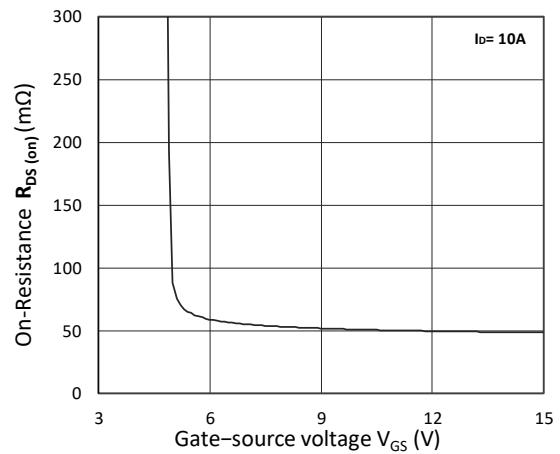
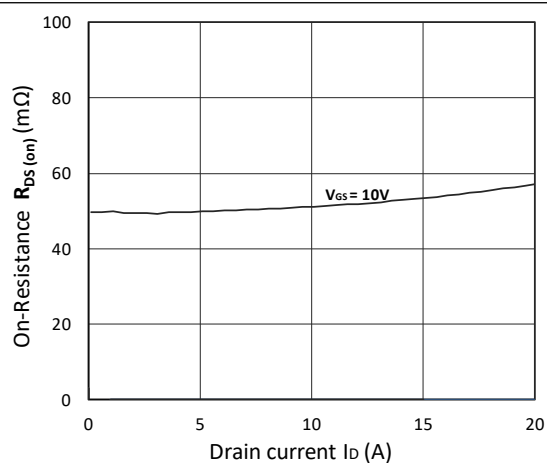
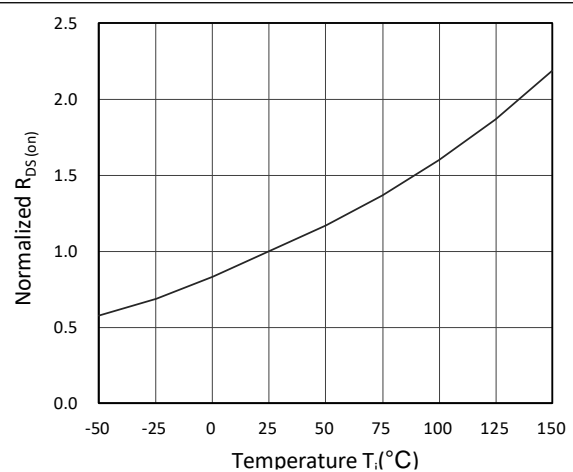


Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{DS(on)}$ vs. V_{GS} Figure 5. $R_{DS(on)}$ vs. I_D Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

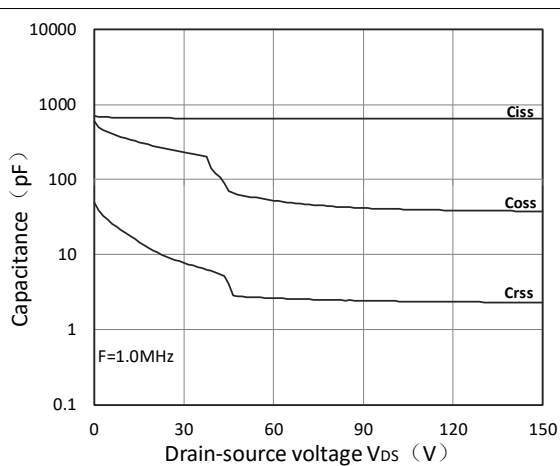


Figure 7. Capacitance Characteristics

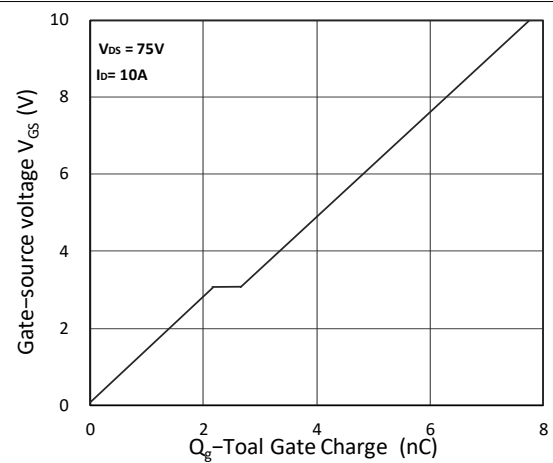


Figure 8. Gate Charge Characteristics

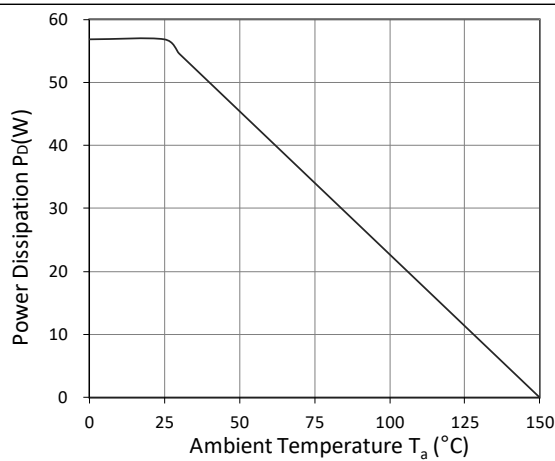


Figure 9. Power Dissipation

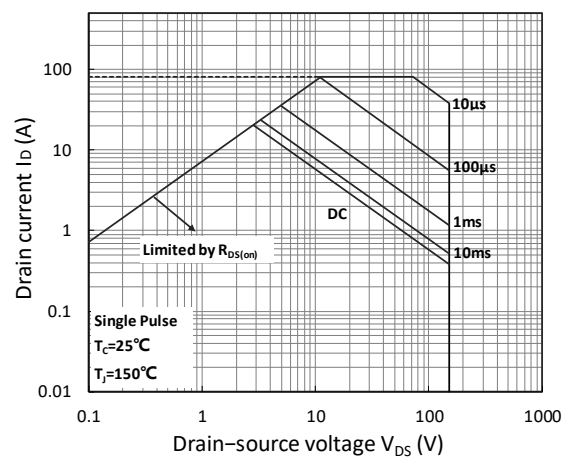


Figure 10. Safe Operating Area

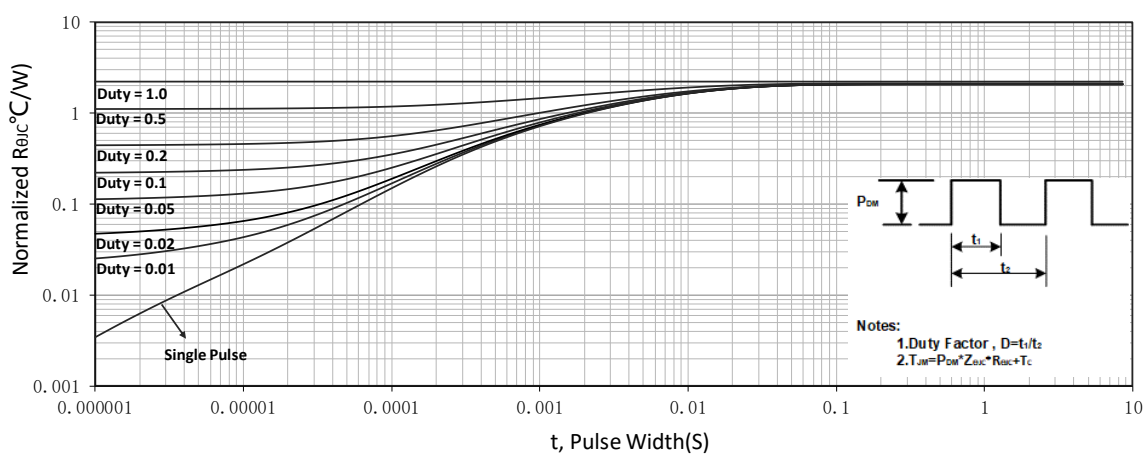


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

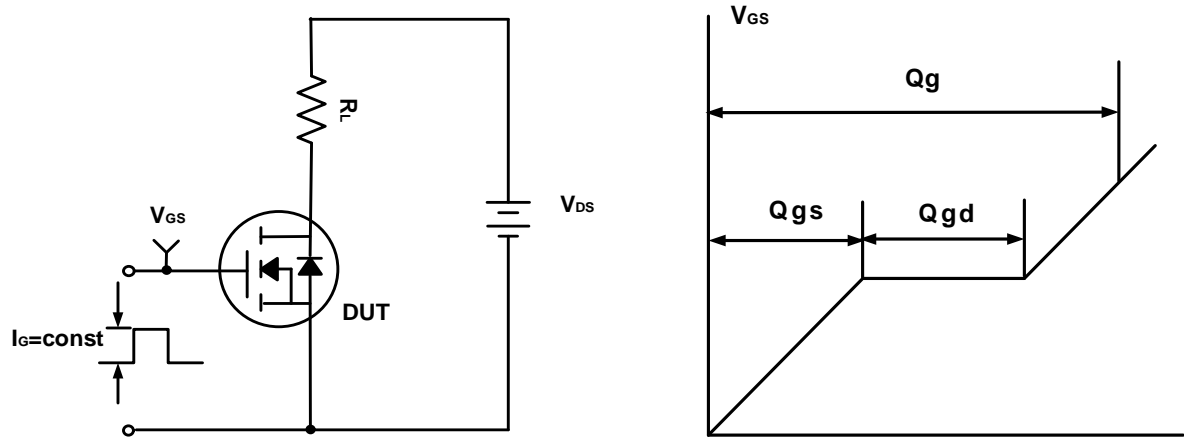


Figure A. Gate Charge Test Circuit & Waveforms

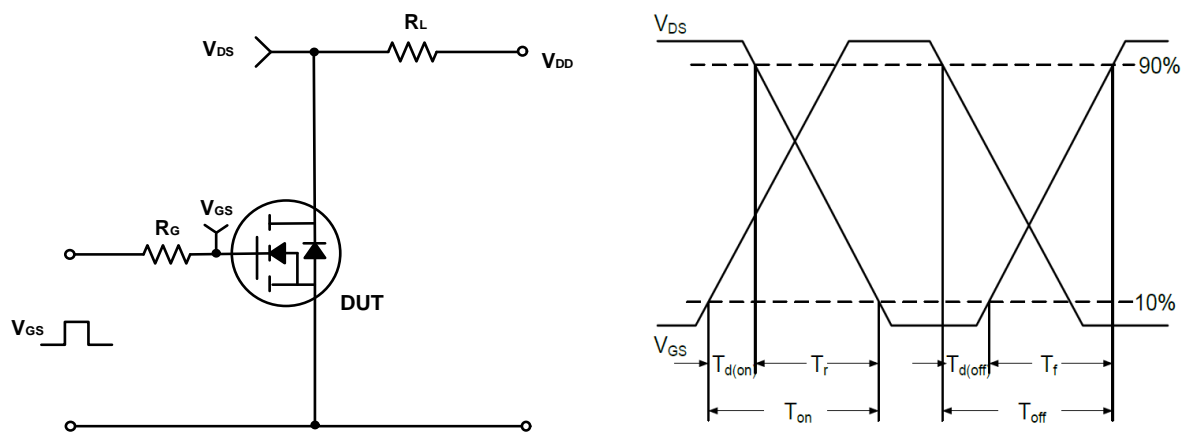


Figure B. Switching Test Circuit & Waveforms

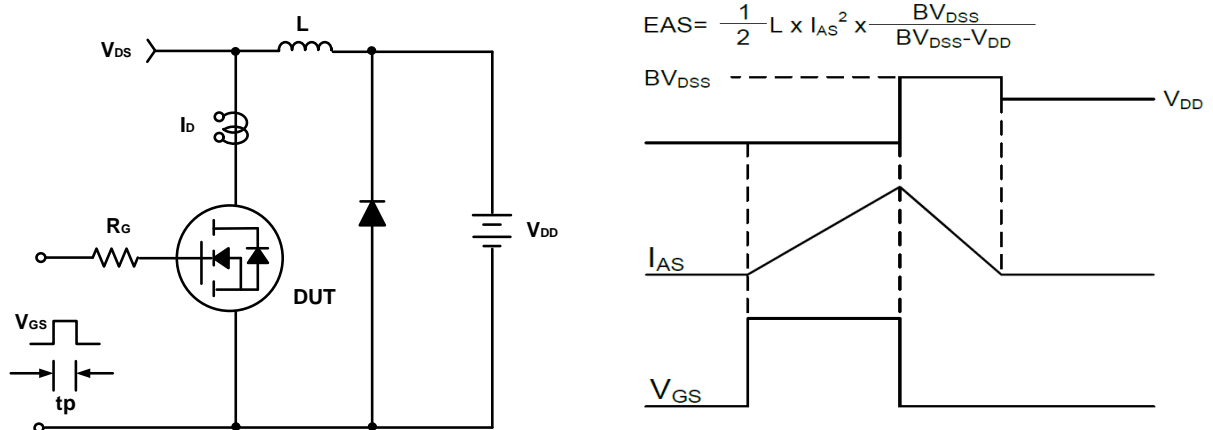
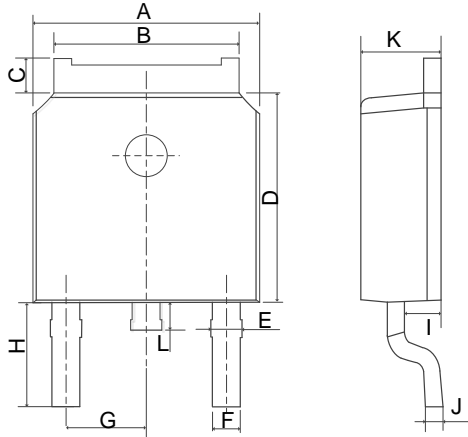


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for TO-252

COMMON DIMENSIONS

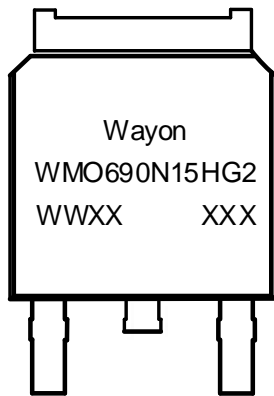


| SYMBOL | MM | |
|--------|---------|------|
| | MIN | MAX |
| A | 6.40 | 6.80 |
| B | 5.13 | 5.50 |
| C | 0.88 | 1.28 |
| D | 5.90 | 6.22 |
| E | 0.68 | 1.10 |
| F | 0.68 | 0.91 |
| G | 2.29REF | |
| H | 2.90REF | |
| I | 0.85 | 1.17 |
| J | 0.51REF | |
| K | 2.10 | 2.50 |
| L | 0.40 | 1.00 |

Ordering Information

| Part | Package | Marking | Packing method |
|--------------|---------|--------------|----------------|
| WMO690N15HG2 | TO-252 | WMO690N15HG2 | Tape and Reel |

Marking Information



WMO690N15HG2= Device code

WWXX XXX= Date code

Contact Information

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