

60V N-Channel Enhancement Mode Power MOSFET

Description

WMS13N06T1 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- $V_{DS} = 60V$, $I_D = 13A$
 $R_{DS(on)} < 8.5m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 12m\Omega$ @ $V_{GS} = 4.5V$
- Low $R_{DS(on)}$
- Low Gate Charge
- 100% EAS Guaranteed

Applications

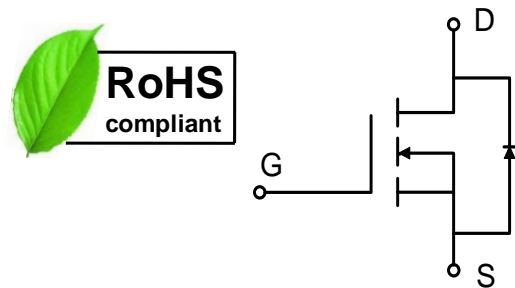
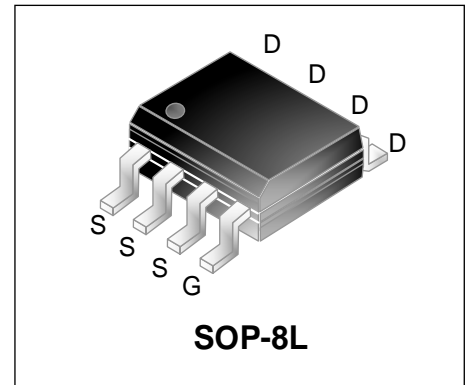
- Power Management Switches
- Synchronous Rectification for AC/DC Quick Charger

Absolute Maximum Ratings

| Parameter | | Symbol | Value | Unit |
|--|---------------------|----------------|-------------|------------|
| Drain-Source voltage | | V_{DS} | 60 | V |
| Gate-Source voltage | | V_{GS} | ± 20 | V |
| Continuous Drain Current@10V ¹ | $T_A = 25^\circ C$ | I_D | 13 | A |
| | $T_A = 100^\circ C$ | | 8 | |
| Pulsed Drain Current ² | | I_{DM} | 60 | A |
| Single Pulse Avalanche Energy ³ | | EAS | 80 | mJ |
| Avalanche Current | | I_{AS} | 40 | A |
| Total Power Dissipation ⁴ | $T_A = 25^\circ C$ | P_D | 2.7 | W |
| Operating Junction and Storage Temperature Range | | T_J, T_{STG} | -55 to +150 | $^\circ C$ |

Thermal Characteristics

| Parameter | Symbol | Value | Unit |
|---|-----------------|-------|--------------|
| Thermal Resistance from Junction-to-Ambient ¹ ($t \leq 10S$) | $R_{\theta JA}$ | 45 | $^\circ C/W$ |
| Thermal Resistance from Junction-to-Ambient ¹ | $R_{\theta JA}$ | 80 | $^\circ C/W$ |



Electrical Characteristics $T_c = 25^\circ\text{C}$, unless otherwise noted

| Parameter | | Symbol | Test Conditions | Min. | Typ. | Max. | Unit |
|--|----------------------|----------------------|---|------|------|------|------|
| Static Characteristics | | | | | | | |
| Drain-Source Breakdown Voltage | | V _{(BR)DSS} | V _{GS} = 0V, I _D = 250μA | 60 | - | - | V |
| Gate-body Leakage current | | I _{GSS} | V _{DS} = 0V, V _{GS} = ±20V | - | - | ±100 | nA |
| Zero Gate Voltage Drain Current | T _J =25°C | I _{DSS} | V _{DS} = 48V, V _{GS} = 0V | - | - | 1 | μA |
| | T _J =55°C | | | - | - | 5 | |
| Gate-Threshold Voltage | | V _{GS(th)} | V _{DS} = V _{GS} , I _D = 250μA | 1.2 | - | 2.5 | V |
| Drain-Source On-Resistance ² | | R _{DS(on)} | V _{GS} = 10V, I _D = 10A | - | - | 8.5 | mΩ |
| | | | V _{GS} = 4.5V, I _D = 8A | - | - | 12 | |
| Forward Transconductance | | g _{fs} | V _{DS} = 5V I _D = 10A | - | 50 | - | S |
| Dynamic Characteristics | | | | | | | |
| Input Capacitance | | C _{iss} | V _{DS} = 25V, V _{GS} =0V, f =1MHz | - | 3307 | - | pF |
| Output Capacitance | | C _{oss} | | - | 201 | - | |
| Reverse Transfer Capacitance | | C _{rss} | | - | 151 | - | |
| Switching Characteristics | | | | | | | |
| Total Gate Charge | | Q _g | V _{GS} = 10V,V _{DS} = 48V, I _D = 10A | - | 57 | - | nC |
| Gate-Source Charge | | Q _{gs} | | - | 8.7 | - | |
| Gate-Drain Charge | | Q _{gd} | | - | 14 | - | |
| Turn-On Delay Time | | t _{d(on)} | V _{GS} =10V, V _{DD} = 30V,R _G = 3.3Ω, I _D = 10A | - | 16.2 | - | nS |
| Rise Time | | t _r | | - | 41.2 | - | |
| Turn-Off Delay Time | | t _{d(off)} | | - | 56.4 | - | |
| Fall Time | | t _f | | - | 16.2 | - | |
| Drain-source body diode Characteristics | | | | | | | |
| Diode Forward Voltage ² | | V _{SD} | I _S = 1A, V _{GS} = 0V | - | - | 1.2 | V |
| Continuous Source Current ^{1,5} | | I _S | V _G =V _D =0V , Force Current | - | - | 10 | A |
| Pulsed Source Current ^{2,5} | | I _{SM} | | - | - | 60 | |
| Body Diode Reverse Recovery Time | | t _{rr} | I _F = 10A, dI/dt = 100A/μs | - | 24 | - | nS |
| Body Diode Reverse Recovery Charge | | Q _{rr} | | - | 15 | - | nC |

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is $V_{DD} = 50V, V_{GS} = 10V, L = 0.1mH, I_{AS} = 40A$
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

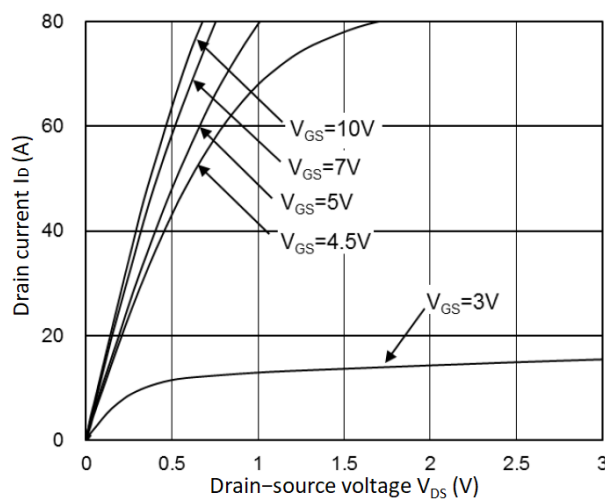


Fig1. Typical Output Characteristics

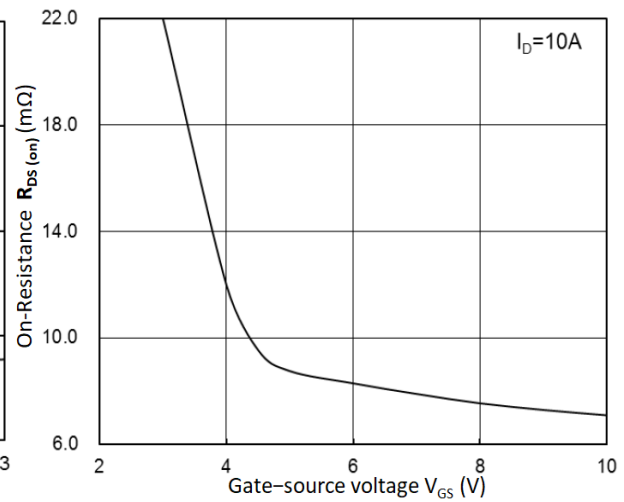
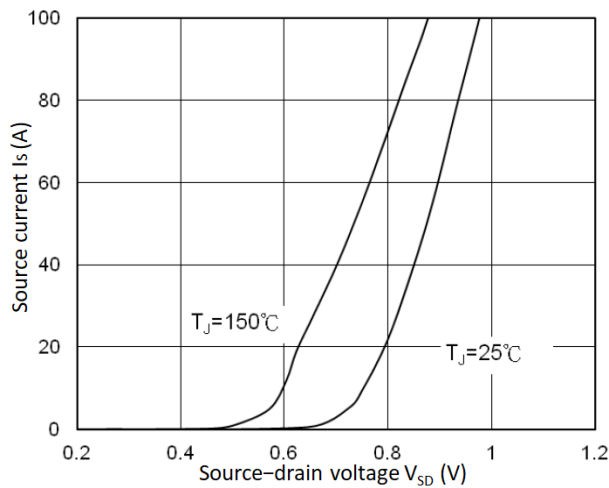
Fig2. $R_{DS(on)}$ vs. V_{GS} 

Fig3. Forward Characteristics Of Reverse

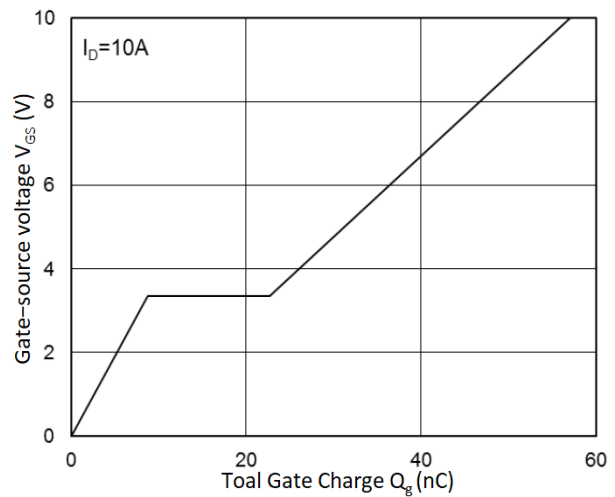
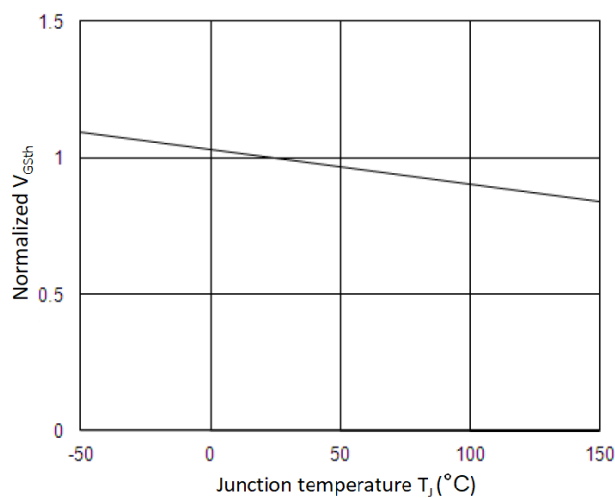
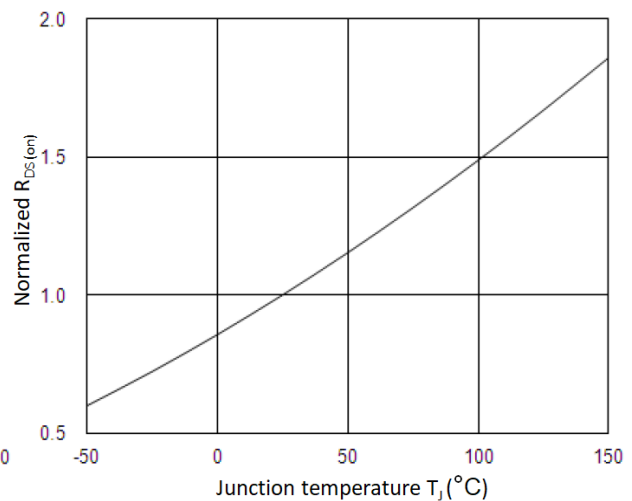


Fig4. Gate Charge Characteristics

Fig5. Normalized $V_{GS(th)}$ vs. T_J Fig6. Normalized $R_{DS(on)}$ vs. T_J

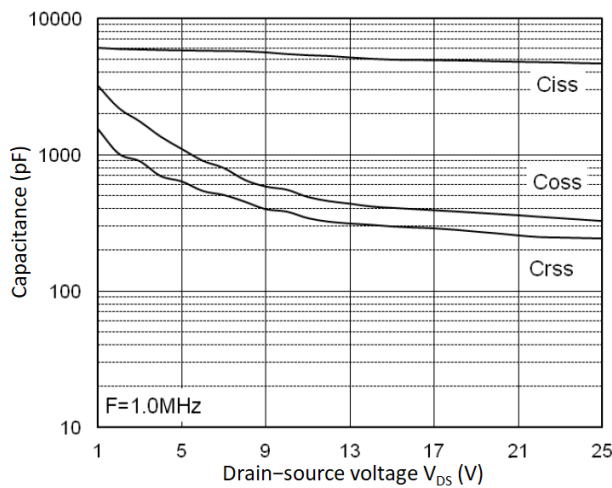


Fig7. Capacitance

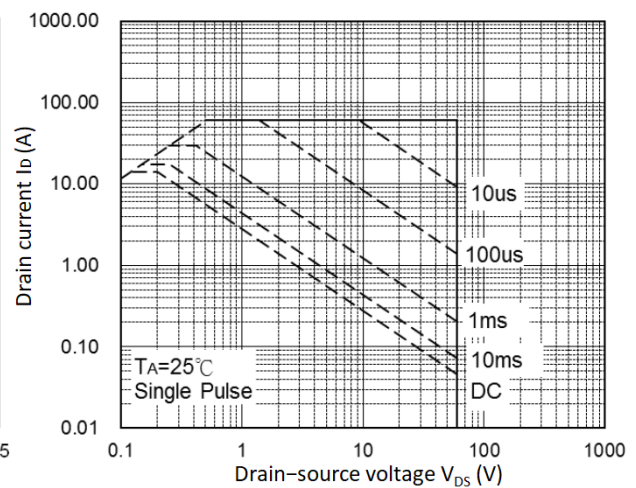


Fig8. Safe Operating Area

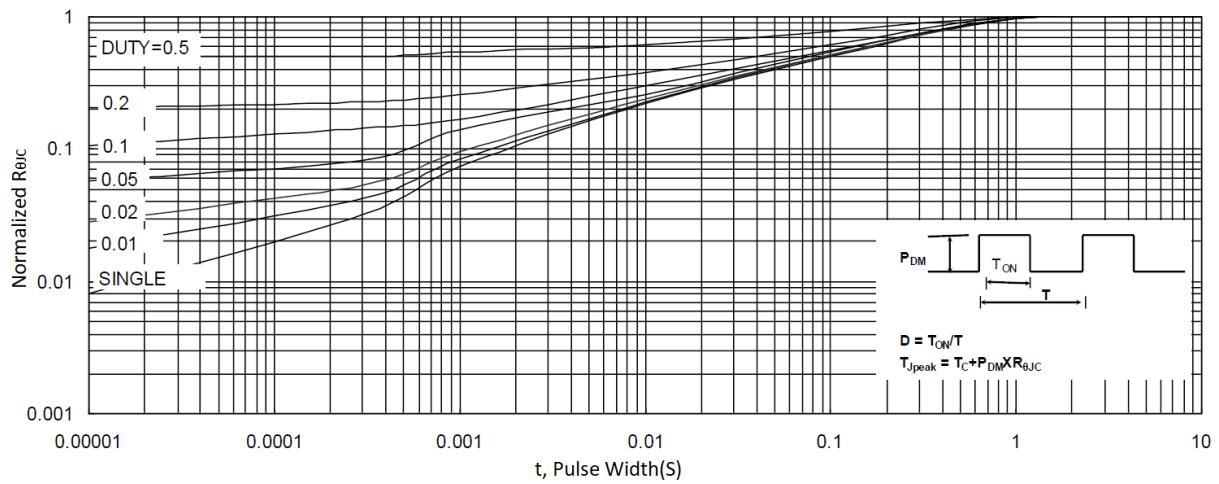


Fig9. Normalized Maximum Transient Thermal Impedance

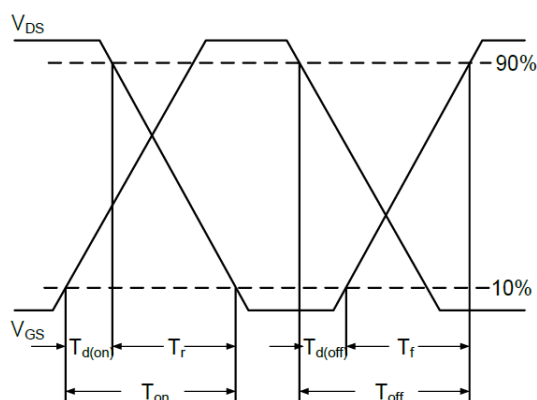
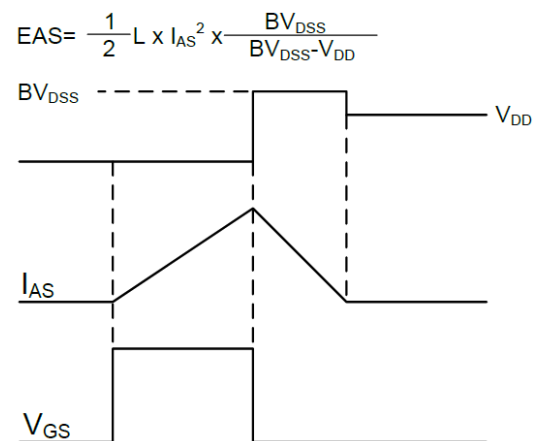
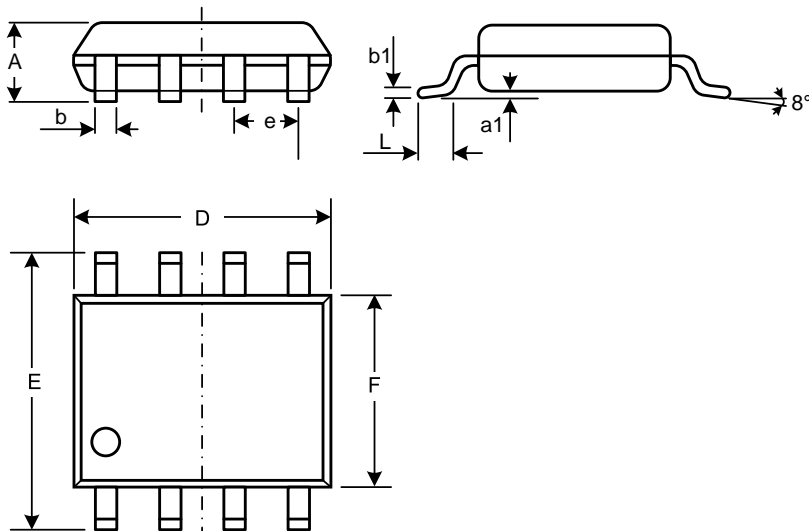


Figure 10. Switching Time Waveform

Figure 11. Unclamped Inductive Switching
Waveform

Mechanical Dimensions for SOP-8L



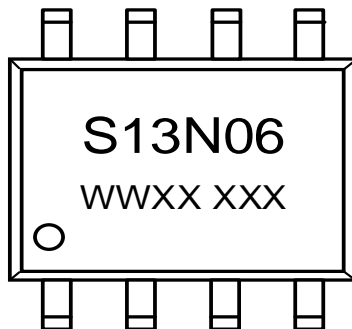
COMMON DIMENSIONS

| SYMBOL | MM | |
|--------|------|------|
| | MIN | MAX |
| A | 1.23 | 1.75 |
| a1 | 0.05 | 0.25 |
| b | 0.31 | 0.51 |
| b1 | 0.16 | 0.25 |
| D | 4.70 | 5.15 |
| E | 5.75 | 6.25 |
| e | 1.07 | 1.47 |
| F | 3.70 | 4.10 |
| L | 0.4 | 1.27 |

Ordering Information

| Part | Package | Marking | Packing method |
|------------|---------|---------|----------------|
| WMS13N06T1 | SOP-8L | S13N06 | Tape and Reel |

Marking Information



S13N06 = Device code

WWXX XXX= Date code

Contact Information

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