Fig 1: System level block diagram
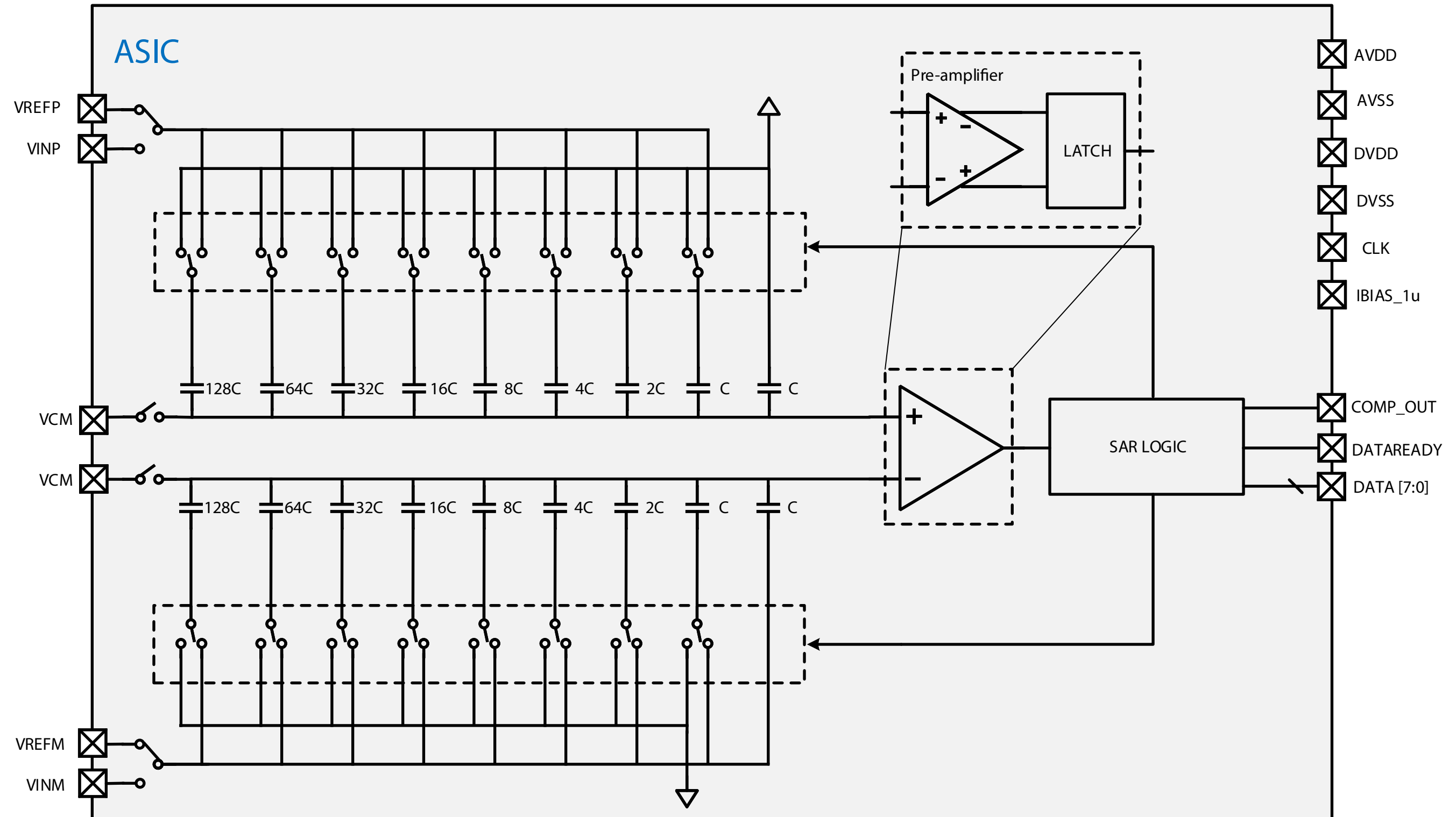

Fig 2: Fully-differential ADC architecture