

Source: 2020PHYS201/KBe20phys250retCapacitorsLab.md

#ret

1 | Introduction

The purpose of this lab is to verify the relation between capacitance, resistance, voltage, and charge time of a simple capacitor circuit. The equation that will be verified is

$$V_{cap} = V_{bat} \left(1 - e^{-\frac{t}{\tau}} \right)$$

where each variable has the following meaning:

Variable	Units	Description
t	Seconds (s)	Time elapsed since charging of the capacitor started. May be represented as $t - t_0$, where t is the current absolute time and t_0 is the absolute start time.
V_{cap}	Volts (V)	Voltage across the capacitor after a given elapsed time
V_{bat}	Volts (V)	Voltage of the battery, assumed to be constant.
τ	Seconds (s)	“Time constant” that scales the equation to the circuit. Equal to the product of the resistance and capacitance of the circuit (RC), and roughly equal to the number of seconds required to charge the capacitor to $\frac{2}{3}$ of V_{bat} .

2 | Charging Capacitors

2.1 | Voltage over Time

2.1.1 | General Procedure

A number of circuits were built and capacitors measured using a voltage and amperage probe with Logger Pro. The raw data can be found on Canvas. Desmos was used to manually fit curves to the data, using a modified version of the model with t and t_0 variables to truncate scrap data:

$$V_{cap} = V_{bat} \left(1 - e^{-\frac{t-t_0}{\tau}} \right)$$

The results of the fits are summarized below:





The scattered points near the x-axis are residuals between the curve fit and the collected data. The fit heuristic was to visually center the dots around the x-axis.

2.1.2 | Uncertainties

Because each fit was done manually, the absolute error was difficult to estimate because there was no correct answer. Instead, error was taken as the precision at which the number was considered “close enough” by the human doing the curve fit. Depending on the size of the value (and scale of the Desmos slider), this results in an absolute error ranging from 0.1 units to 0.001 units.

These uncertainties were propagated through calculations as follows:

$$\delta(A + B) = \sqrt{\delta A^2 + \delta B^2}$$

for additive combinations and

$$\frac{\delta(AB)}{AB} = \sqrt{\left(\frac{\delta A}{A}\right)^2 + \left(\frac{\delta B}{B}\right)^2}$$

for multiplicative ones.

The manufacturing tolerances of electronic components were taken into account as well. Resistor tolerances were based on the absolute difference between standard resistances and those measured by a multimeter. Capacitance tolerances were based on the tolerances of a similar capacitor for which the data sheet was available: the FOAI CD110 radial capacitor, which had capacitance tolerances of $\pm 20\%$.

2.1.3 | Results

Ω	F	Fit τ (s)	Modeled τ (s)	% Error
100k Ω	22 μ F	2.3860	2.2000	8.45%
33k Ω	1000 μ F	0.0017	0.0016	9.61%
100 Ω	1000 μ F	0.1058	0.1000	5.80%
15 Ω	1000 μ F	0.0188	0.0150	25.33%

Assuming perfect capacitor manufacturing, the curve fit results fell within uncertainties of the model predicted τ value for all circuits except the first. This is likely due to the high uncertainties placed on smaller scale curve fits, especially the final trial. Furthermore, with the unexpectedly large 20% uncertainties in capacitance all trials fell within the uncertainties of the experiment. Thus, the model predicted the charging behavior of capacitors over time accurately.

The exact numerical analysis for this section can be found [here](#).

2.2 | Integration of Current

Another way to evaluate the model is to compare the amount of transferred charge. For this comparison, a modified equation is used:

$$C_{cap} = C_{max} \left(1 - e^{-\frac{t-t_0}{\tau}} \right)$$

This is similar to the model used in the previous procedure, but voltage variables are replaced with charge. Current flow through the circuit is used as a proxy to calculate the amount of charge accumulated in the capacitor. As before, current is measured across a resistor using a digital probe and Logger Pro.

The amount of current on the capacitor was calculated by taking a Riemann sum of the momentary current measurements. In addition, the voltage measurements across the capacitor were used as another proxy to calculate the amount of charge on the capacitor. A graph of these three theoretically identical datasets:

Accumulated Charge

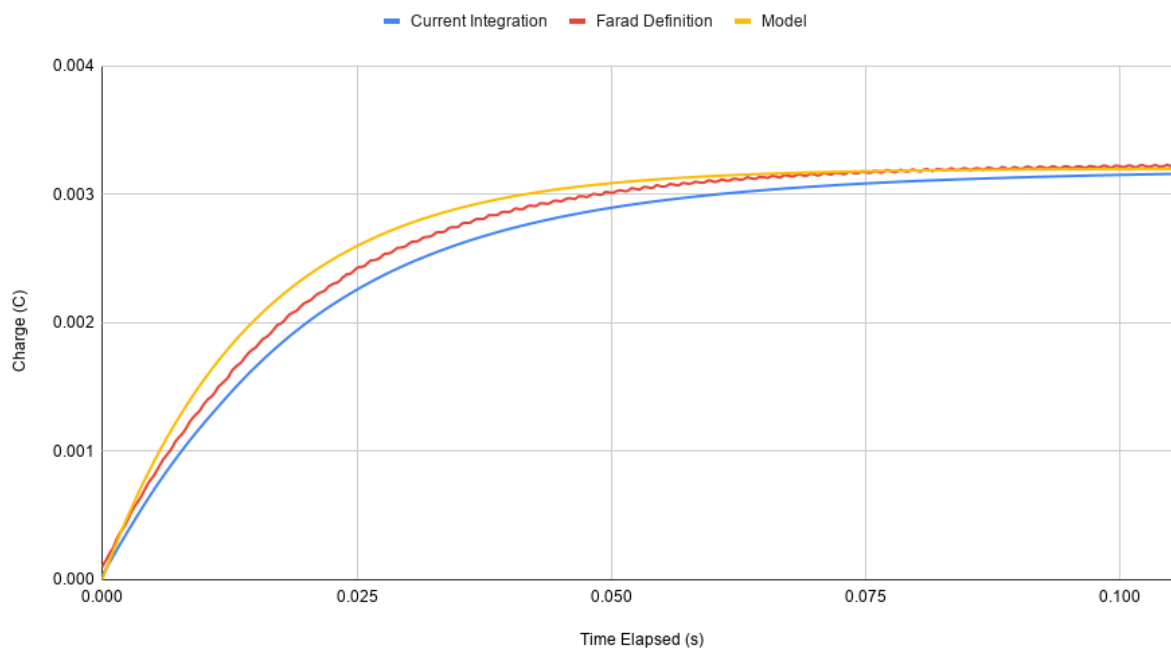


Figure 1: Accumulated Charge Graphs

3 | Time Constant with Various Components

Most of the analysis for data collected as a class was done on the “charge rate constant” (t_{time}/RC) of each circuit, defined as:

$$\frac{t}{RC}$$

t_{time}/RC is a unitless scalar that represents how quickly it takes to charge any capacitor for a given voltage. Voltage data was not collected during the experiment, so the voltage is assumed to be constant across trials. If our model of capacitor charge rate is correct, we expect t_{time}/RC to be constant across trials. The actual data was skewed right:

Histogram of t_{time}/RC

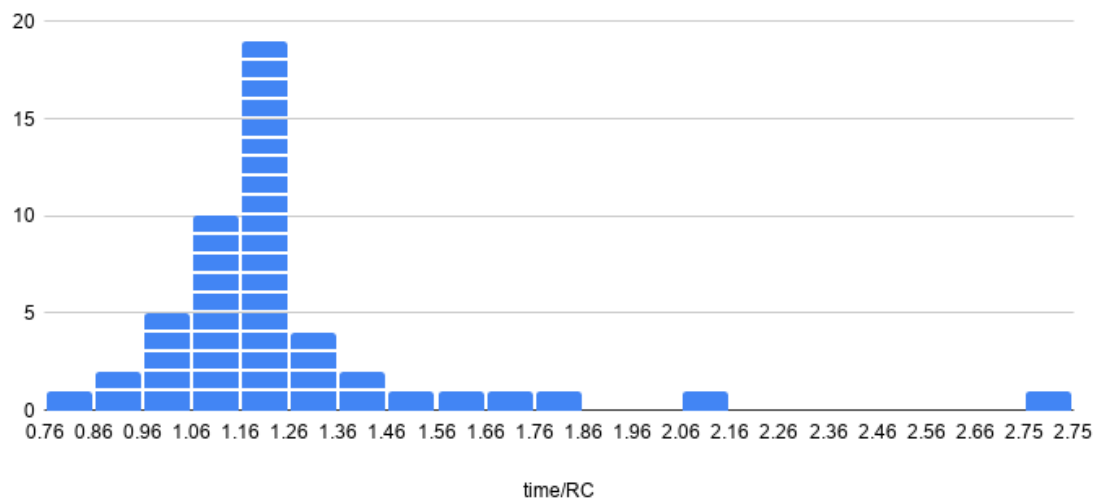


Figure 2: Histogram of t_{time}/RC

By comparing the t_{time}/RC and different properties of each circuit, reasoning for the outliers may be deduced. The visualization of the histogram is reflected in each of the following charts by the density of points in a column.

First, the t_{time}/RC values were compared with the resistance of the circuit:

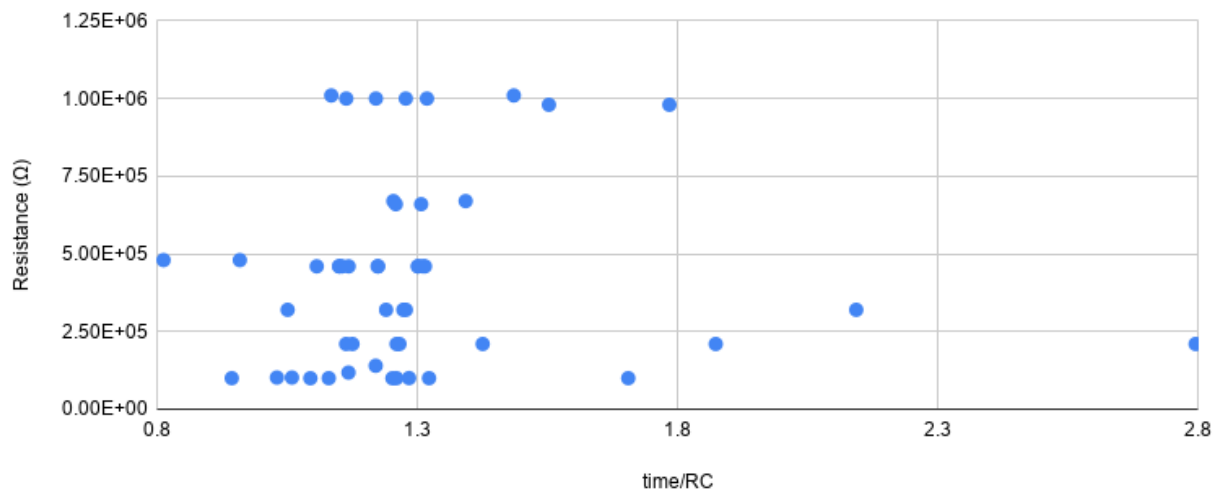
time/RC vs. $R (\Omega)$ 

Figure 3: time/RC vs Resistance

Although some outliers came from circuits with high resistance, the most skewed ones came from those with least resistance. The same can be said of capacitance:

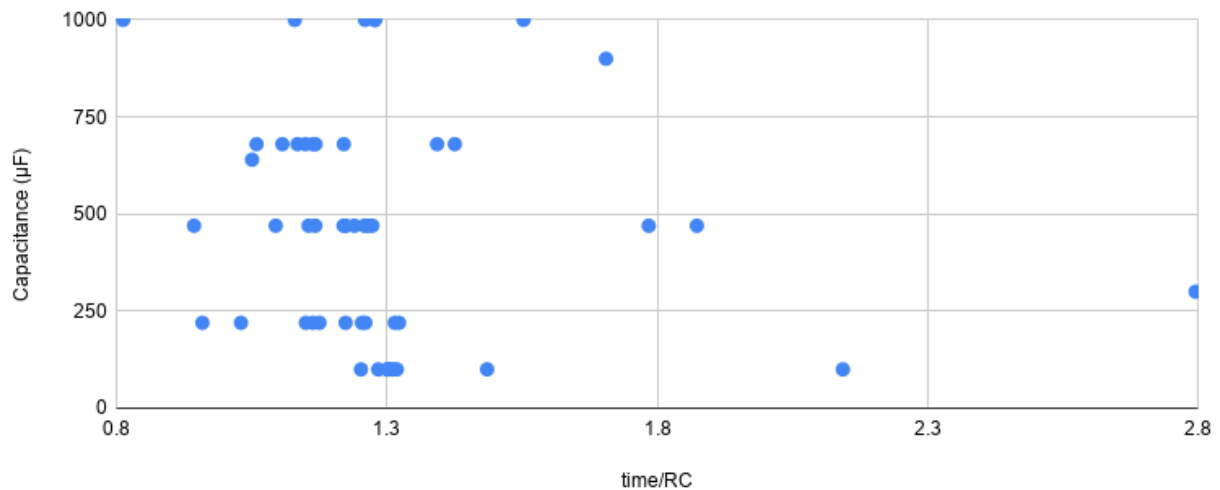
time/RC vs. $C (\mu F)$ 

Figure 4: time/RC vs Capacitance

Finally, the comparison with time taken to charge the capacitor shows the strongest correlation, which is to be expected because according to the model, this value is the product of the previous two trends.

time/RC vs. Time to 2V (s)

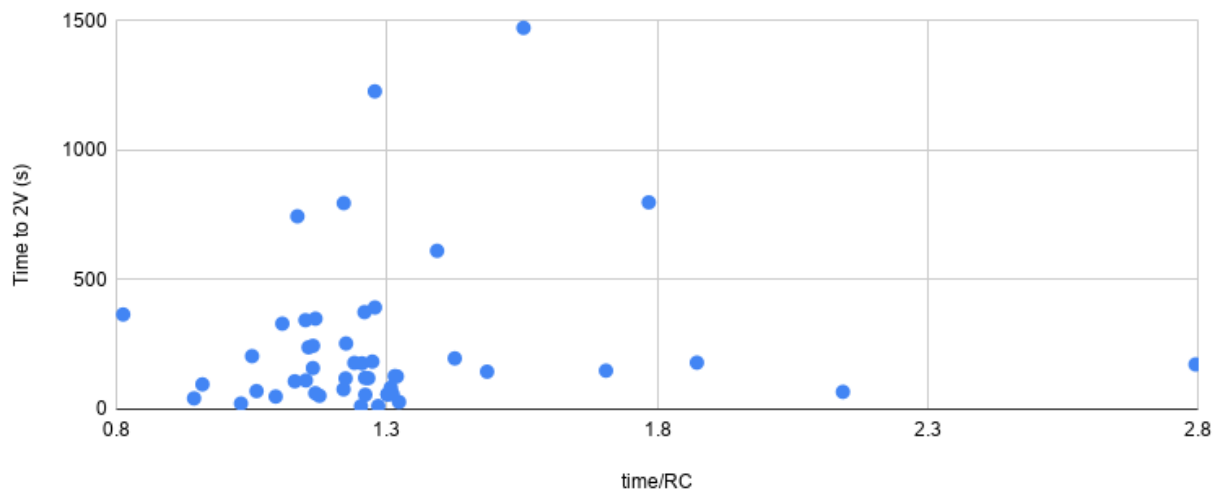


Figure 5: time/RC vs Time to Charge

On explanation for the data skew is reaction time: for lower values of τ , the capacitor plateaus faster near 2V and thus the time keeper may not react as quickly. Components with smaller ratings also need tighter tolerances to achieve the same relative tolerances, so smaller capacitors may have relatively higher manufactured variability.

The source analysis for these conclusions can be found [here](#).