	float local	float shared	float shared volatile	double local	double shared	double shared volatile
(14, 32)	40.51	51.15	61.07	132.84	133.02	153.82
(14, 64)	40.74	51.31	61.64	137.1	137.24	157.89
(14, 128)	45.72	56.57	67.14	162.78	162.94	182.88
(14, 256)	72.99	84.39	96.18	293.51	293.49	312.74
(14, 512)	141.3	159.5	178.02	**307.47**	**283.13**	439.27
(14, 1024)	141.66	158.26	174.88			
(28, 32)	40.27	51.28	61.6	137.11	137.25	156.71
(28, 64)	42.04	53.18	63.79	146.09	146.27	166.51
(28, 128)	59.52	70.08	80.89	233.91	234.05	256.33
(28, 256)	107.33	124.07	142.11	358.04	358.05	390.79
(28, 512)	*178.7*	**159.68**	*178.27*	563.55	563.78	610.15
(28, 1024)	280.58	313.77	347.09			
(56, 32)	41.97	53.14	63.68	146.01	146.23	166.38
(56, 64)	53.5	63.91	74.73	199.15	199.27	219.84
(56, 128)	92.4	109.8	127.75	**381.23**	**348.81**	384.47
(56, 256)	160.12	182.21	203.29	624.65	*637.08*	*627.92*
(56, 512)	280.67	314.79	349.64	1124.22	1125.05	1215.87
(56, 1024)	558.18	624.81	691.21			
(112, 32)	47.35	57.98	68.46	165.48	165.55	185.17
(112, 64)	77.26	89.32	102.72	**320.34**	*320.41*	***315.95***
(112, 128)	142.09	162.53	185.1	**640.87**	**621.2**	646.11
(112, 256)	299.13	317.92	355.2	1133.09	1133.1	1234.69
(112, 512)	557.45	625.05	692.31	2245.47	2247.29	2427.22
(112, 1024)	1113.09	1246.78	1378.88			
(224, 32)	92.33	113.6	134.68	328.03	328.12	362.32
(224, 64)	150.05	172.91	197.43	597.67	597.69	629.87
(224, 128)	281.14	319.1	362.91	**1307.3**	**1210.64**	**1274.69**
(224, 256)	575.95	644.75	706.68	2298.19	2310.33	2446.42
(224, 512)	1136.35	1245.38	1377.22	4487.5	4491.74	4849.74
(224, 1024)	2222.98	2490.92	2754.44			

(448, 32)	180.75	224.34	266.07	651.32	651.55	719.69
(448, 64)	288.7	335.46	379.3	1157.16	1157.17	1247.64
(448, 128)	558.98	634.43	711.4	**2329.06**	**2295.98**	2496.84
(448, 256)	1141.78	1268.91	1388.22	4479.88	4479.92	4870
(448, 512)	2220.66	2489.1	2746.96	8971.52	8980.48	9695.04
(448, 1024)	4442.59	4978.92	5505.34			
SUM	**17906.83**	**20099.3**	**22336.3**	**36060.54**	**35896.83**	**38842.96**
Compared to local memory		**-12.24%**	**-24.74%**		**0.45%**	**-7.72%**

Register usage

Plan	Float local	Float shared	Float shared volatile	Double local	Double shared	Double shared volatile
PE	20	22	22	32	33	29
DTLA	19	24	23	34	35	30
TLAP	19	24	23	34	35	30
TI	20	24	24	34	35	30
DA	25	26	25	45	46	34
DTI	25	27	26	46	46	34

S.Mem/block

	Float local	Float shared	Float shared volatile	Double local	Double shared	Double shared volatile
1state, 32threads	0	768	768	0	1536	1536
2state, 32threads	0	1536	1536	0	3072	3072
1state, 64threads	0	1536	1536	0	3072	3072
2state, 64threads	0	3072	3072	0	6144	6144
1state, 128thread	0	3072	3072	0	6144	6144
2state, 128thread	0	6144	6144	0	12288	12288
1state, 256thread	0	6144	6144	0	12288	12288
2state, 256thread	0	12288	12288	0	24576	24576
1state, 512thread	0	12288	12288	0	24576	24576
2state, 512thread	0	24576	24576	0	49152	49152
1state, 1024threa	0	24576	24576			
2state, 1024threa	0	49152	49152			