

ESE 381

Lab 11: Air Monitoring System II - Sensirion Driver Operation of SCD41 CO₂, Humidity, and
Temperature Sensor

5/1/2025

Muhammad Sharjeel and Naafiul Hossain

115185427

115107623

Section: L02 Bench 7

```
1  /*
2  * sensirion_i2c.c
3  *
4  * Created: 4/27/2025 2:16:57 AM
5  * Author: Naafiul Hossain
6  */
7  #include "sensirion_i2c_hal.h"
8  #include "sensirion_common.h"
9  #include "sensirion_config.h"
10 #include <avr/io.h>
11 #include <util/delay.h>
12 #include <stdint.h>
13
14 /*
15  * INSTRUCTIONS
16  * =====
17  *
18  * Implement all functions where they are marked as IMPLEMENT.
19  * Follow the function specification in the comments.
20  */
21
22 /**
23  * Select the current i2c bus by index.
24  * All following i2c operations will be directed at that bus.
25  *
26  * THE IMPLEMENTATION IS OPTIONAL ON SINGLE-BUS SETUPS (all sensors on the
27  *   same
28  *   bus)
29  * @param bus_idx    Bus index to select
30  * @returns           0 on success, an error code otherwise
31  */
32 int16_t sensirion_i2c_hal_select_bus(uint8_t bus_idx) {
33     // prof short said we can ignore
34     /* TODO:IMPLEMENT or leave empty if all sensors are located on one single
35      * bus
36      */
37     return NOT_IMPLEMENTED_ERROR;
38 }
39
40 /**
41  * Initialize all hard- and software components that are needed for the I2C
42  * communication.
43  */
44 void sensirion_i2c_hal_init(void) {
45     /* TODO:IMPLEMENT */
46     // same as lab 10
47     PORTMUX.TWIROUTEA = PORTMUX_TWI0_ALT1_gc; // Use PA2 (SDA) and PA3 (SCL)
48     TWI0.MBAUD = 0x01; // 400kHz if F_CPU = 4MHz
```

```

49     TWI0.MCTRLA = TWI_ENABLE_bm;
50     TWI0.MSTATUS = TWI_BUSSTATE_IDLE_gc;
51     PORTA_PIN2CTRL |= PORT_PULLUPEN_bm;
52     PORTA_PIN3CTRL |= PORT_PULLUPEN_bm;
53 }
54
55 /**
56  * Release all resources initialized by sensirion_i2c_hal_init().
57  */
58 void sensirion_i2c_hal_free(void) {
59     /* TODO:IMPLEMENT or leave empty if no resources need to be freed */
60     // short did say we dont have to this function
61     TWI0.MCTRLA &= ~TWI_ENABLE_bm; //DISABLE TWI...?
62 }
63
64 /**
65  PER PROF SHORT-MAKE CHANGES IN THE READ FUNCTION
66
67  * Execute one read transaction on the I2C bus, reading a given number of bytes.
68  * If the device does not acknowledge the read command, an error shall be
69  * returned.
70  *
71  * @param address 7-bit I2C address to read from
72  * @param data pointer to the buffer where the data is to be stored
73  * @param count number of bytes to read from I2C and store in the buffer
74  * @returns 0 on success, error code otherwise
75  */
76 int8_t sensirion_i2c_hal_read(uint8_t address, uint8_t* data, uint8_t count) {
77     /* TODO:IMPLEMENT */
78     TWI0.MADDR = (address << 1) | 0x01;
79     // read 9 bytes
80     for(uint8_t i = 0; i < count; i++) {
81         while(!(TWI0.MSTATUS & TWI_RIF_bm));
82         data[i] = TWI0.MDATA;
83         if(i < count-1) {
84             // TWI0.MCTRLB = TWI_MCMD_RECVTRANS_gc; // ack and
85             continue read but same as 0x02
86             TWI0.MCTRLB = 0x02; // ACK
87         }
88         else {
89             TWI0.MCTRLB = TWI_ACKACT_NACK_gc | TWI_MCMD_STOP_gc; // Read
90             last byte and send nack+stop condition
91         }
92     }
93     return NOT_IMPLEMENTED_ERROR;
94 }

```

```
95
96 /**
97  * PER PROF SHORT-MAKE CHANGES IN THE WRITE
98  * Execute one write transaction on the I2C bus, sending a given number of
99  * bytes. The bytes in the supplied buffer must be sent to the given address. ↗
100  * If
101  * the slave device does not acknowledge any of the bytes, an error shall be
102  * returned.
103  *
104  * @param address 7-bit I2C address to write to
105  * @param data pointer to the buffer containing the data to write
106  * @param count number of bytes to read from the buffer and send over I2C
107  * @returns 0 on success, error code otherwise
108  */
109 int8_t sensirion_i2c_hal_write(uint8_t address, const uint8_t* data,
110                                uint8_t count) {
111     /* TODO:IMPLEMENT */
112     TWI0.MADDR = (address << 1); // 7-bit address, write
113     while (!(TWI0.MSTATUS & TWI_WIF_bm)); // Wait for write interrupt flag
114     for (uint8_t i = 0; i < count; i++) {
115         TWI0.MDATA = data[i];
116         while (!(TWI0.MSTATUS & TWI_WIF_bm)); // Wait for data write or until ↗
117             slave ACKs
118         if (TWI0.MSTATUS & (TWI_ARBLOST_bm | TWI_BUSERR_bm)) { //it didnt ack
119             return -1; // Error
120         }
121     }
122     TWI0.MCTRLB = TWI_MCMD_STOP_gc; // Send STOP condition
123     return 0; // Success
124     return NOT_IMPLEMENTED_ERROR;
125 }
126
127 /**
128  * Sleep for a given number of microseconds. The function should delay the
129  * execution for at least the given time, but may also sleep longer.
130  *
131  * Despite the unit, a <10 millisecond precision is sufficient.
132  *
133  * @param useconds the sleep time in microseconds
134  */
135 void sensirion_i2c_hal_sleep_usec(uint32_t useconds) {
136     // don't need according to short but eh---this might still work
137     /* TODO:IMPLEMENT */
138     while (useconds-- > 0) {
139         _delay_us(1);
140     }
141 }
```

142 }

```
1  /*
2   * Labb11_Task2_sensirion_i2c_hal.c.c
3   *
4   * Created: 4/30/2025 1:17:08 AM
5   * Author : Naafiul Hossain
6   */
7
8  /*
9   * sensirion_i2c_hal.c - Task 2: Interrupt-based HAL using array-like buffers
10  * Author: Naafiul Hossain
11  */
12
13 #include "sensirion_i2c_hal.h"
14 #include "sensirion_common.h"
15 #include "sensirion_config.h"
16 #include <avr/io.h>
17 #include <util/delay.h>
18 #include <stdint.h>
19 #include <avr/interrupt.h>
20
21 #define I2C_BUFFER_SIZE 32
22
23
24 // Global array-based buffers
25
26 volatile uint8_t i2c_tx_buffer[I2C_BUFFER_SIZE];
27 volatile uint8_t i2c_rx_buffer[I2C_BUFFER_SIZE];
28 volatile uint8_t i2c_tx_len = 0;
29 volatile uint8_t i2c_rx_len = 0;
30 volatile uint8_t i2c_tx_index = 0;
31 volatile uint8_t i2c_rx_index = 0;
32 volatile uint8_t i2c_done_flag = 0;
33 volatile uint8_t i2c_error = 0;
34
35
36 int16_t sensirion_i2c_hal_select_bus(uint8_t bus_idx) {
37     return 0;
38 }
39
40
41 // TWI0 Initialization
42
43 void sensirion_i2c_hal_init(void) {
44     PORTMUX.TWIROUTEA = PORTMUX_TWI0_ALT1_gc;
45     TWI0.MBAUD = 0x01; // 400kHz for F_CPU = 4MHz
46     TWI0.MCTRLA = TWI_ENABLE_bm | TWI_WIEN_bm | TWI_RIEN_bm;
47     TWI0.MSTATUS = TWI_BUSSTATE_IDLE_gc;
48
49     PORTA.PIN2CTRL |= PORT_PULLUPEN_bm; // Pull-ups on SDA
```

```
50     PORTA.PIN3CTRL |= PORT_PULLUPEN_bm; // Pull-ups on SCL
51
52     sei(); // Global interrupt enable
53 }
54
55
56 // TWI0 Free
57
58 void sensirion_i2c_hal_free(void) {
59     TWI0.MCTRLA &= ~TWI_ENABLE_bm;
60 }
61
62
63 // I2C Write (Interrupt-based)
64
65 int8_t sensirion_i2c_hal_write(uint8_t address, const uint8_t* data, uint8_t count) {
66     // Reject if data size exceeds buffer capacity
67     if (count > I2C_BUFFER_SIZE) return -1;
68
69     // Copy user data into internal transmit buffer
70     for (uint8_t i = 0; i < count; i++) {
71         i2c_tx_buffer[i] = data[i];
72     }
73
74     // Initialize state variables for the ISR
75     i2c_tx_len = count; // Total bytes to send
76     i2c_tx_index = 0; // Reset byte index
77     i2c_done_flag = 0; // Clear "done" signal
78     i2c_error = 0; // Clear error status
79
80     // Begin transmission by writing slave address + write bit (0) to MADDR
81     TWI0.MADDR = (address << 1); // Automatically triggers ISR via WIF
82
83     // Wait (block) until the ISR completes the entire transmission
84     while (!i2c_done_flag);
85
86     // Return error status (0 = success, -1 = error)
87     return i2c_error ? -1 : 0;
88 }
89
90
91
92 // I2C Read (Interrupt-based)
93
94 int8_t sensirion_i2c_hal_read(uint8_t address, uint8_t* data, uint8_t count) {
95     // Reject if requested read size exceeds buffer size
96     if (count > I2C_BUFFER_SIZE) return -1;
97
```

```

98     // Initialize read-related state variables
99     i2c_rx_len = count;           // Total number of bytes to receive
100    i2c_rx_index = 0;             // Start at beginning of RX buffer
101    i2c_done_flag = 0;           // Clear completion flag
102    i2c_error = 0;               // Clear error flag
103
104    // Send I2C START + address + read bit (1)
105    TWI0.MADDR = (address << 1) | 0x01;
106
107    // Wait (block) until ISR finishes reading all bytes
108    while (!i2c_done_flag);
109
110    // Copy data from internal RX buffer to user-provided buffer
111    for (uint8_t i = 0; i < count; i++) {
112        data[i] = i2c_rx_buffer[i];
113    }
114
115    // Return success or error status
116    return i2c_error ? -1 : 0;
117 }
118
119
120
121 void sensirion_i2c_hal_sleep_usec(uint32_t useconds) {
122     while (useconds--) {
123         _delay_us(1);
124     }
125 }
126
127
128 // TWI0 ISR
129
130 ISR(TWI0_TWIM_vect) {
131     // Error case
132     if (TWI0.MSTATUS & (TWI_BUSERR_bm | TWI_ARBLOST_bm)) { //if it doesnt find
an ack
133         i2c_error = 1;
134         i2c_done_flag = 1;
135         TWI0.MCTRLB = TWI_MCMD_STOP_gc;
136         TWI0.MSTATUS |= TWI_RIF_bm | TWI_WIF_bm;
137         return;
138     }
139
140     // Write mode
141     if (TWI0.MSTATUS & TWI_WIF_bm) {
142         if (i2c_tx_index < i2c_tx_len) {
143             TWI0.MDATA = i2c_tx_buffer[i2c_tx_index++]; //loads next byte. do
we need to ack? i assumed hardware handles in
144         } else {

```



```
145         TWI0.MCTRLB = TWI_MCMD_STOP_gc; //send stop
146         i2c_done_flag = 1;
147     }
148 }
149
150 // Read mode
151 if (TWI0.MSTATUS & TWI_RIF_bm) {
152     if (i2c_rx_index < i2c_rx_len) {
153         i2c_rx_buffer[i2c_rx_index++] = TWI0.MDATA;
154
155         if (i2c_rx_index == i2c_rx_len) {
156             TWI0.MCTRLB = TWI_ACKACT_bm | TWI_MCMD_STOP_gc; // This is the ↗
157                 LAST byte ? Send NACK + STOP
158             i2c_done_flag = 1;
159         } else {
160             TWI0.MCTRLB = TWI_MCMD_RECVTRANS_gc; //Not the last byte ? ↗
161                 Send ACK and continue
162         }
163     }
164 }
```

```
1  #include "sensirion_i2c_hal.h"
2  #include "scd4x_i2c.h"
3  #include "lcd.h"
4  #include <stdint.h>
5  #include <stdbool.h>
6  #include <stdio.h>
7
8  int main(void) {
9      // === Init I²C for sensor ===
10     sensirion_i2c_hal_init();
11     scd4x_init(0); // Initialize sensor with default address..task 2 or sei
12     // task 1 scd4x_init(); // No address parameter in polling version
13
14     scd4x_start_periodic_measurement(); // Begin measurement loop
15
16     // === Init SPI for LCD ===
17     init_spi0_SerLCD();
18     clear_display_buffs();
19
20     // === Sensor variables ===
21     bool data_ready = false;
22     uint16_t co2 = 0;
23     int32_t temperature = 0;
24     int32_t humidity = 0;
25
26     while (1) {
27         // Check if new sensor data is available
28         scd4x_get_data_ready_status(&data_ready);
29
30         if (data_ready) {
31             // Read latest CO2, temperature, humidity values
32             scd4x_read_measurement(&co2, &temperature, &humidity);
33
34             // === Format sensor data into LCD buffers ===
35             sprintf(dsp_buff1, "CO2: %u ppm", co2);
36             sprintf(dsp_buff2, "Temp: %ld.%02ld C", temperature / 1000,
37                     (temperature % 1000) / 10);
38             sprintf(dsp_buff3, "RH: %ld.%02ld %%", humidity / 1000, (humidity
39                     % 1000) / 10);
40             sprintf(dsp_buff4, "Air Monitor Ready");
41
42             update_SerLCD(); // Push display buffers to the LCD
43
44             // Small delay before checking again
45             sensirion_i2c_hal_sleep_usec(5000);
46     }
```