



Fundamental of Electronics Assignment

Amanuel Legesse

ATE/3628/11



SUBMITTED TO: - ENDRIAS HAILE

1. What is an atom? What are atoms made of?

- ✓ An atom is the smallest particle of an element that retains the characteristics of that element. Every solid, liquid, gas, and plasma is composed of neutral or ionized atoms.
 - An atom is made up of three major sub-atomic particles namely protons, electrons and neutrons. The electrons form a cloud around the nucleus and are bound to the nucleus by electromagnetic forces.

2. Why is Bohr's model of the atom called the "planetary model"?

- ✓ Because it shows electrons orbiting the nucleus in distinct energy levels. This model is great for showing how the electrons are placed outside of the nucleus but is not the most recent model of the atom.

3. Describe why it is important to have both conductors and insulators?

- ✓ Because for an electric current to flow in a conductor, the outer electrons of the atom are loosely bound and can freely move through the material when an electric charge is applied. And the opposite of a conductor is an insulator. An insulator opposes the flow of electricity. Insulators are important to keep us safe from electricity. The wire that carries electricity to your computer or television is covered with a rubber-like insulator that protects you from getting electrocuted.

4. Why is silicon the most preferred semiconductor material?

- ✓ There is more force trying to hold a valence electron to the atom in silicon also are more stable at high temperatures and doesn't results in excessive reverse current when related with germanium.

5. How does the two types of current produced in semiconductor?

- ✓ When a voltage is applied across a piece of intrinsic silicon, the thermally generated free electrons in the conduction band, which are free to move randomly in the crystal structure, are now easily attracted toward the positive end. This movement of free electrons is one type of current in a semi conductive material and is called electron current.
 - Holes are positions in the semiconductor atoms that can be but are not occupied by electrons. An atom with a hole can "rob" the electron of an adjacent atom to fill the hole, causing the adjacent atom to lose an electron and get a hole, therefore effectively "conducting" the hole. In a circuit, holes current is produced when electrons in a semiconductor are taken away by the positive terminal.

6. Can we put pure semiconductors to any use?

✓ Yes, some of the best examples are :

Germanium

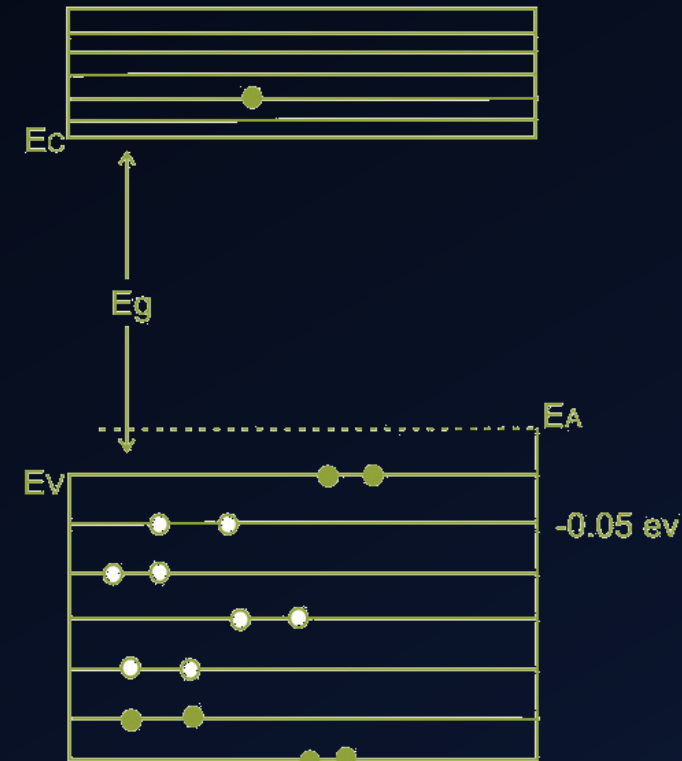
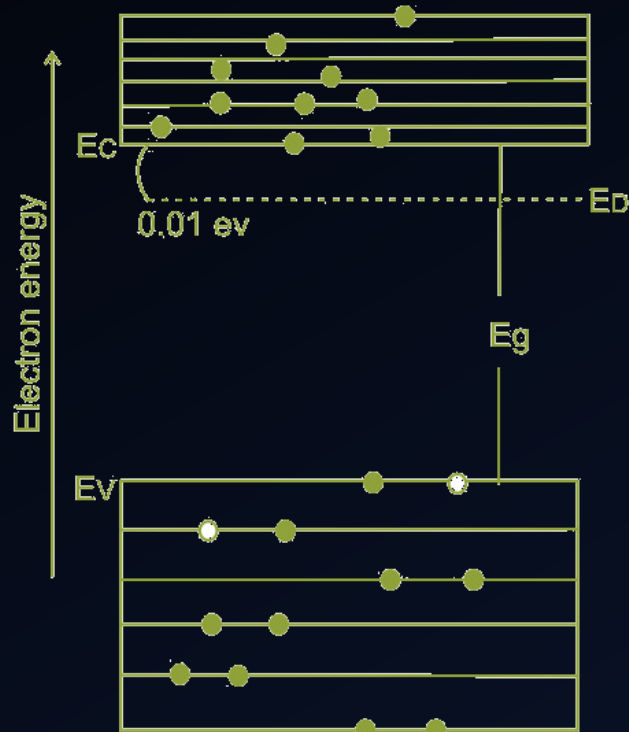


Silicon



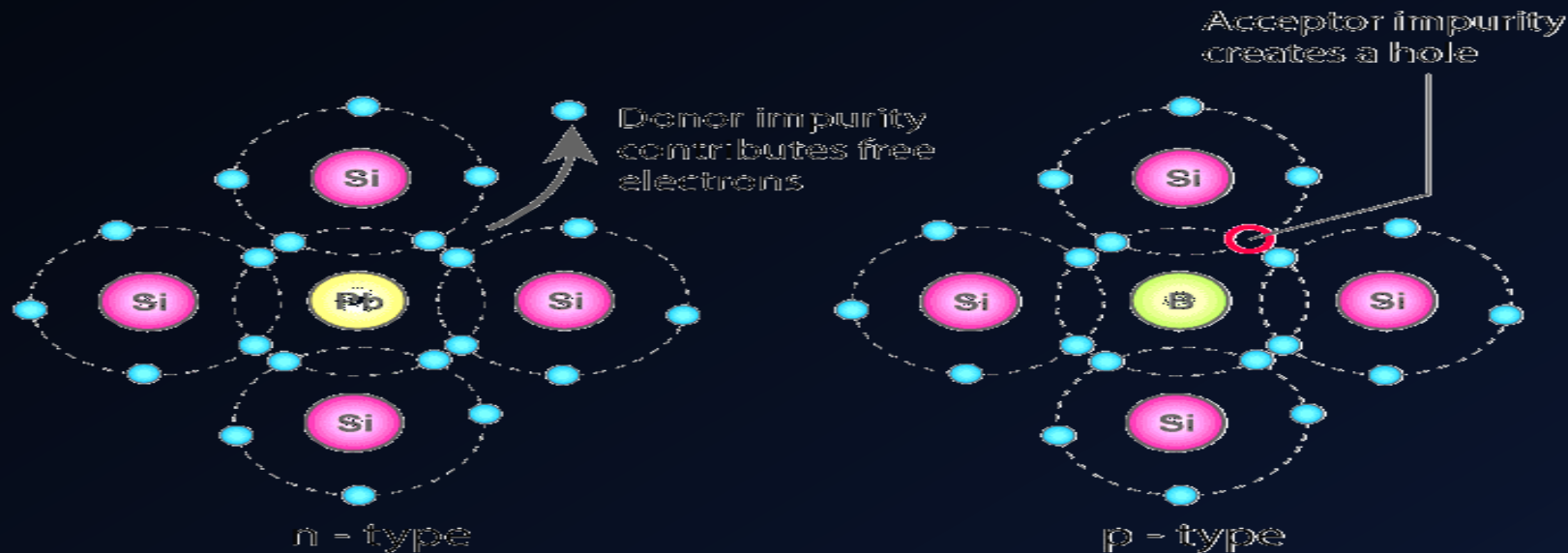
7. How are p-type and n-type semiconductor obtained? Draw a necessary diagram to show the creation of hole and the creation of free electron. What are the majority and minority carriers in each type? And why?

- ✓ P-type: - are formed when a trivalent impurity is added to a pure semiconductor in a small amount and a large number of holes are created in it.
- ✓ N-type: - are formed when doping a semiconductor by exposing them to other elements.



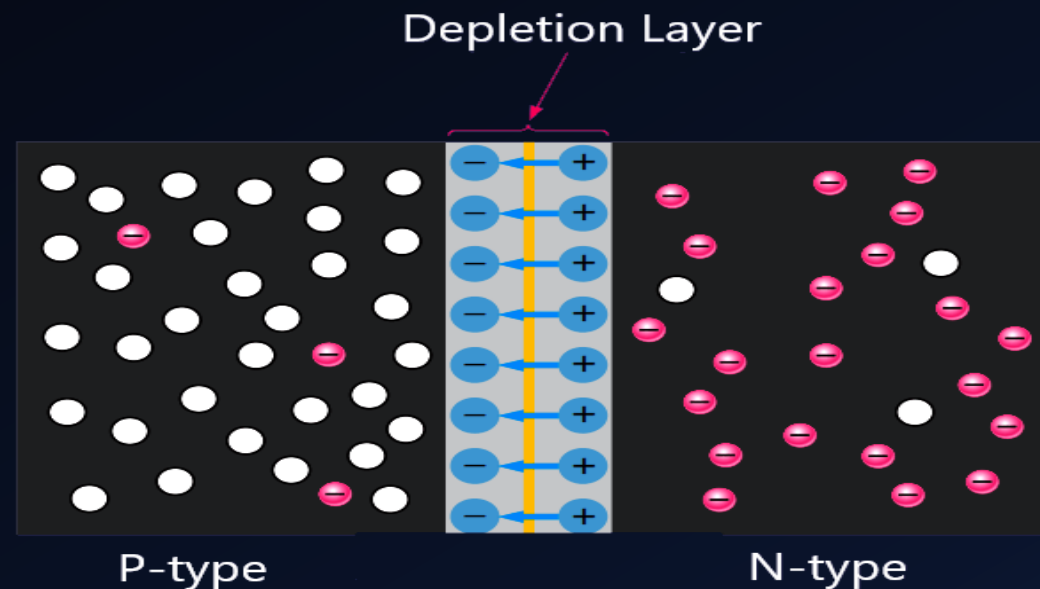
- ✓ **Majority and minority carriers in N-type:-** the majority of current carriers in n-type material are electrons, there are also a few holes that are created when electron-hole pairs are thermally generated. These holes are not produced by the addition of the pentavalent impurity atoms. Holes in an n-type material are called minority carriers.
- ✓ **Majority and minority carriers in P-type:-** the majority of current carriers in p-type material are holes, there are also a few conduction-band electrons that are created when electron-hole pairs are thermally generated. The holes are the majority carriers in p-type material. Conduction-band electrons in p-type material are the minority carriers.

EXTRINSIC SEMICONDUCTORS



3. Discuss how a depletion layer is formed in a P-N junction and give a brief explanation

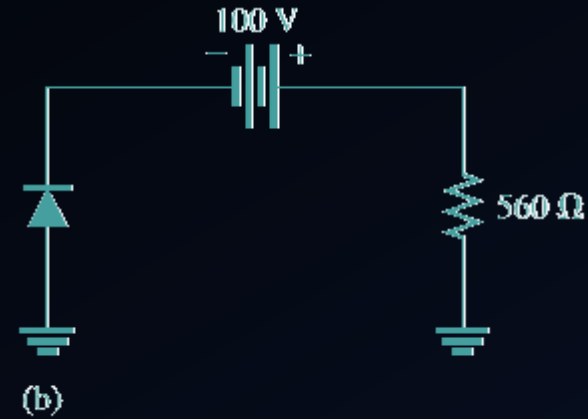
- ✓ When the PN junction is formed, the n region loses free electrons as they diffuse across the junction. This creates a layer of positive charges (pentavalent ions) near the junction. As the electrons move across the junction, the p region loses holes as the electrons and holes combine. This creates a layer of negative charges (trivalent ions) near the junction. These two layers of positive and negative charges form the depletion region



4. Explain the difference between forward bias and reverse bias.

Forward bias: - is the condition that allows current through the pn junction. The resistor limits the forward current to a value that will not damage the diode. Because like charges repel, the negative side of the bias-voltage source "pushes" the free electrons, which are the majority carriers in the n region, toward the PN junction.

Reverse bias: - is the condition that essentially prevents current through the diode. The positive side of V is connected to the n region of the diode and the negative side is connected to the p region.



5. Determine the forward voltage and forward current for the diode in Figure (b) for each of the diode models. Also find the voltage across the limiting resistor in each case. Assume $r_d = 10 \Omega$ at the determined value of forward current.

Ideal model:

$$\begin{aligned}
 V_F &= 0 \text{ V} \\
 I_F &= \frac{V_{\text{bias}}}{R_{\text{limit}}} \\
 &= \frac{100 \text{ V}}{5.6 \text{ K}\Omega} \\
 &= 17.8 \text{ mA}
 \end{aligned}$$

Practical model:



$$I_{NVbias} = \frac{V_f = 0.7V}{R_{limit}}$$

$$\frac{0.7V}{5.6k\Omega}$$

$$17.7mA$$

$$V_{Rlimit} = I_{Rlimit} \times R_{limit}$$
$$17.7mA \times 5.6k\Omega$$

$$= 99.1V$$





Complete model:

$$i_f = \frac{V_{bias} - 0.7V}{R_{limit} + r'_d}$$

$$\frac{(100V - 0.7V)}{5.6k\Omega + 10\Omega}$$

$$= 6.36mA$$

$$V_{Rlimit} = I_{FRlimit}$$

$$17.7mA \times 5.6k\Omega = 99.1V$$

6. Determine the reverse voltage and reverse current for the diode in Figure (a) for each of the diode models. Also find the voltage across the limiting resistor in each case. Assume $I_R = 1 \text{ mA}$.

Ideal model :

$$I_R = 0 \text{ A}$$

$$V_R = V_{\text{bias}}$$

$$8 \text{ V} - 5 \text{ V} = 3 \text{ V}$$

$$V_{R\text{limit}} = 0 \text{ V}$$

Practical Model :

$$I_R = 0 \text{ A}$$

$$V_R = V_{\text{bias}}$$

$$= 3 \text{ V}$$

$$V_{R\text{limit}} = 0 \text{ V}$$

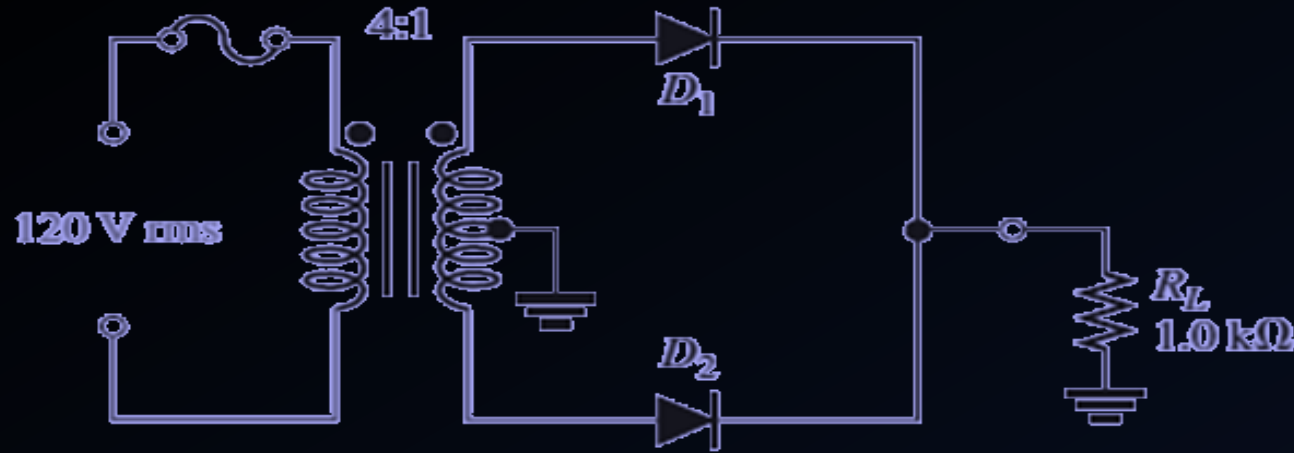


Complete Model :

$$\begin{aligned} I_R &= \mu A \\ V_{Rlimit} &= I_R r_{limit} \\ &= 1\mu A \times 1.0k\Omega \\ &= 1mV \end{aligned}$$

$$\begin{aligned} V_R &= V_{bias} - V_{Rlimit} \\ &= 3V - 1mV \\ &= 2.999V \end{aligned}$$





A center tapped full wave rectifier
Circuit

(a) What is the total peak secondary voltage?

$$V_{p_{sec}} = (0.25)(1.414) 120V \\ = 42.4V$$

(b) Find the peak voltage across each half of the secondary?

$$\frac{V_{p_{sec}}}{2} = \frac{42.4 V}{2} = 21.2 V$$

(d) Sketch the voltage waveform across RL.



(e) What is the peak current through each diode?

$$I_f = \frac{\frac{V_p \text{ sec}}{2} - 0.7V}{R_l} = \frac{20.5V}{1 \text{ k}\Omega}$$

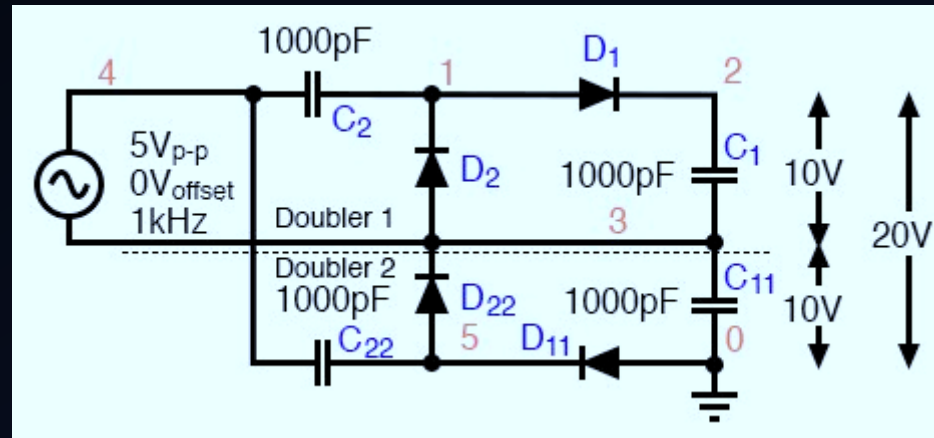
$$20.05mA$$

(f) What is the PIV for each diode?

$$PIV = 21.2V + 20.5V = 41.7V$$

7. With the help of a neat diagram, explain the operation Voltage Quadrupler

A voltage quadrupler is a stacked combination of two doublers shown in Figure below. Each doubler provides 10 V (8.6 V) for a series total at node 2 with respect to ground of 20 V (17.2 V)



Voltage quadrupler, composed of two doublers stacked in series, with output at node 2

8. Can an ordinary diode be used as a Zener diode? Justify your answer.

- ✓ Typically not. An ordinary diode is designed to be used as a rectifier and not for operating in reverse breakdown. Consequently, an ordinary diode will not be able to reliably handle much current before the junction is damaged permanently. On the other hand, a Zener diode is designed to operate in reverse breakdown and to conduct significant reverse breakdown current. Therefore, if your design requires a reverse breakdown of a diode, you should use a Zener diode

9. Explain how a Zener diode can be used as voltage regulator.

- ✓ Zener Diodes can be used as a type of voltage regulator to produce a stabilized voltage output with low ripple under varying load current conditions. By passing a small current through the diode from a voltage source, via a suitable current limiting resistor (R_S), the Zener diode will conduct sufficient current to maintain a voltage drop of V_{Z} .

10. How does Zener diode regulate the voltage? See answer in No3 what happens to the series current, load current and Zener current when the D.C input voltage of a Zener regulator increases?

- ✓ A rectifier with an appropriate filter serves as a good source of D.C output. However, the major disadvantage of such a power supply is that the output voltage changes with the variations in the input voltage or load. Thus, if the input voltage increases, the D.C output voltage of the rectifier also increases. Similarly, if the load current increases, the output voltage falls due to the voltage drop in the rectifying element, filter chokes, transformer winding etc. In many electronic applications, it is desired that the output voltage should remain constant regardless of the variations in the input voltage or load. In order to ensure this, a voltage stabilizing device, called voltage stabilizer is used. Several stabilizing circuits have been designed but only Zener diode as a voltage stabilizer.

11. Explain how a Zener diode can be used as clipper circuit.

- ✓ Diode clipping and clamping circuits are circuits that are used to shape or modify an input AC waveform (or any sinusoid) producing a differently shape output waveform depending on the circuit arrangement. Diode clipper circuits are also called limiters because they limit or clip-off the positive (or negative) part of an input AC signal. As Zener clipper circuits limit or cut-off part of the waveform across them, they are mainly used for circuit protection or in waveform shaping circuits.

