

ASSESSMENT RUBRICS LAB # 5

Verification of KVL using breadboard

LAB REPORT ASSESSMENT				
Criteria	Excellent	Average	Nil	Marks Obtained
1. Objectives of Lab	All objectives of lab are properly covered [Marks 0.5]	Objectives of lab are partially covered [Marks 0.25]	Objectives of lab are not shown [Marks 0]	
2. Kirchoff's Voltage Law (Statement, Mathematical Expression, Circuit Diagram)	Correct KVL statement and mathematical expression is written. Circuit diagram shown is correct and properly labeled [Marks 1]	KVL statement or mathematical expression or circuit diagram is missing or circuit diagram is not properly labeled [Marks 0.5]		
3. Apparatus Used	All equipment and electrical components used are shown [Marks 1]	Equipment and electrical components are partially shown and some of the components are missing [Marks 0.5]	Equipment and electrical components used are not shown [Marks 0]	
4. Procedure	All experimental steps are shown in detail [Marks 1.5]	Some of the experimental steps are missing [Marks 1]	Experimental steps are missing [Marks 0]	
5. Observations & Calculations	All experimental results are completely shown in form of table for both cases of using same resistors and for different resistors with varying applied source voltage [Marks 4]	Experimental results are partially shown and some of the observations are missing [Marks 2]	No experimental results are shown [Marks 0]	
6. Analysis	Analysis and discussion about all experimental results are shown [Marks 2]	Analysis and discussion about experimental results are partially shown [Marks 1]	Analysis is not shown [Marks 0]	
<div style="text-align: right; margin-right: 100px;">Total Marks Obtained: _____</div> <div style="text-align: right; margin-right: 100px;">Instructor Signature: _____</div>				

