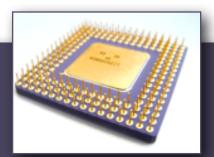


One Way to Implement a Stack

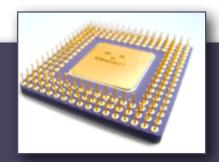


- array DWORD 256 DUP(?)
- top address DWORD (OFFSET array + SIZEOF array)
- ▶ Push (push 32-bit value in EAX onto stack):
 - sub top address, 4 ; Stack grows downward in memory!
 - mov esi, top address
 - mov [esi], eax
- ▶ *Pop (remove 32-bit top element, return in EAX):*
 - mov esi, top_address
 - mov eax, [esi]
 - add top_address, 4 ; Omit this to implement Top

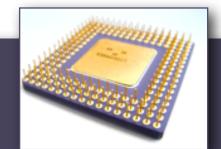
This is essentially how the runtime stack works

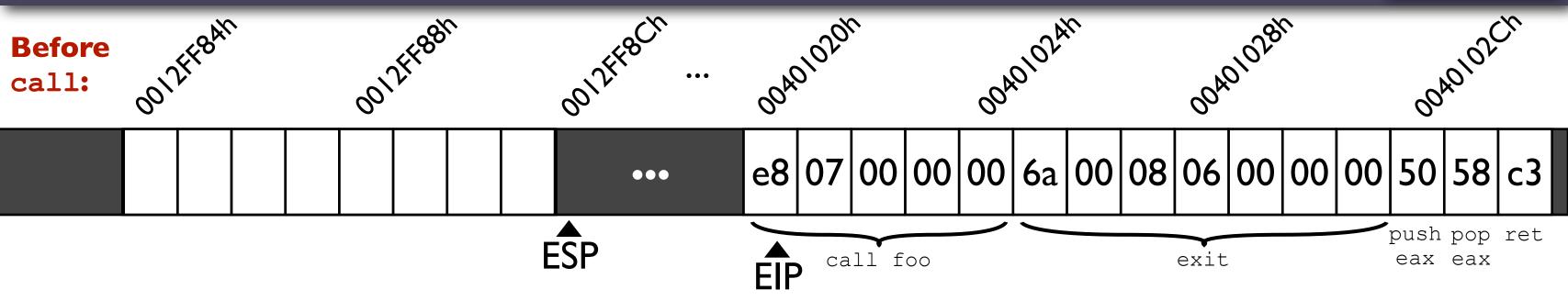
(but the top address is stored in ESP)

Topics Covered in Notes:



- ▶ PUSH instruction
- ▶ POP instruction
- ▶ CALL instruction
- ▶ RET instruction

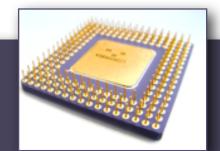


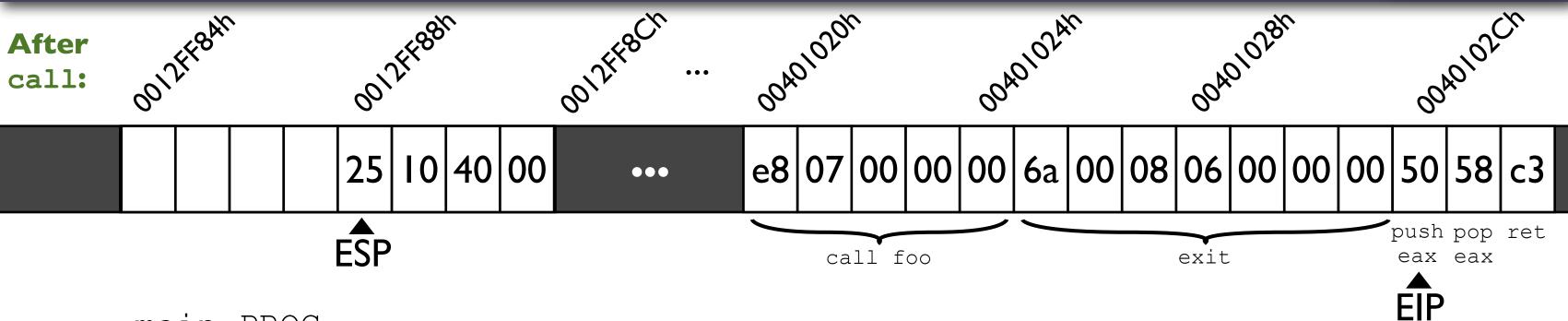


```
main PROC
call foo
exit
main ENDP
```

```
foo PROC
  push eax
  pop eax
  ret
foo ENDP
```

- ▶ The call instruction will
 - Decrease ESP by 4
 - Store the address of the instruction *following* call at the memory address now in ESP
 - > Set EIP to the memory address of the first instruction in the called procedure





- main PROC
 call foo
 exit
 main ENDP
- foo PROC

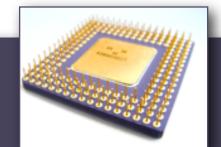
 push eax

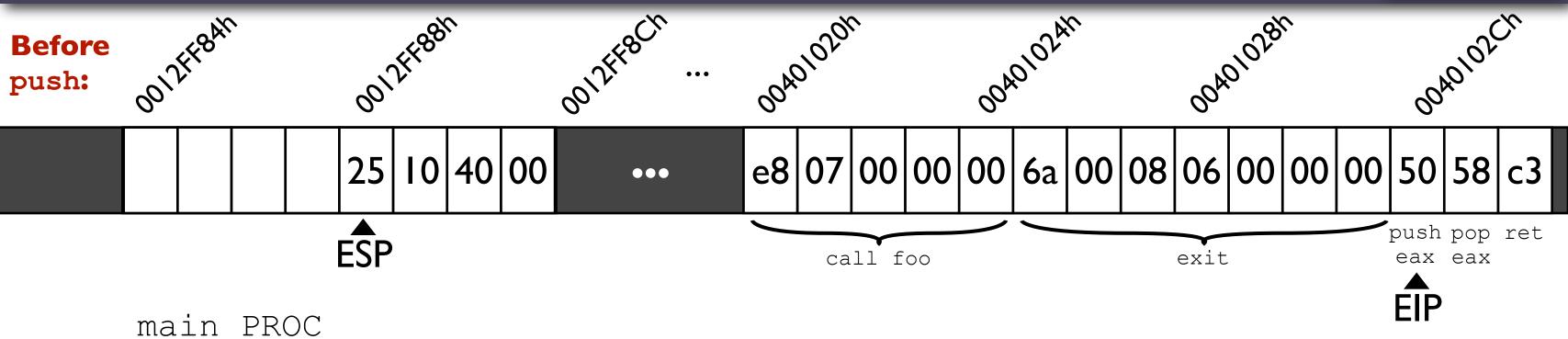
 pop eax

 ret

 foo ENDP

- ▶ The call instruction
 - Decrease ESP by 4
 - Store the address of the instruction *following* call at the memory address now in ESP
 - Set EIP to the memory address of the first instruction in the called procedure





The push instruction will exit

main ENDP

foo PROC

foo ENDP

push eax

pop eax

ret

- Decrease ESP by 4
- ► Store the value indicated at the address in ESP (we'll assume EAX contains 00000000h)

main ENDP

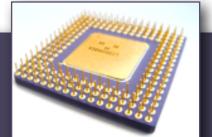
foo PROC

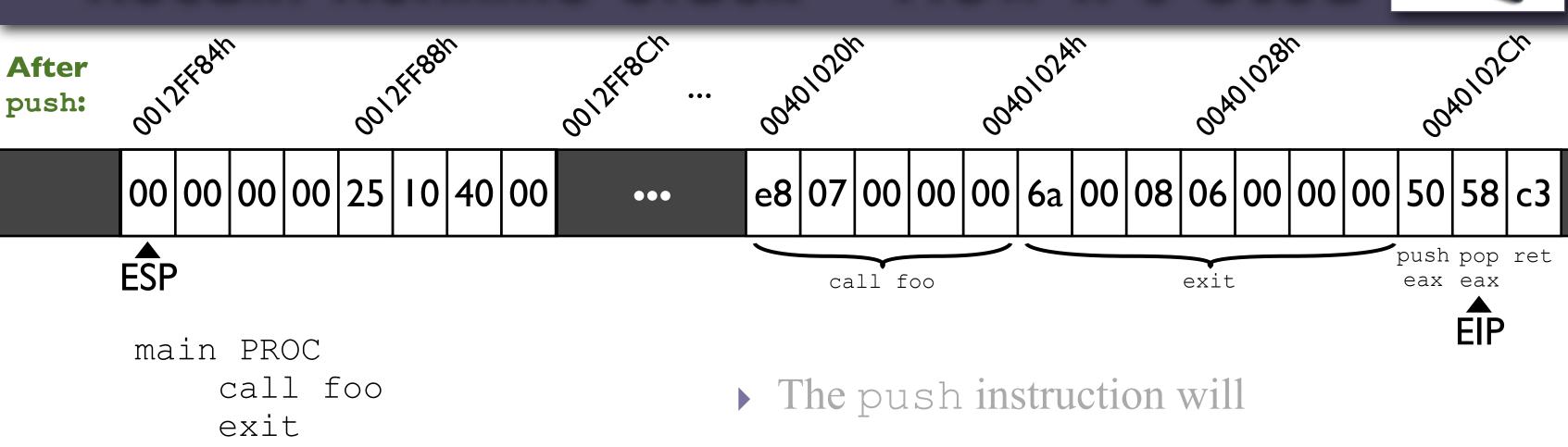
foo ENDP

push eax

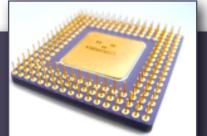
pop eax

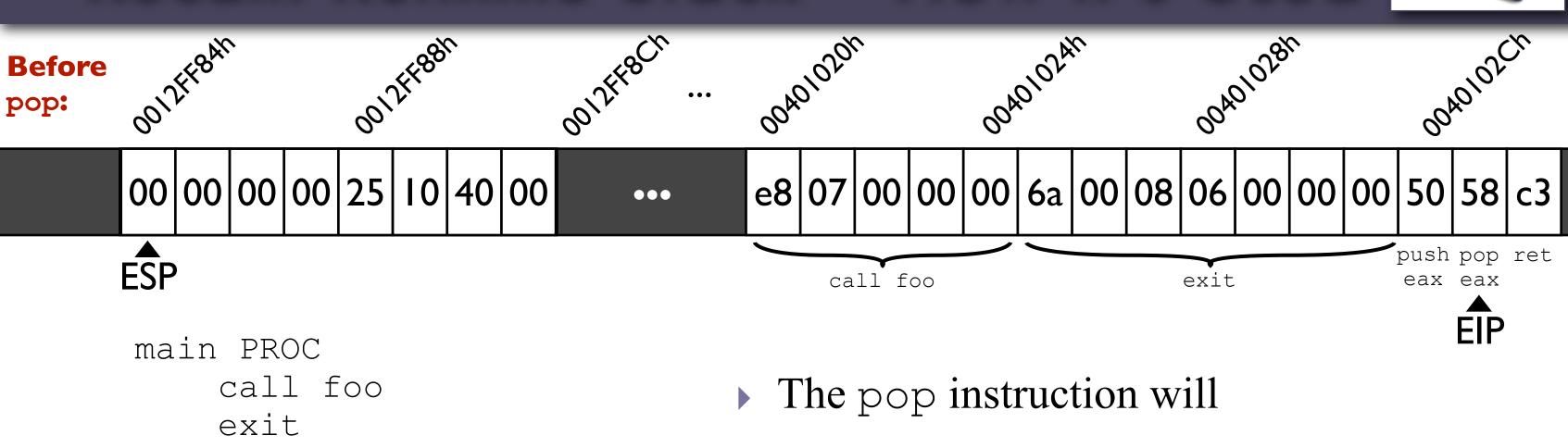
ret





- Decrease ESP by 4
- Store the value indicated at the address in ESP (we'll assume EAX contains 00000000h)





main ENDP

foo PROC

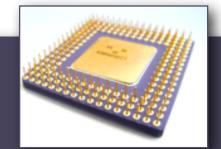
push eax

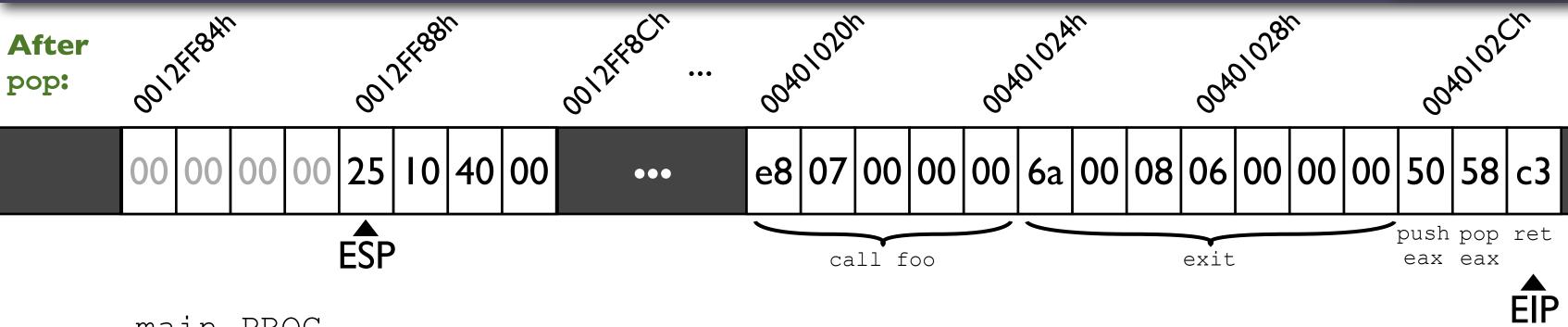
pop eax

ret

foo ENDP

- Load the value from the address given by ESP, copying it into the given register
- Increase ESP by 4





- main PROC
 call foo
 exit
 main ENDP
- foo PROC

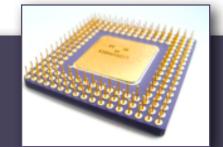
 push eax

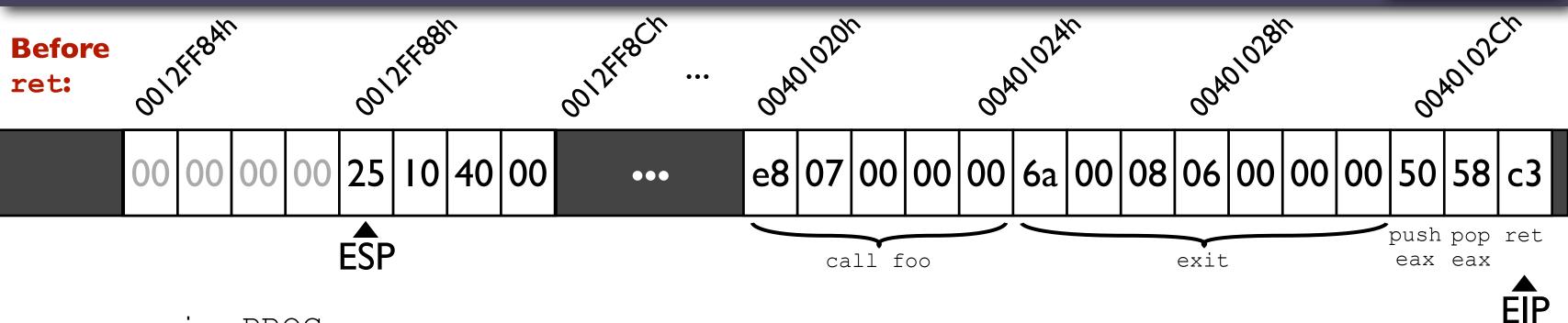
 pop eax

 ret

ENDP

- ▶ The pop instruction will
 - Load the value from the address given by ESP, copying it into the given register
 - ► Increase ESP by 4



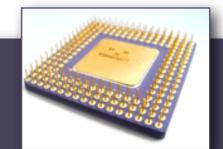


main PROC
call foo
exit
main ENDP

foo PROC
push eax
pop eax
ret

ENDP

- The ret instruction will
 - Read the 32-bit value at ESP (in this example, 00401025h)
 - Increase ESP by 4
 - Set EIP to the value it just read (00401025h)



```
After ret: 00171188 00171188 001711188 0017111 0017111 0017111 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 001711 0017111 001711 001711 001711 001711 001711 001711 001711 001711 0017111 001711 001711 001711 001711 001711 001711 001711 001711 0017111 001711 001711 001711 001711 001711 001711 001711 001711 0017111 001711 001711 001711 001711 001711 001711 001711 001711 00171
```

```
main PROC
call foo
exit
main ENDP
foo PROC
```

foo PROC

push eax

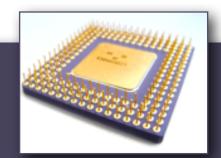
pop eax

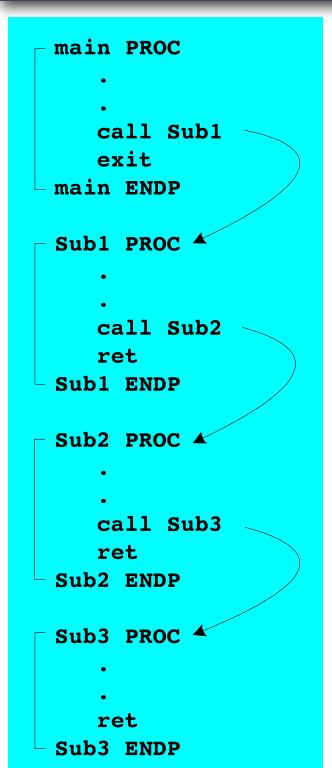
ret

foo ENDP

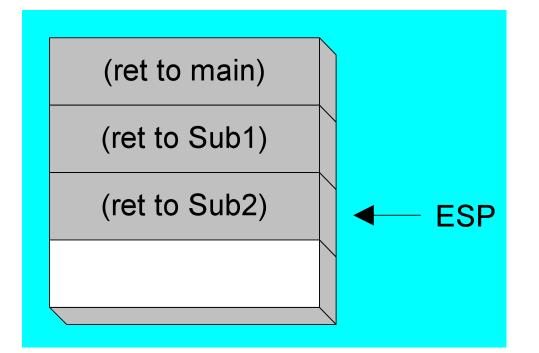
- ▶ The ret instruction will
 - Read the 32-bit value at ESP (in this example, 00401025h)
 - Increase ESP by 4
 - Set EIP to the value it just read (00401025h)

Nested Procedure Calls





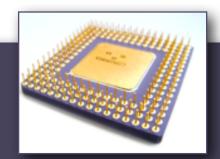
- Nested procedure calls:
 you call a procedure, and it calls other procedures
 before returning to you
- main calls Sub1Sub1 calls Sub2Sub2 calls Sub3



- Recall: Stacks are last-in-firstout (LIFO) data structures
- You always want to return to the *last* procedure that CALLed
- The last procedure that CALLed will be the first address POPped

Irvine, Kip R. Assembly Language for x86 Processors 6/e, 2010.

Nested Procedure Calls



```
main PROC
    call A
    exit
main ENDP
A PROC
    push eax
    push ebx
    call B
    pop ebx
    pop eax
    ret
A ENDP
B PROC
               at this point, the stack contains:
    push eax
    pop eax
    ret
```

B ENDP

Return address for main	
Saved value of EAX from A	
Saved value of EBX from A	
Return address for A	
Saved value of EAX from B	

bottom of stack

<top of stack