

Control and FB Connectors

Pyro Circuit

File: Pyro_Circuit.kicad_sch

PHOENIX_SPTAF1/2-3.5 J3

1 1 PYRO-CTRL

2 2 GND

TP12

PHOENIX_SPTAF1/2-3.5 J4

2 2 GND

1 1 GFB_OUT

TP1 HARWIN_S1751-46

TP2 HARWIN_S1751-46

TP3 HARWIN_S1751-46

TP4 HARWIN_S1751-46

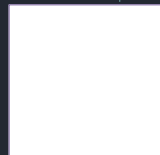
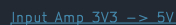
GND

TP13 HARWIN_S1751-46

TP14 HARWIN_S1751-46

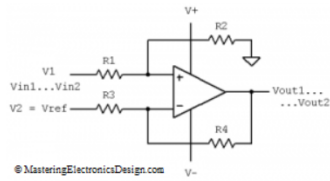
GND

Scope Grounding Points (for 4ch Scope)



File: Diff_Amp_Calcs.kicad_sch

Differential Amp Calculated Design



Given Input Range

Vin1 = 1.65 V

Vin2 = 2.31 V

Required Output Range

Vout1 = -0 V

Vout2 = 5 V

Choose a Reference Voltage

V2 = 1.8 V

Choose R2 and R3

R2 = 1 kOhm

R3 = .22 kOhm

Calculate R1 and R4

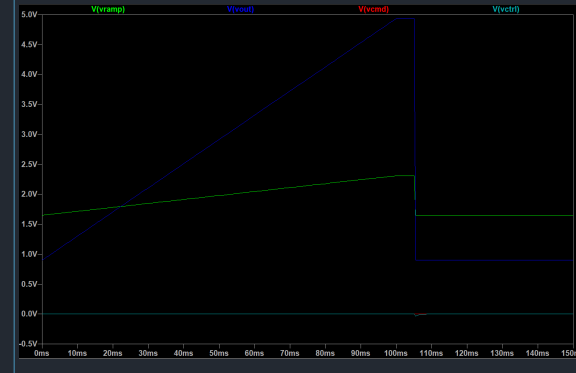
R1 = 0.05 kOhm

R4 = 1.53 kOhm

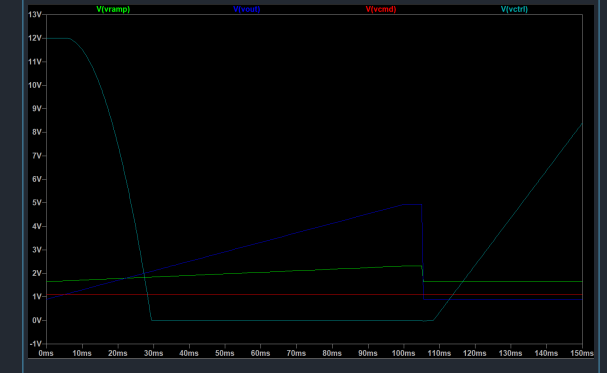
Calculator Used:

<https://masteringelectronicsdesign.com/differential-amplifier-calculator-2/>

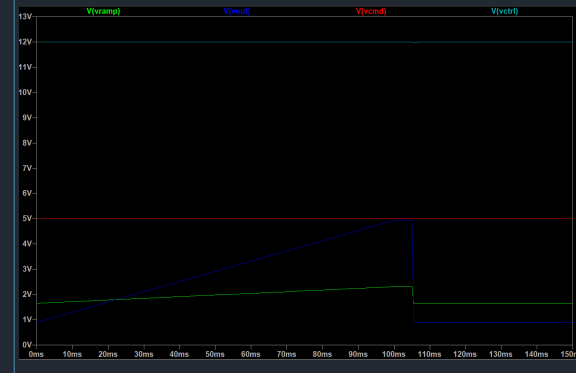
Test Case VCMD = 0V



Test Case VCMD = 1V1



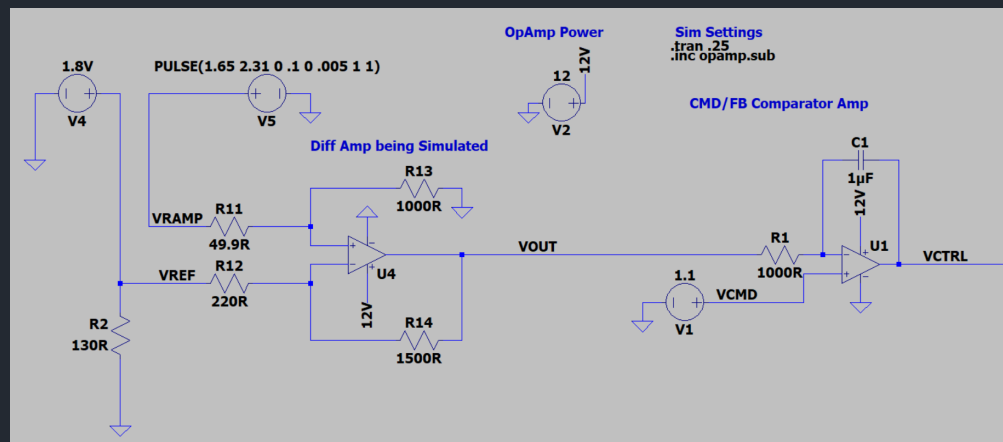
Test Case VCMD = 5V



Test Case VCMD = 1V1 w/ 100nF Slew-Limiting Cap



LTSpice Differential Amp Sim Setup



Test Cases:

- VCMD = 0V -> VCTRL should remain low at all times
- VCMD = 1V1 -> VCTRL should be high until VOUT exceeds VCMD, indicating desired current has been reached
- VCMD = 5V -> VCTRL should be high until VOUT exceeds VCMD, indicating desired current has been reached

Test cases display control voltage (VCTRL) to the darlington pair, in response to a static command voltage (VCMD) and a constant-time ramp in feedback, thereby showing the response of the system as a function of inputs and given feedback.

For faster rise time, adjusting the slew limiting cap can decrease current rise times.

Sheet: /Pyro Circuit/Differential Amplifier Calc/ File: Diff_Amp_Calc.xlsx

Title: Forerunner Pyro Test Board

Rev: 1.0

Date: 2023-12-07

Rev: 1.0

Rev: 1.0

Date: 2023