

REPORT

CS4100 - Computer System Design

Assignment - 4

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CPI (CLOCK CYCLES PER INSTRUCTION)

TEST CASE 1 :

Total clock cycles = 11

Number of instructions = 6

$CPI = 11/6$

TEST CASE 2 :

Total clock cycles = 22

Number of instructions = 9

$CPI = 22/9$

Reason for stalls :

Read after Write (RAW) Data Hazard

TEST CASE 3 :

Total clock cycles = 17

Number of instructions = 8

CPI = $17/8$

Reason for stalls :

Read after Write (RAW) Data Hazard and Control Hazard (JMP statement)