# Spark4pi-sensors

Project Plan

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#### Abstract

Spark4pi-sensors is a hardware platform for I2C sensors. It is designed to be scalable utilizing the NETCONF protocol and YANG data models for configuration. The platform is designed to be rack mounted taking up one unit in a 19-inch rack. The platform can be powered by one of two methods, a 10-60V power line or through an ethernet4pi-zero PoE hat. The platform uses an ICE40 FPGA to establish six different I2C buses, each dedicated to an interface. This makes the platform both performant and easy to integrate. The FPGA communicates with a Raspberry Pi Zero which runs the NETCONF server.

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# 1 Introduction

When designing a sensor platform, universality is of the highest importance. This document will describe the preliminary design of Spark4pi-sensors and the development plan. Spark4pi-sensors is a hardware platform for I2C sensors. The sensor platform is designed to be rack mounted taking up one unit in a 19" rack. This platform is powered by a 10-60V power supply or trough PoE using the ethernet4pi-zero PoE hat. The platform will be controlled and configured through a NETCONF/YANG interface, this will allow it to scale easily making the sensor network size agnostic. The platform will be built around an Ice4pi FPGA and a Raspberry Pi Zero. All logic will be powered by a 3.3V power line regulated from the main 5V power line. Spark4pi-sensors is designed with Sensirion sensors in mind, but there is no technical reason why other sensors can't be used.

# 2 Technical background

## 2.1 Power Over Ethernet (PoE)

Power over Ethernet (PoE) is a technology that allows network cables to carry electrical power. There are two standards for PoE, IEEE 802.3af and IEEE 802.3at [8]. The ethernet4pi-zero PoE hat uses IEEE 802.3af which is the older standard and allows for a maximum power draw of 15.4w.

#### 2.2 Raspberry Pi Zero

The Raspberry Pi Zero is a single board computer developed by the Raspberry Pi Foundation [6]. The Raspberry Pi Zero is the smallest and cheapest Raspberry Pi.

#### 2.3 Ethernet4pi-zero PoE

The ethernet4pi-zero PoE is a PoE network shield in the late stages of development. The shield is designed to be used with the Raspberry Pi Zero and the Raspberry Pi Zero W. Ethernet4pi-zero PoE is built around a Lightside-Instruments powermod-tps54260 which delivers a maximum of five volts at three amps. This is equal to a maximum power draw of 15 watts which is the maximum power draw of the IEEE 802.3af standard.

#### 2.4 Powermod-tps54260

The powermod-tps54260 is a DC to DC voltage regulator that can deliver a maximum of five volts at three amps. It works with an input voltage between 10 and 60 volts. The powermod-tps54260 is designed to be modular and can be placed on any project that requires a stable five-volt power supply. It is based on the Texas Instruments TPS54260 [9].

#### 2.5 RJ45

An RJ45 connector is a type of connector commonly used on Ethernet cables.

#### 2.6 Sensirion RJ45 sensor interface

Sensirion uses the RJ45 connector to implement their proprietary sensor interface. This interface is designed to be used with their reference sensor implementations. Through an adapter called the SEK-SensorBridge [1].

#### 2.7 YANG

YANG is a data modeling language, similar to XML. It is used to model and configure the state of network controlled devices [3] YANG implements a hierarchical model of what the device can do. Unlike SNMP this model is fetched from the device.

#### 2.8 NETCONE

NETCONF is a protocol that is used to configure and monitor network devices. It uses the YANG data modeling language to describe the configuration and state of network devices. NETCONF can be implemented over any protocol but is usually implemented over SSH [4].

#### 2.9 I2C

I2C is a two wire bus protocol used to communicate between devices. Each device has an address on the bus and can be read from and Written to. Only one device can communicate on the bus at a time. The I2C bus is a master slave bus, the master controls the bus and the slaves respond to the master. I2C stands for Inter-Integrated Circuit and was developed by Philips in 1982, now known as NXP [7].

### 2.10 Field Programmable Gate Array (FPGA)

A field programmable gate array is a hardware language defined integrated circuit [5]. Much like a microcontroller an FPGA is programmed. FPGA's are programmed using a hardware description language like VHDL or Verilog. The difference between an FPGA and a microcontroller is that when an FPGA is programmed we are defining the states of the field gates and the connections between them. This makes an FPGA much more flexible than a microcontroller.

# 3 Design

### 3.1 Philosophy

We believe that simplicity is the key to robust systems, the Spark4pi-sensors platform is designed following this simple philosophy based on the Unix philosophy which favors modularity over monolithic design [2]. Each of Lightside Instruments AS products must therefor meet the following criteria:

- Do one thing and do it well.
- Interopperate with other Lightside Instruments AS products.
- Present a NETCONF/YANG interface.

Consisting only of a power delivery system, an FPGA, twelve pull-up resistors and six RJ45 connectors. We have created a design that meets these criteria.

Through this simple philosophy Lightside Instruments AS has created an ecosystem of interoperating products that can be leveraged in numerous ways.

## 3.2 Schematic

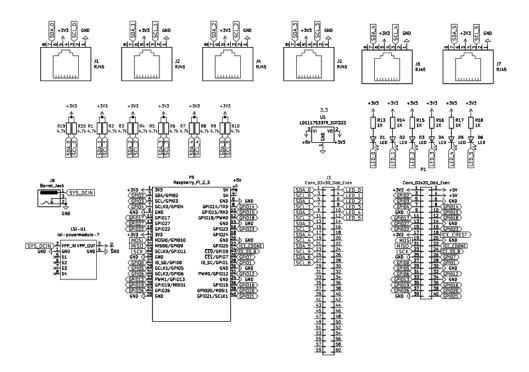


Figure 1: Preliminary schematic overview of the Spark4pi-sensors platform 0.02

#### 3.3 Ice4pi FPGA

The ice4pi-spark-6x-sfp is a 6x10 pin Field Programmable Gate Array (FPGA) designed by Lightside Instruments AS. This FPGA will provide the six I2C buses required by the Spark4pi-sensors platform. The ice4pi-spark-6x-sfp will communicate with the Raspberry Pi through an SPI interface. This can be seen in the schematic in figure 1.

# References

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