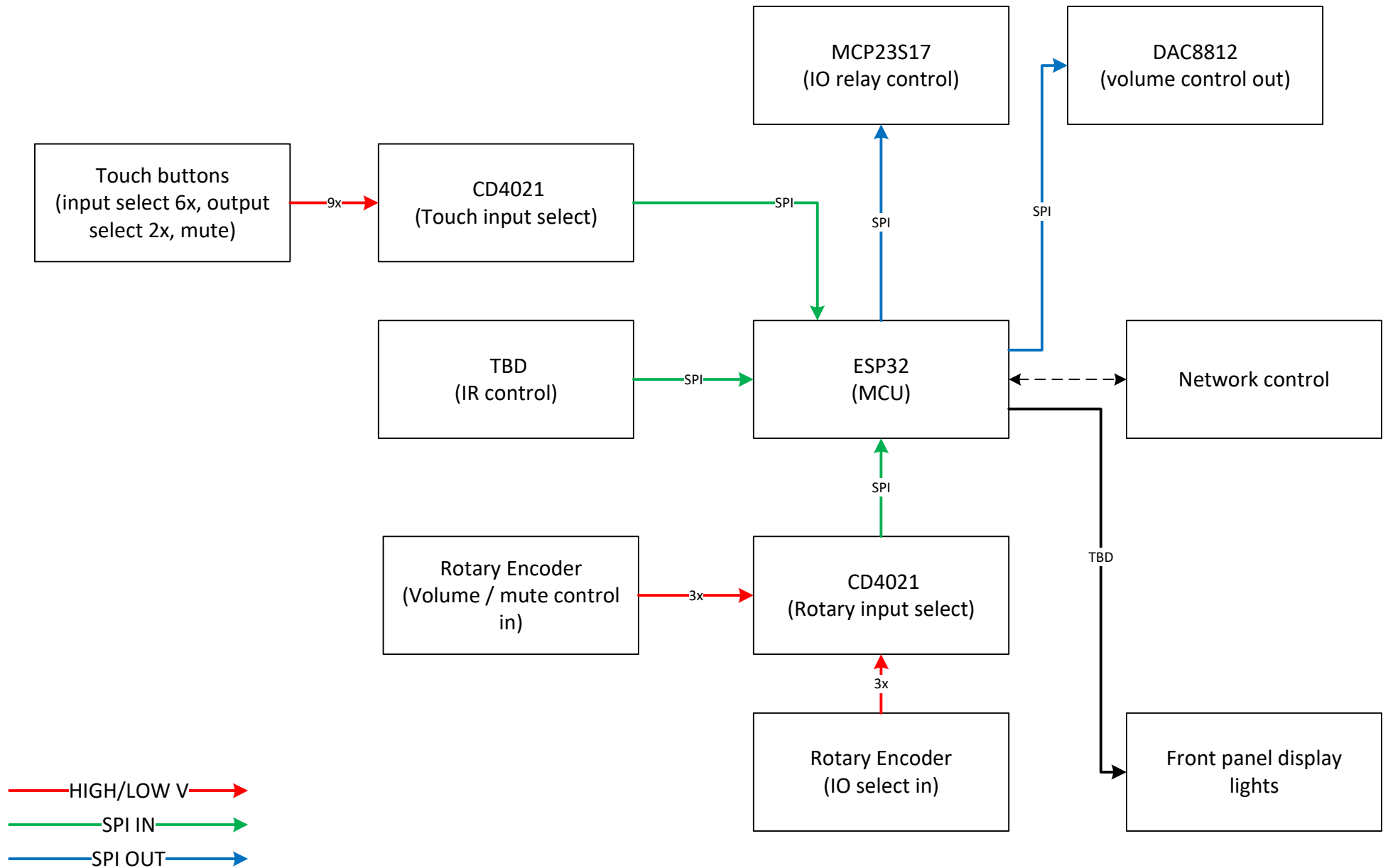


# Digital Control Layout



# Analog Signal Path Layout

