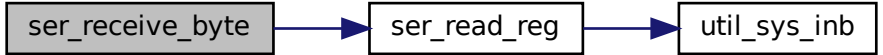


ser_receive_byte



```
graph LR; A[ser_receive_byte] --> B[ser_read_reg]; B --> C[util_sys_inb];
```

ser_read_reg

util_sys_inb