

The circuit contained above is the circuit given on the HX711 development board Joe gave to me.

AVDD: Positive excitation (+2.75V regulated)
 GND : Negative Excitation (GND)
 CH+: Channel A positive differential (load cell green)
 CH-: Channel A negative differential (load cell white)

GPIO "Bit-Banged" Pins:

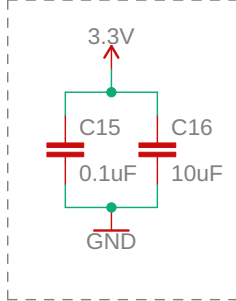
- (1) HX_SDA
- (2) HX_CLOCK

Design Notes:

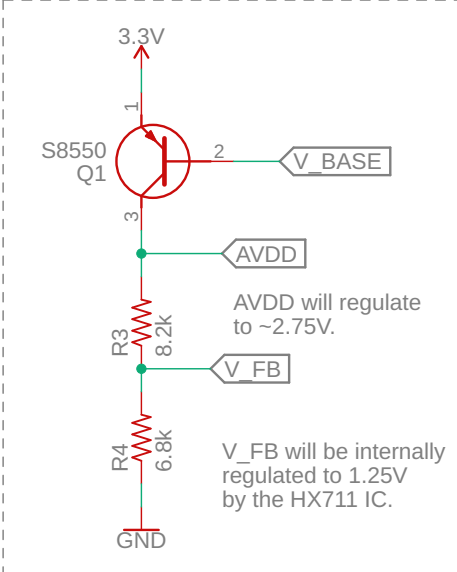
- (1) X0 = HI-Z and XI = 0 for internal crystal oscillator
- (2) AVDD generated by HX711 as shown on pg. 4 of datasheet:
 $V_{avdd} = V_{bg} * (1 + R1/R2)$ // incorrect in datasheet
 MUST be at least 0.1V less than VSUP
- (3) SDATA and SD_CLK go directly to microcontroller input header
- (4) RATE = 0 means a 10Hz sampling frequency, 1 for 80 Hz.
- (5) DVDD = V_MCU (3.3V)
- (6) AGND = GND

HX711 PCB (Needs Scoring)

HX711 Decoupling



Analog Voltage Regulation



HX711 Load Cell IC Biasing

