



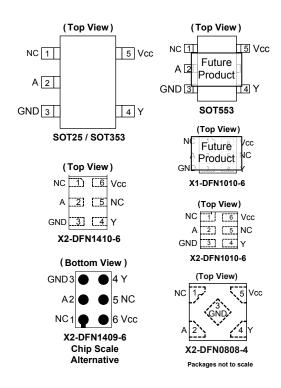
SINGLE SCHMITT-TRIGGER INVERTER

Description

The 74LVC1G14 is a single 1-input Schmitt-trigger inverter with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using $I_{\rm OFF}$. The $I_{\rm OFF}$ circuitry disables the output preventing damaging current backflow when the device is powered down. The gate performs the positive Boolean function:

$$Y = \overline{A}$$

Pin Assignments



Features

- Wide Supply Voltage Range from 1.65V to 5.5V
- ± 24mA Output Drive at 3.3V
- · CMOS low power consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs accept up to 5.5V
- ESD Protection Exceeds JESD 22
 - o 200-V Machine Model (A115)
 - o 2000-V Human Body Model (A114)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Range of Package Options
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Applications

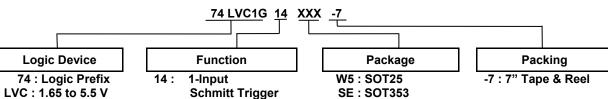
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
 - o PCs, networking, notebooks, netbooks, PDAs
 - o Computer peripherals, hard drives, CD/DVD ROM
 - o TV, DVD, DVR, set top box
 - o Cell Phones, Personal Navigation / GPS
 - o MP3 players ,Cameras, Video Recorders

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and < 1000 ppm antimony compounds.



Ordering Information



Inverter

Logic Family 1G : One Gate

Z:SOT553 FS3:X2-DFN0808-4 FW5:X1-DFN1010-6 FW4:X2-DFN1010-6 FX4:X2-DFN1409-6 FZ4:X2-DFN1410-6

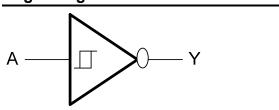
Desiles	Package	Package	Package	7" Tape	and Reel
Device	Code	(Notes 4,5)	Size	Quantity	Part Number Suffix
74LVC1G14W5-7	W5	SOT25	3.0mm X 2.8mm X 1.2mm	2000/Tana & Daal	-7
74LVC1G14VV5-7	CVV	50125	0.95mm lead pitch	3000/Tape & Reel	-7
74LVC1G14SE-7	SE	SOT353	2.0mm X 2.0mm X 1.1mm	2000/Tana & Daal	-7
74LVC1G145E-7	SE	501353	0.65mm lead pitch	3000/Tape & Reel	-7
74LVC1G14Z-7	Z	SOT553	1.6mm X 1.6 mm X 0.62mm	4000/Tana & Baal	-7
Future Product		Future Product	0.5mm lead pitch	4000/Tape & Reel	-7
74LVC1G14FS3-7	FS3	X2-DFN0808-4	0.9mm X 0.9 mm X 0.35mm	5000/Tana 9 Daal	-7
74LVC1G14F33-7	FSS	A2-DFN0000-4	0.5mm pad pitch (diamond)	5000/Tape & Reel	-7
74LVC1G14FW5-7		X1-DFN1010-6	1.0mm X 1.0mm X 0.5mm	5000/Tana 8 Daal	7
(Future Product)	FW5	(Future Product)	0.35mm pad pitch	5000/Tape & Reel	-7
74LVC1G14FW4-7	FW4	X2-DFN1010-6	1.0mm X 1.0mm X 0.4mm	5000/Tana 9 Daal	-7
74LVC1G14FVV4-7	F VV4	A2-DFN1010-6	0.35mm pad pitch	5000/Tape & Reel	-7
741.1/04.04.45.14.7	EV4	X2-DFN1409-6	1.4mm X 0.9mm X 0.4mm	5000/Tana 8 Daal	7
74LVC1G14FX4-7	FX4	(Chip scale alternative)	0.5mm pad pitch	5000/Tape & Reel	-7
741.1/04.044.574.7	FZ4	V2 DEN1440 C	1.4mm X 1.0mm X 0.4mm	5000/Tana 9 Daal	7
74LVC1G14FZ4-7	FZ4	X2-DFN1410-6	0.5mm pad pitch	5000/Tape & Reel	-7

Notes:

Pin Descriptions

Pin Name	Description
Α	Data Input
GND	Ground
Υ	Data Output
V _{CC}	Supply Voltage

Logic Diagram



Function Table

Inputs	Output
Α	Υ
Н	L
L	Н

^{4.} Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

^{5.} The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf



Absolute Maximum Ratings (Notes 6, 7)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or I _{OFF} State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{CC} +0.5	V
l _{IK}	Input Clamp Current V _I < 0	-50	mA
lok	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
ICC, IGND	Continuous Current Through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

Notes:

Recommended Operating Conditions (Note 8)

Symbol		Parameter	Min	Max	Unit
V	On a ration a Malta and	Operating	1.65	5.5	V
V _{CC}	Operating Voltage	Data retention only	1.5	_	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V _{CC}	V
	DH High-Level Output Current	V _{CC} = 1.65V	_	-4	
		V _{CC} = 2.3V	_	-8	
١.	Library Control Comment	V _{CC} = 2.7V	_	-12	
Іон	Operating Voltage Input Voltage Output Voltage	\\ - 2\\	_	-16	mA
		V _{CC} = 3V	_	-24	
		V _{CC} = 4.5V	_	-32	
		V _{CC} = 1.65V	_	4	
		V _{CC} = 2.3V	_	8	
l ,		V _{CC} = 2.7V	_	12	
l _{OL}	Low-Level Output Current	\\ - 2\\	_	16	mA
		V _{CC} = 3V	_	24	
		V _{CC} = 4.5V	_	32	
TA	Operating Free-Air Temperature	_	-40	+125	°C

Notes: 8. Unused inputs should be held at $V_{\mbox{\footnotesize{CC}}}$ or Ground.

^{6.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{7.} Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ (All typical values are at $V_{CC} = 3.3\text{V}$, $T_A = +25^{\circ}\text{C}$)

Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур.	Max	Unit
		_	1.65V	0.70	_	1.20	_
	Positive-Going Input Threshold Voltage Negative- Going Input Threshold Voltage Hysteresis (V _{T+} - V _{T-}) High Level Output Voltage	_	2.3V	1.11	_	1.60	_
$V_{T^{+}}$	• .	_	3V	1.50	_	2.00	_
	Threshold Voltage	_	4.5V	2.16	_	2.74	_
		_	5.5V	2.61	_	3.33	_
		_	1.65V	0.30	_	0.72	_
		_	2.3V	0.58	_	1.00	_
V_{T-}		_	3V	0.80	_	1.30	_
	Threshold Voltage	_	4.5V	1.21	_	1.95	_
		_	5.5V	1.45	_	2.35	_
		_	1.65V	0.30	_	0.62	_
		_	2.3V	0.40	_	0.80	_
ΔV_T		_	3V	0.35	_	1.00	_
	(V _{T+} - V _{T-)}	_	4.5V	0.55	_	1.10	_
		_	5.5V	0.60	_	1.20	_
		I _{OH} = -100μA	1.65V to 5.5V	V _{CC} - 0.1	_	_	
		I _{OH} = -4mA	1.65V	1.2	_	_	
		I _{OH} = -8mA	2.3V	1.9	_	_	
V_{OH}	High Level Output Voltage	I _{OH} = -12mA	2.7V	2.2	_	_	V
		I _{OH} = -16mA	01/	2.4	_	_	
		I _{OH} = -24mA	3V	2.3	_	_	
		I _{OH} = -32mA	4.5V	3.8	_	_	
		I _{OL} = 100μA	1.65V to 5.5V	_	_	0.1	
		I _{OL} = 4mA	1.65V	_	_	0.45	
		I _{OL} = 8mA	2.3V	_	_	0.3	
V_{OL}	Low-Level Output Voltage	I _{OL} = 12mA	2.7V	_	_	0.4	V
		I _{OL} = 16mA	01/	_	_	0.4	
		I _{OL} = 24mA	3V	_	_	0.55	
		I _{OL} = 32mA	4.5	_	_	0.55	1
II	Input Current	V _I = 5.5 V or GND	0 to 5.5V	_	_	± 5	μΑ
I _{OFF}	Power Down Leakage Current	V _I or V _O = 5.5V	0	_	_	± 10	μΑ
Icc	Supply Current	V _I = 5.5V of GND I _O = 0	1.65V to 5.5V	_	_	10	μA
Δlcc	Additional Supply Current	Input at V _{CC} –0.6V	3V to 5.5V	_	_	500	μΑ



Electrical Characteristics $T_A = -40$ °C to +125 °C (All typical values are at $V_{CC} = 3.3$ V, $T_A = +25$ °C)

Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур.	Max	Unit
		_	1.65V	0.70	_	1.20	_
		_	2.3V	1.11	_	1.60	_
V_{T^+}	Positive- Going Input	_	3V	1.50	_	2.00	_
	Threshold Voltage	_	4.5V	2.16	_	2.74	_
		_	5.5V	2.61	_	3.33	_
		_	1.65V	0.30	_	0.75	_
		_	2.3V	0.58	_	1.03	_
V_{T-}		_	3V	0.80	_	1.33	_
	Inreshold voltage	_	4.5V	1.21	_	1.95	_
		_	5.5V	1.45	_	2.35	_
		_	1.65V	0.30	_	0.62	_
		_	2.3V	0.37	_	0.80	_
ΔV_{T}	-	_	3V	0.32	_	1.00	_
	(V _{T+} - V _{T-})	_	4.5V	0.50	_	1.20	_
		_	5.5V	0.55	_	1.40	_
		I _{OH} = -100μA	1.65V to 5.5V	V _{CC} -0.1	_	_	
	′он High Level Output Voltage	I _{OH} = -4mA	1.65V	0.95	_	_	
		I _{OH} = -8mA	2.3V	1.7	_	_	
V_{OH}	High Level Output Voltage	I _{OH} = -12mA	2.7V	1.9	_	_	V
		I _{OH} = -16mA		1.9	_	_	
		I _{OH} = -24mA	3V	2.0	_	_	
		I _{OH} = -32mA	4.5V	3.4	_	_	
		I _{OL} = 100μA	1.65V to 5.5V	_	_	0.1	
		I _{OL} = 4mA	1.65V	_	_	0.7	
		I _{OL} = 8mA	2.3V	_	_	0.45	
V_{OL}	Low-Level Output Voltage	I _{OL} = 12mA	2.7V	_	_	0.6	V
		I _{OL} = 16mA		_	_	0.6	
VOL LC		I _{OL} = 24mA	3V	_	_	0.8	
	Hysteresis (V _{T+} - V _{T-}) High Level Output Voltage Low-Level Output Voltage Input Current Power Down Leakage Current C Supply Current	I _{OL} = 32mA	4.5V	_	_	0.8	
II	Input Current	V _I = 5.5V or GND	0 to 5.5V	_	_	± 100	μA
I _{OFF}	Power Down Leakage Current	V_1 or $V_0 = 5.5V$	0	_	_	± 200	μA
		V _I = 5.5V of GND I _O = 0	1.65V to 5.5V	_	_	200	μA
Δlcc	Additional Supply Current	Input at V _{CC} –0.6V	3V to 5.5V	_	_	5000	μA



Package Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C)

Symbol	Parameter	Test Conditions	V _{cc}	Min	Тур.	Max	Unit
		SOT25		_	204	_	
		SOT353		_	371	_	
		SOT553		_	231	_	
	Thermal Resistance	X2-DFN0808-4	(Nata 0)	_	400	_	°C/14/
θ_{JA}	Junction-to-Ambient	X1-DFN1010-6	(Note 9)	_	435	_	°C/W
		X2-DFN1010-6		_	445	_	
		X2-DFN1409-6		_	470	_	
		X2-DFN1410-6		_	460	_	
		SOT25		_	52	_	
		SOT353		_	143	_	°C/W
		SOT553		_	105	_	
	Thermal Resistance	X2-DFN0808-4	(Nata 0)	_	225	_	
$\theta_{ m JC}$	Junction-to-Case	X1-DFN1010-6	(Note 9)	_	250	_	
		X2-DFN1010-6		_	250	_	
		X2-DFN1409-6		_	275	_	
		X2-DFN1410-6		_	265	_	

Note: 9. Test condition for each of the 8 package types: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

T_A = -40°C to +85°C, C_L = 15pF as noted (see Figure 1)

Parameter	From Input	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
			Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	Α	Υ	1.0	9.9	0.7	5.5	0.7	4.6	0.7	4.4	ns

T_A = -40°C to +85°C, C_L = 30 or 50pF as noted (see Figure 2)

Parameter	From Input	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
			Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	A	Y	1.0	11	0.7	6.5	0.7	5.5	0.7	5	ns

T_A = -40°C to +125°C, C_L = 15pF as noted (see Figure 1)

Parameter	From Input	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
			Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	Α	Y	1.0	12.5	0.7	7.5	0.7	6.5	0.7	5.5	ns

$T_A = -40$ °C to +125°C, $C_L = 30$ or 50pF as noted (see Figure 2)

Parameter	From Input	To Output	V _{CC} = 1.8V ± 0.15V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 3.3V ± 0.3V		V _{CC} = 5V ± 0.5V		Unit
			Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	A	Y	1.0	14.0	0.7	8.5	0.7	7.0	0.7	6.5	ns

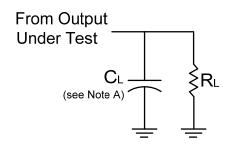


Operating Characteristics

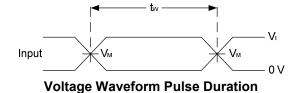
T_△ = +25°C

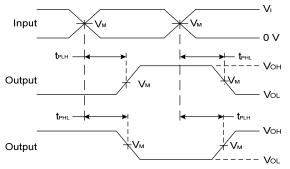
	Parameter	Test Conditions	V _{CC} = 1.8V Typ.	V _{CC} = 2.5V Typ.	V _{CC} = 3.3V Typ.	V _{CC} = 5V Typ.	Unit
$C_{\sf pd}$	Power Dissipation Capacitance	f = 10 MHz	20	21	22	25	pF

Parameter Measurement Information



V _{CC}	In	puts	V _W G		V _M C _L		R _L
*66	VI	t _r /t _f	• WI	J.	I.L		
1.8V±0.15V	V _{CC}	≤2ns	V _{CC} /2	15pF	1ΜΩ		
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	15pF	1ΜΩ		
3.3V±0.3V	3V	≤2.5ns	1.5V	15pF	1ΜΩ		
5V±0.5V	Vcc	≤2.5ns	V _{CC} /2	15pF	1ΜΩ		





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

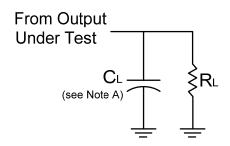
Figure 1. Load Circuit and Voltage Waveforms

Notes:

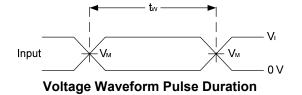
- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as $t_{PD.}$

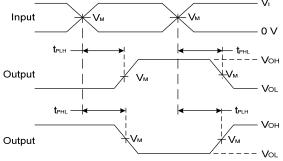


Parameter Measurement Information (Cont.)



V	Inputs		V		В
V _{CC}	VI	t _r /t _f	V _M	CL	R_L
1.8V±0.15V	V _{CC}	≤2ns	V _{CC} /2	30pF	1kΩ
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	30pF	500Ω
3.3V±0.3V	3V	≤2.5ns	1.5V	50pF	500Ω
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	50pF	500Ω





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 2. Load Circuit and Voltage Waveforms

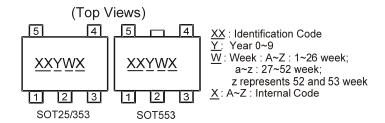
Notes:

- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .



Marking Information

(1) SOT25, SOT353 and SOT553



Part Number	Package	Identification Code
74LVC1G14W5-7	SOT25	UP
74LVC1G14SE-7	SOT353	UP
74LVC1G14Z-7	SOT553	UP

(2) DFN packages

(Top View)

XX $\underline{Y} \underline{W} \underline{X}$ XX : Identification Code

Y : Year 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week;

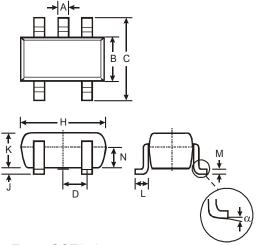
z represents 52 and 53 week \underline{X} : A~Z : Internal Code

Part Number	Package	Identification Code
74LVC1G14FS3-7	X2-DFN0808-4	WP
74LVC1G14FW5-7	X1-DFN1010-6	V8
74LVC1G14FW4-7	X2-DFN1010-6	UP
74LVC1G14FX4-7	X2-DFN1409-6	MG
74LVC1G14FZ4-7	X2-DFN1410-6	UP



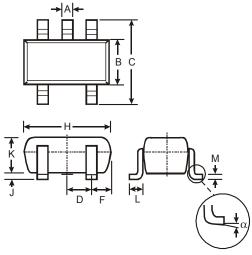
Package Outline Dimensions (All Dimensions in mm)

(1) Package Type: SOT25



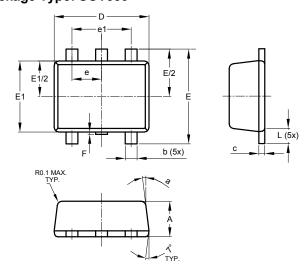
	SOT25					
Dim	Min	Max	Тур			
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D		_	0.95			
Н	2.90	3.10	3.00			
7	0.013	0.10	0.05			
K	1.00	1.30	1.10			
L	0.35	0.55	0.40			
М	0.10	0.20	0.15			
N	0.70	0.80	0.75			
α	0°	8°				
All D	imensi	ons in	mm			

(2) Package Type: SOT353



	SOT353					
Dim	Min	Max				
Α	0.10	0.30				
В	1.15	1.35				
С	2.00	2.20				
D	D 0.65 Typ					
F	0.40	0.45				
Н	1.80	2.20				
J	0	0.10				
K	0.90	1.00				
L	0.25	0.40				
М	0.10	0.22				
α	0°	8°				
All Di	mensions	in mm				

(3) Package Type: SOT553

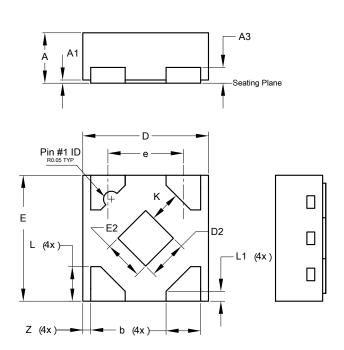


SOT553					
Dim	Min	Max	Тур		
Α	0.55	0.62	0.60		
b	0.15	0.30	0.20		
С	0.10	0.18	0.15		
D	1.50	1.70	1.60		
Е	1.55	1.70	1.60		
E1	1.10	1.25	1.20		
е	C	.50 BS	\circ		
e1	1	.00 BS	C		
F	0.00	0.10			
L	0.10	0.30	0.20		
а	6°	8°	7°		
All	All Dimensions in mm				



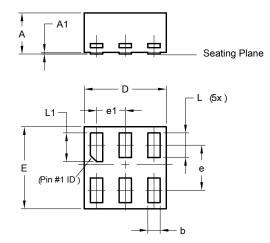
Package Outline Dimensions (cont.)

(4) Package Type X2-DFN0808-4



X2-DFN0808-4					
Dim	Min	Max	Тур		
Α	0.25	0.35	0.30		
A 1	0	0.04	0.02		
A3	-	-	0.13		
b	0.17	0.27	0.22		
D	0.75	0.85	0.80		
D2	0.15	0.35	0.25		
Е	0.75	0.85	0.80		
E2	0.15	0.35	0.25		
е	-	-	0.48		
K	0.20	-	-		
L	0.17	0.27	0.22		
L1	0.02	0.12	0.07		
Z	-	-	0.05		
All Dimensions in mm					

(5) Package Type: X1-DFN1010-6

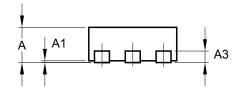


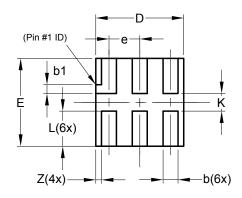
X1-DFN1010-6					
Dim	Min	Max	Тур		
Α	ı	0.50	0.39		
A1	ı	0.04			
b	0.12	0.20	0.15		
D	0.95	1.050	1.00		
E	0.95	1.050	1.00		
е		0.55 B	SC		
e1		0.35 B	SC		
٦	0.27	0.35	0.30		
L1	0.32	0.40	0.35		
AII	All Dimensions in mm				



Package Outline Dimensions (cont.)

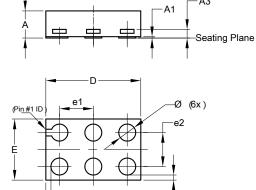
(6) Package Type X2-DFN1010-6





	X2-DFN1010-6					
Dim	Min	Max	Тур			
Α		0.40	0.39			
A1	0.00	0.05	0.02			
А3			0.13			
b	0.14	0.20	0.17			
b1	0.05	0.15	0.10			
D	0.95	1.05	1.00			
Е	0.95	1.05	1.00			
е	_		0.35			
٦	0.35	0.45	0.40			
K	0.15	_				
Z	_	_	0.065			
All	Dimens	ions in	mm			

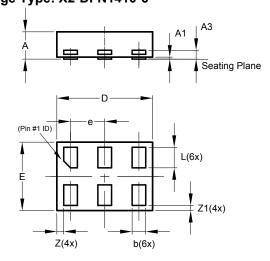
(7) Package Type: X2-DFN1409-6 6 CHIP SCALE ALTERNATIVE



X2-DFN1409-6					
Dim	Min	Max	Тур		
Α	-	0.40	0.39		
A 1	0	0.05	0.02		
A3	-	-	0.13		
Ø	0.20	0.30	0.25		
D	1.35	1.45	1.40		
Е	0.85	0.95	0.90		
e1	-	-	0.50		
e2	-	-	0.50		
Z 1	-	-	0.075		
Z2	-	-	0.075		
All	Dimens	ions in	mm		

(8) Package Type: X2-DFN1410-6

____Z1 (4x)



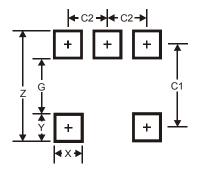
Z2 (4x)—

	X2-DFN1410-6		
Dim	Min	Max	Тур
Α	_	0.40	0.39
A1	0.00	0.05	0.02
А3	_	_	0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е			0.50
L	0.25	0.35	0.30
Z			0.10
Z 1	0.045	0.105	0.075
All Dimensions in mm			



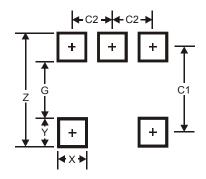
Suggested Pad Layout

(1) Package Type: SOT25



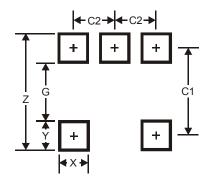
Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Υ	0.80
C1	2.40
C2	0.95

(2) Package Type: SOT353



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

(3) Package Type: SOT553

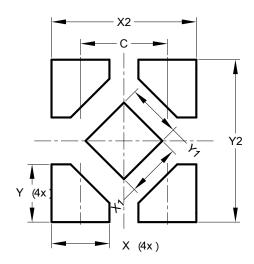


Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Υ	0.5
C1	1.7
C2	0.5



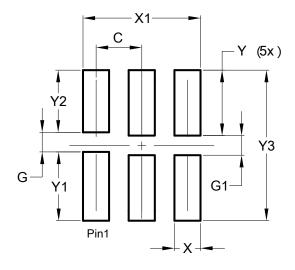
Suggested Pad Layout (cont.)

(4) Package Type X2-DFN0808-4



Dimensions	Value (in mm)
С	0.480
X	0.320
X1	0.300
X2	0.800
Y	0.320
Y1	0.300
Y2	0.900

(5)(Package Type X1-DFN1010-6

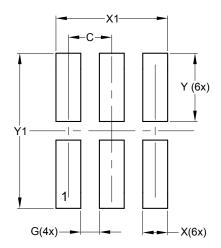


Dimensions	Value
Dimensions	(in mm)
С	0.350
G	0.150
G1	0.150
Х	0.200
X1	0.900
Υ	0.500
Y1	0.525
Y2	0.475
Y3	1.150



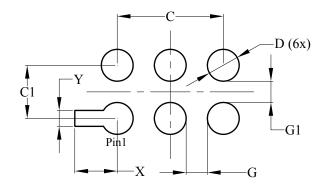
Suggested Pad Layout (cont.)

(6)(Package Type X2-DFN1010-6



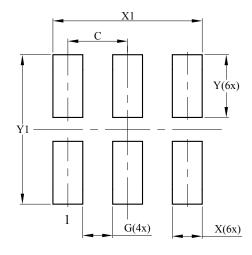
Dimensions	Value (in mm)
С	0.350
G	0.150
Х	0.200
X1	0.900
Y	0.550
Y1	1.250

(7) Package Type: X2-DFN1409-6



Dimensions	Value (in mm)
С	1.000
C1	0.500
D	0.300
G	0.200
G1	0.200
x	0.400
Y	0.150

(8) Package Type: X2-DFN1410-6



Dimensions	Value (in mm)
С	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250



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