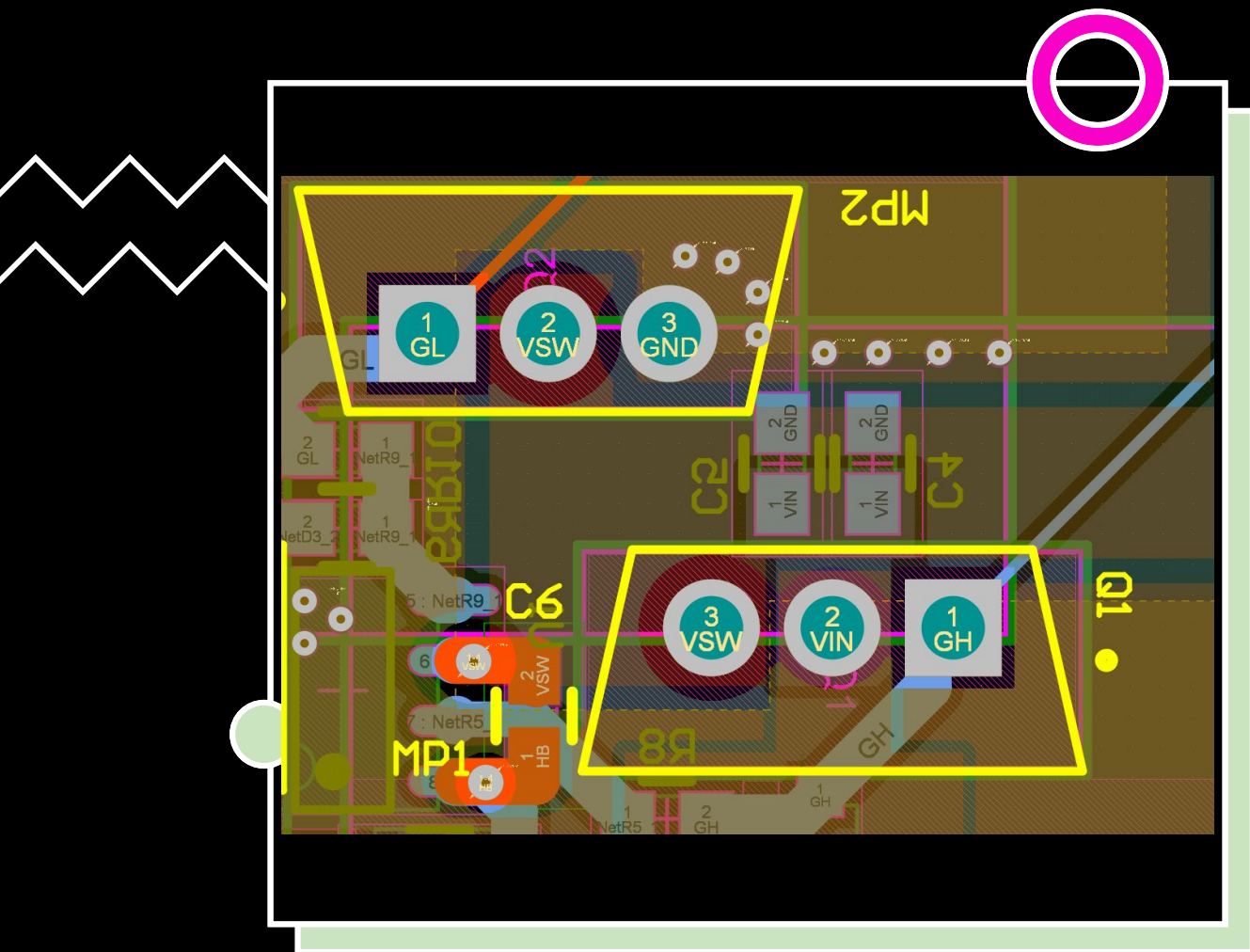




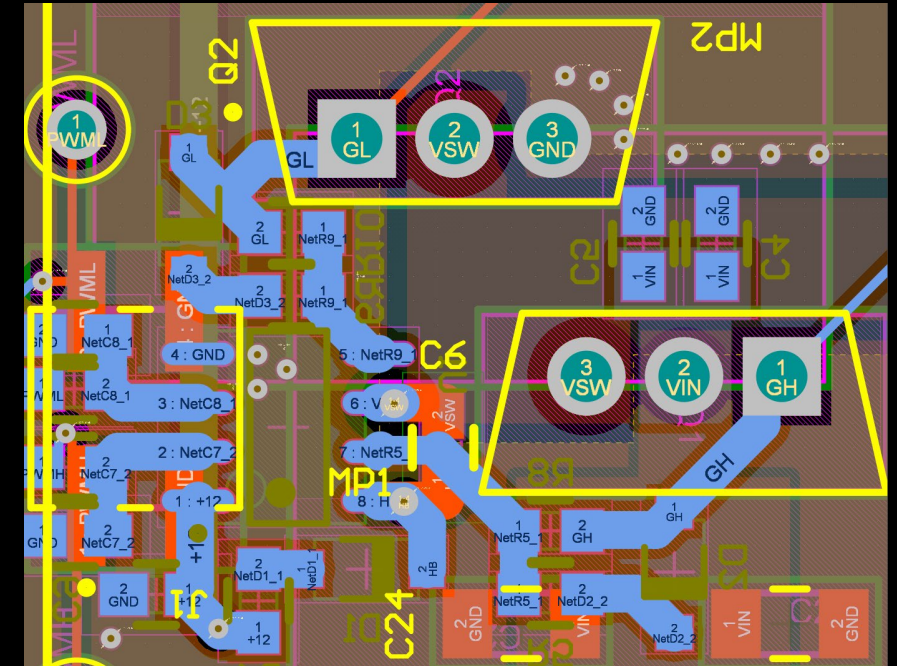
EE 113B PV BUCK CONVERTER

Designed by Smit Malde



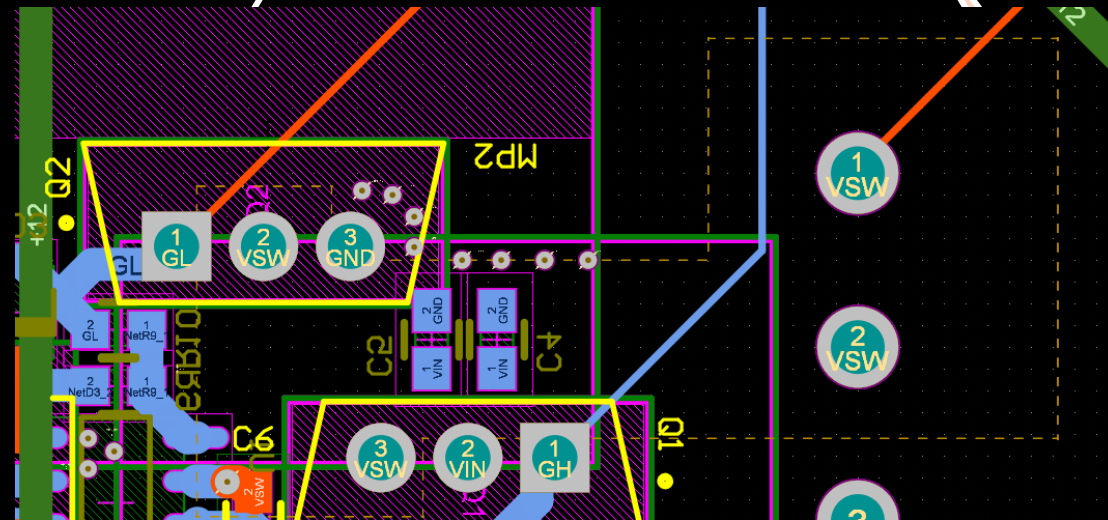
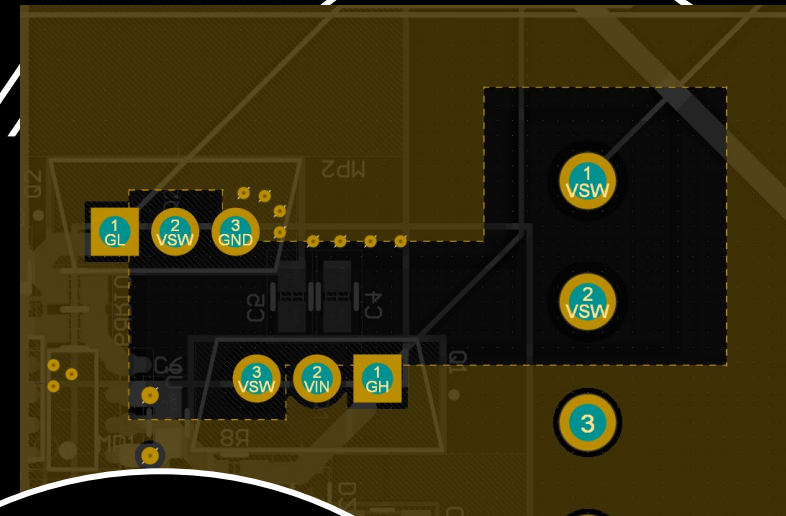
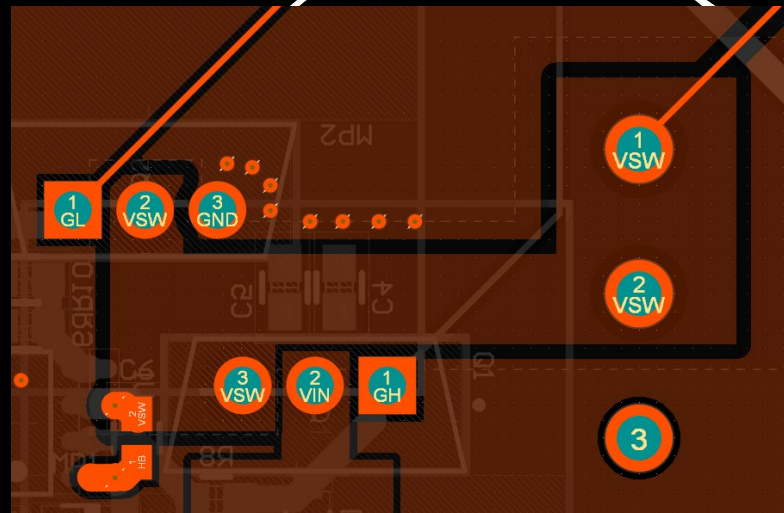
Commutation Loop



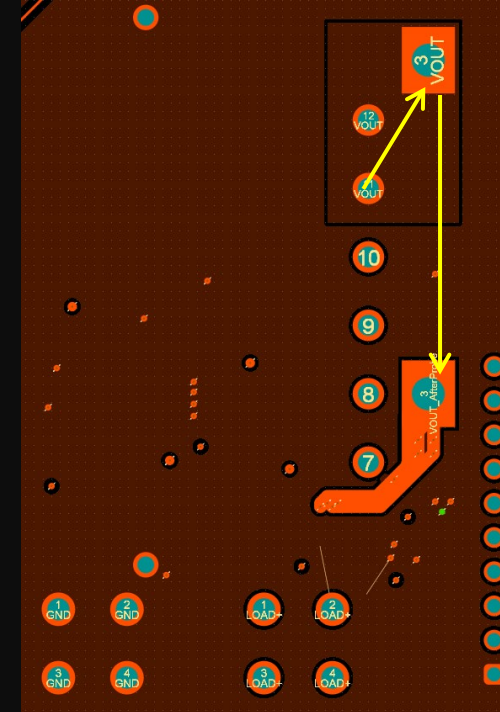
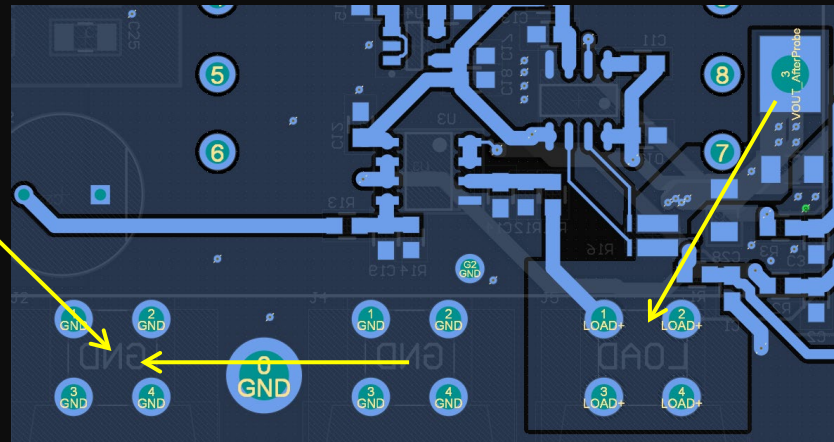
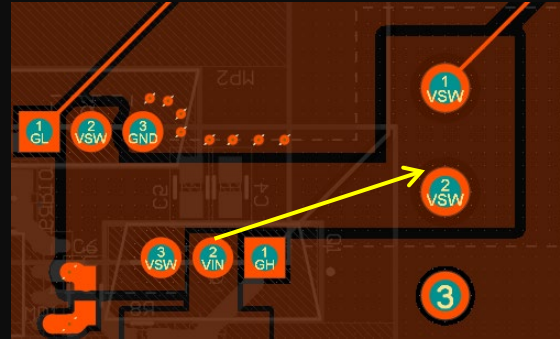
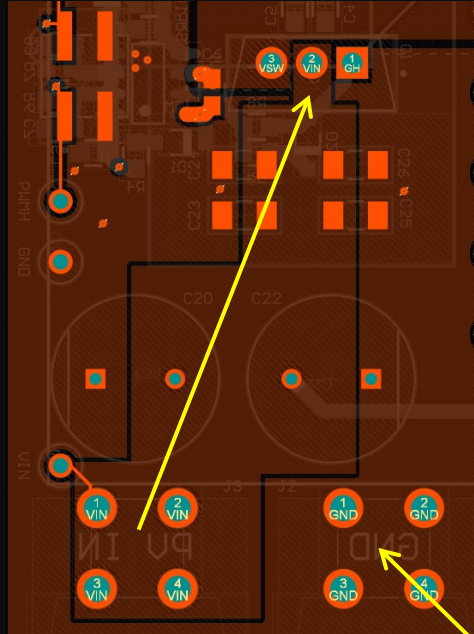




High dV/dt Loops

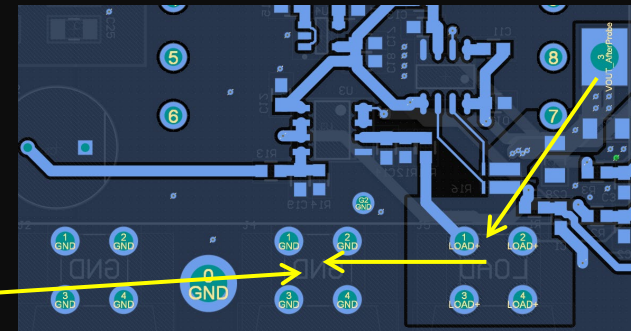
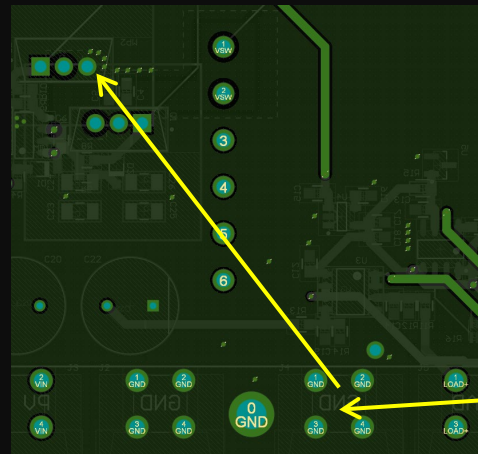
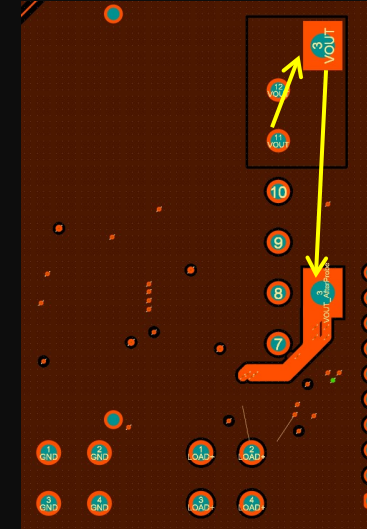
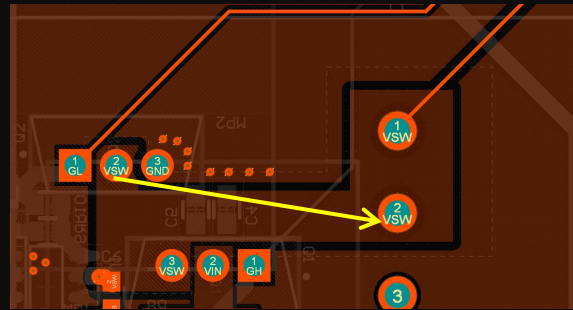


Power Stage Current Loops



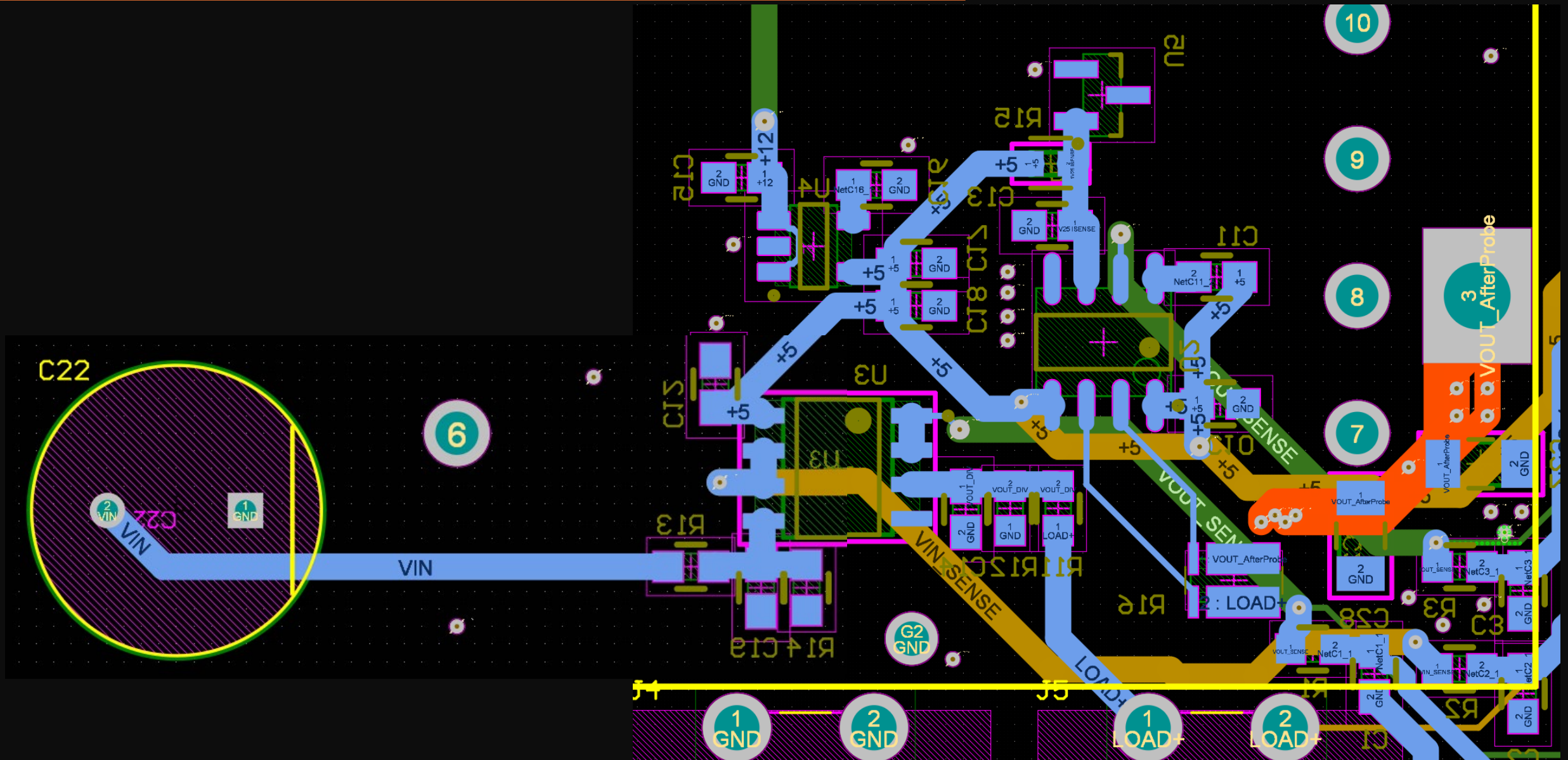
High Side Loop

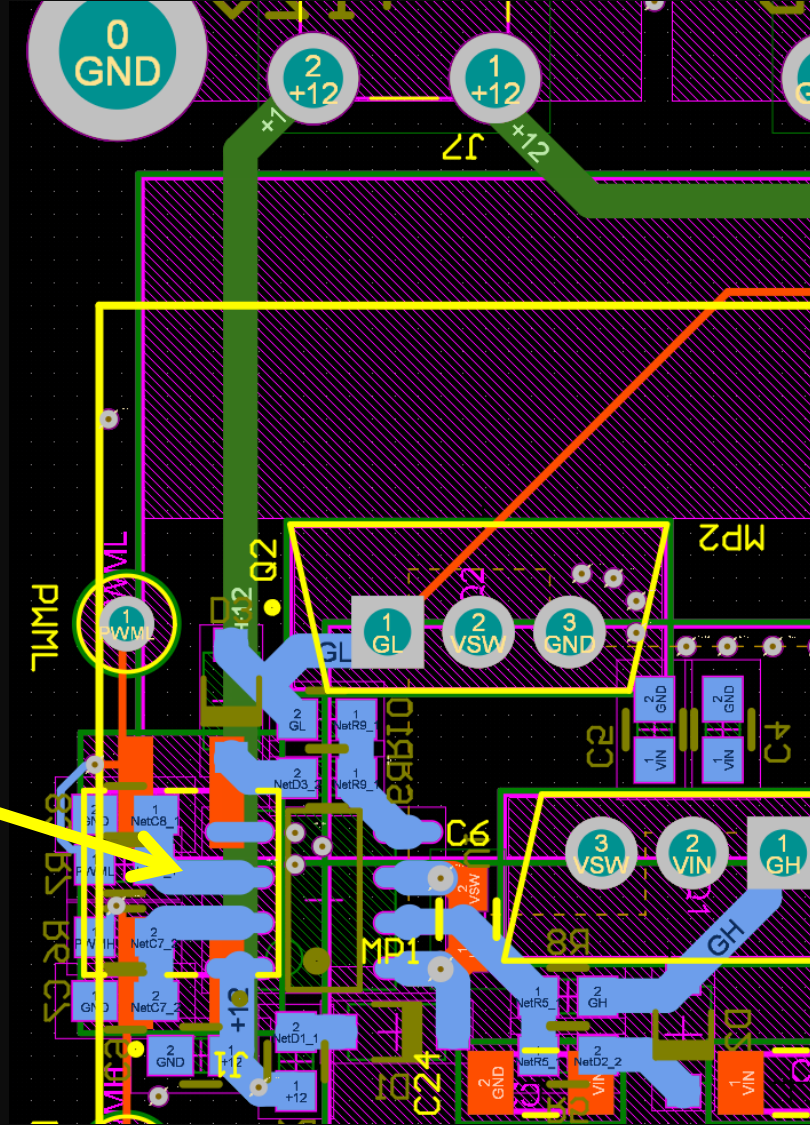
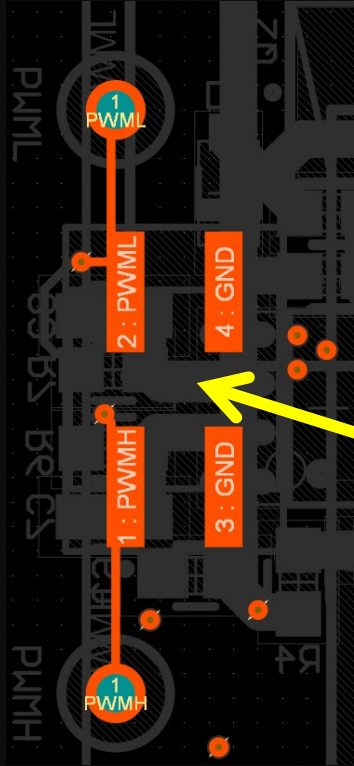
Power Stage Current Loops



Low Side Loop

Analog Sensing Circuit





Digital Circuits

Silk Screen and Test points

