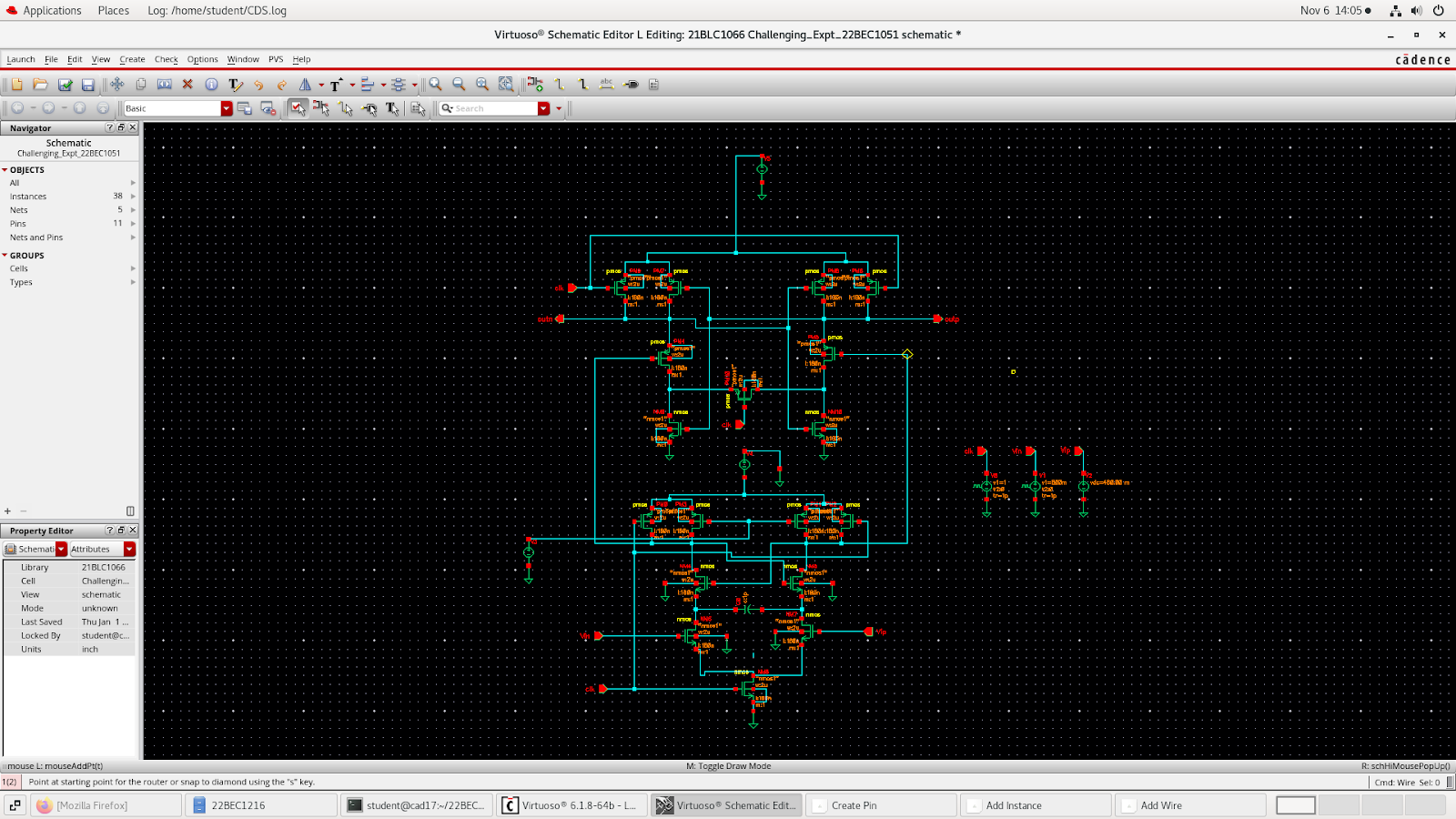
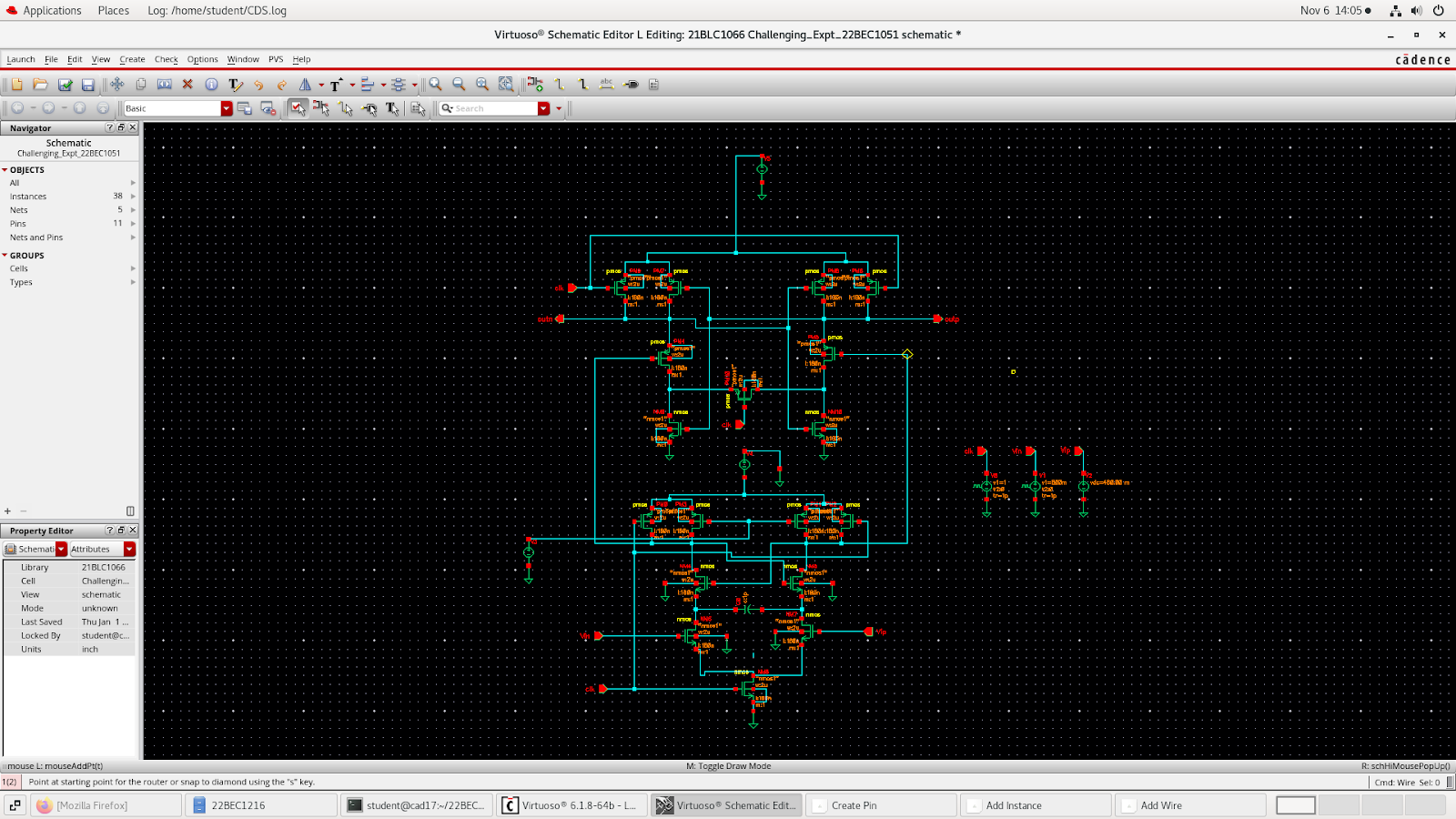
SCHEMATICS

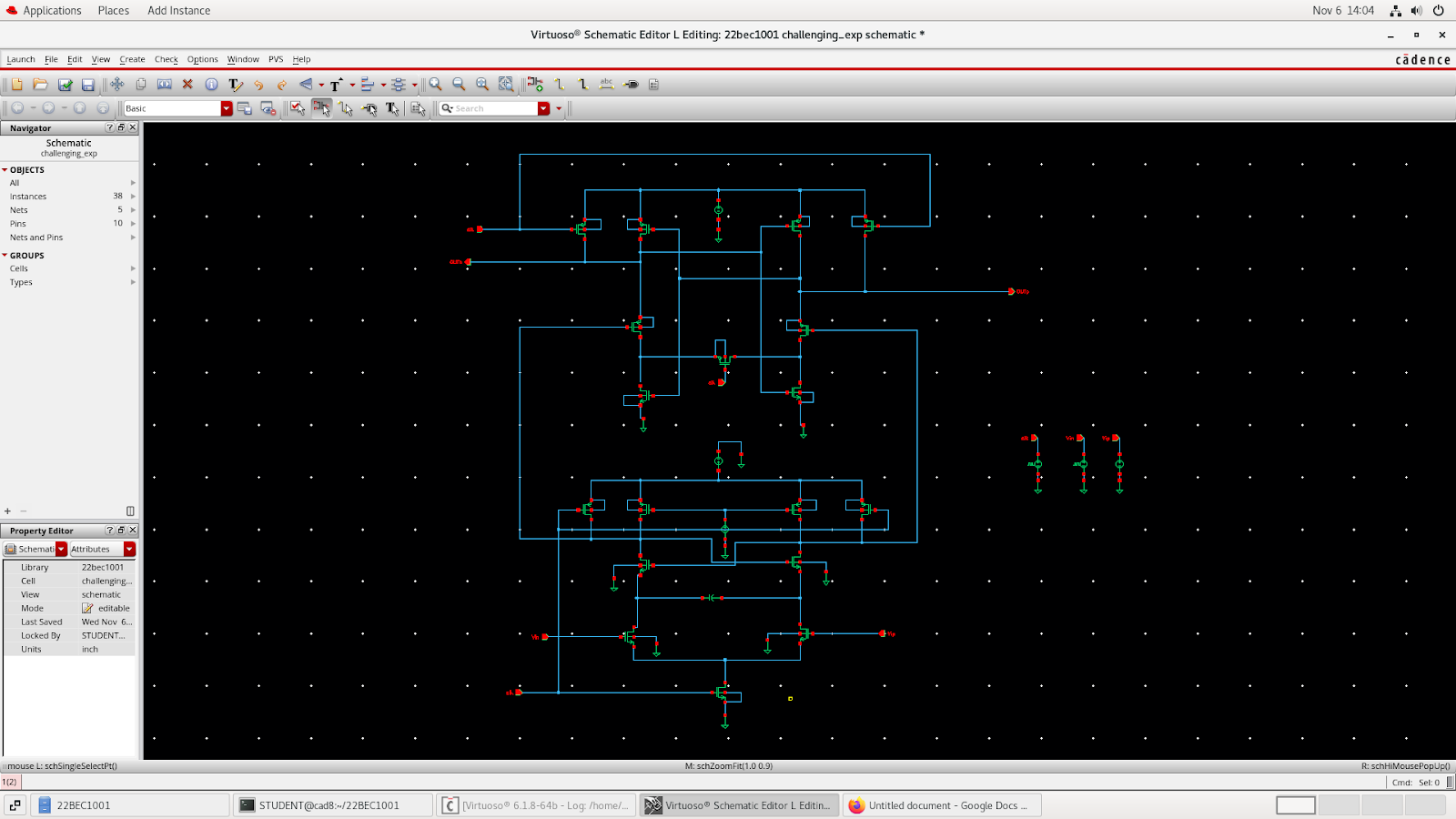


Signals



setup





**Final : Schematic of low power high-speed dynamic latched comparator**