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1 /Users/snehamishra/PycharmProjects/Test/venv/bin/python /
  Users/snehamishra/Desktop/UMKC/Fall2018/Python/Python-
  DeepLearning_Fall2018/Mod2_ICP6/Source/
  DeepLearning_SourceCode6/Mod2_ICP6.1.py
2 Using TensorFlow backend.
3 Loading data...
4 25000 train sequences
5 25000 test sequences
6 Pad sequences (samples x time)
7 x_train shape: (25000, 100)
8 x_test shape: (25000, 100)
9 Result on loaded_model is :
10 2018-11-30 17:24:15.855425: I tensorflow/core/platform/
  cpu_feature_guard.cc:141] Your CPU supports instructions
  that this TensorFlow binary was not compiled to use: AVX2
  FMA
11
12    32/25000 [.....] - ETA: 1:52
13   224/25000 [.....] - ETA: 21s
14   384/25000 [.....] - ETA: 16s
15   576/25000 [.....] - ETA: 12s
16   768/25000 [.....] - ETA: 11s
17   960/25000 [>.....] - ETA: 10s
18  1184/25000 [>.....] - ETA: 9s
19  1376/25000 [>.....] - ETA: 8s
20  1568/25000 [>.....] - ETA: 8s
21  1760/25000 [=>.....] - ETA: 8s
22  1952/25000 [=>.....] - ETA: 7s
23  2144/25000 [=>.....] - ETA: 7s
24  2336/25000 [=>.....] - ETA: 7s
25  2528/25000 [==>.....] - ETA: 7s
26  2720/25000 [==>.....] - ETA: 7s
27  2912/25000 [==>.....] - ETA: 7s
28  3104/25000 [==>.....] - ETA: 6s
29  3264/25000 [==>.....] - ETA: 6s
30  3424/25000 [===>.....] - ETA: 6s
31  3616/25000 [===>.....] - ETA: 6s
32  3776/25000 [===>.....] - ETA: 6s
33  3936/25000 [===>.....] - ETA: 6s
34  4128/25000 [===>.....] - ETA: 6s
35  4320/25000 [====>.....] - ETA: 6s
36  4512/25000 [====>.....] - ETA: 6s
37  4704/25000 [====>.....] - ETA: 6s
38  4896/25000 [====>.....] - ETA: 6s
39  5120/25000 [=====>.....] - ETA: 6s
40  5312/25000 [=====>.....] - ETA: 6s
41  5504/25000 [=====>.....] - ETA: 6s
42  5696/25000 [=====>.....] - ETA: 5s
43  5888/25000 [=====>.....] - ETA: 5s
44  6080/25000 [=====>.....] - ETA: 5s

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45	6272/25000	[=====>.....]	- ETA: 5s
46	6464/25000	[=====>.....]	- ETA: 5s
47	6656/25000	[=====>.....]	- ETA: 5s
48	6848/25000	[=====>.....]	- ETA: 5s
49	7008/25000	[=====>.....]	- ETA: 5s
50	7136/25000	[=====>.....]	- ETA: 5s
51	7296/25000	[=====>.....]	- ETA: 5s
52	7424/25000	[=====>.....]	- ETA: 5s
53	7584/25000	[=====>.....]	- ETA: 5s
54	7744/25000	[=====>.....]	- ETA: 5s
55	7872/25000	[=====>.....]	- ETA: 5s
56	8032/25000	[=====>.....]	- ETA: 5s
57	8192/25000	[=====>.....]	- ETA: 5s
58	8384/25000	[=====>.....]	- ETA: 5s
59	8544/25000	[=====>.....]	- ETA: 5s
60	8704/25000	[=====>.....]	- ETA: 5s
61	8832/25000	[=====>.....]	- ETA: 5s
62	8992/25000	[=====>.....]	- ETA: 5s
63	9184/25000	[=====>.....]	- ETA: 5s
64	9376/25000	[=====>.....]	- ETA: 4s
65	9568/25000	[=====>.....]	- ETA: 4s
66	9760/25000	[=====>.....]	- ETA: 4s
67	9984/25000	[=====>.....]	- ETA: 4s
68	10176/25000	[=====>.....]	- ETA: 4s
69	10368/25000	[=====>.....]	- ETA: 4s
70	10560/25000	[=====>.....]	- ETA: 4s
71	10752/25000	[=====>.....]	- ETA: 4s
72	10944/25000	[=====>.....]	- ETA: 4s
73	11136/25000	[=====>.....]	- ETA: 4s
74	11328/25000	[=====>.....]	- ETA: 4s
75	11520/25000	[=====>.....]	- ETA: 4s
76	11680/25000	[=====>.....]	- ETA: 4s
77	11840/25000	[=====>.....]	- ETA: 4s
78	12000/25000	[=====>.....]	- ETA: 4s
79	12160/25000	[=====>.....]	- ETA: 3s
80	12352/25000	[=====>.....]	- ETA: 3s
81	12512/25000	[=====>.....]	- ETA: 3s
82	12736/25000	[=====>.....]	- ETA: 3s
83	12960/25000	[=====>.....]	- ETA: 3s
84	13184/25000	[=====>.....]	- ETA: 3s
85	13344/25000	[=====>.....]	- ETA: 3s
86	13568/25000	[=====>.....]	- ETA: 3s
87	13760/25000	[=====>.....]	- ETA: 3s
88	13920/25000	[=====>.....]	- ETA: 3s
89	14080/25000	[=====>.....]	- ETA: 3s
90	14240/25000	[=====>.....]	- ETA: 3s
91	14400/25000	[=====>.....]	- ETA: 3s
92	14560/25000	[=====>.....]	- ETA: 3s
93	14720/25000	[=====>.....]	- ETA: 3s
94	14880/25000	[=====>.....]	- ETA: 3s

95	15104/25000	[=====>.....]	- ETA: 3s
96	15328/25000	[=====>.....]	- ETA: 2s
97	15520/25000	[=====>.....]	- ETA: 2s
98	15680/25000	[=====>.....]	- ETA: 2s
99	15840/25000	[=====>.....]	- ETA: 2s
100	16000/25000	[=====>.....]	- ETA: 2s
101	16192/25000	[=====>.....]	- ETA: 2s
102	16416/25000	[=====>.....]	- ETA: 2s
103	16640/25000	[=====>.....]	- ETA: 2s
104	16864/25000	[=====>.....]	- ETA: 2s
105	17088/25000	[=====>.....]	- ETA: 2s
106	17312/25000	[=====>.....]	- ETA: 2s
107	17504/25000	[=====>.....]	- ETA: 2s
108	17696/25000	[=====>.....]	- ETA: 2s
109	17888/25000	[=====>.....]	- ETA: 2s
110	18080/25000	[=====>.....]	- ETA: 2s
111	18272/25000	[=====>.....]	- ETA: 2s
112	18464/25000	[=====>.....]	- ETA: 1s
113	18656/25000	[=====>.....]	- ETA: 1s
114	18848/25000	[=====>.....]	- ETA: 1s
115	19040/25000	[=====>.....]	- ETA: 1s
116	19232/25000	[=====>.....]	- ETA: 1s
117	19424/25000	[=====>.....]	- ETA: 1s
118	19648/25000	[=====>.....]	- ETA: 1s
119	19872/25000	[=====>.....]	- ETA: 1s
120	20032/25000	[=====>.....]	- ETA: 1s
121	20192/25000	[=====>.....]	- ETA: 1s
122	20384/25000	[=====>.....]	- ETA: 1s
123	20576/25000	[=====>.....]	- ETA: 1s
124	20768/25000	[=====>.....]	- ETA: 1s
125	20928/25000	[=====>.....]	- ETA: 1s
126	21088/25000	[=====>.....]	- ETA: 1s
127	21248/25000	[=====>.....]	- ETA: 1s
128	21408/25000	[=====>.....]	- ETA: 1s
129	21568/25000	[=====>.....]	- ETA: 1s
130	21728/25000	[=====>....]	- ETA: 0s
131	21920/25000	[=====>....]	- ETA: 0s
132	22080/25000	[=====>....]	- ETA: 0s
133	22240/25000	[=====>....]	- ETA: 0s
134	22400/25000	[=====>....]	- ETA: 0s
135	22560/25000	[=====>...]	- ETA: 0s
136	22720/25000	[=====>...]	- ETA: 0s
137	22880/25000	[=====>...]	- ETA: 0s
138	23040/25000	[=====>...]	- ETA: 0s
139	23232/25000	[=====>...]	- ETA: 0s
140	23424/25000	[=====>..]	- ETA: 0s
141	23584/25000	[=====>..]	- ETA: 0s
142	23744/25000	[=====>..]	- ETA: 0s
143	23904/25000	[=====>..]	- ETA: 0s
144	24064/25000	[=====>..]	- ETA: 0s

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145 24224/25000 [=====>.] - ETA: 0s
146 24416/25000 [=====>.] - ETA: 0s
147 24640/25000 [=====>.] - ETA: 0s
148 24864/25000 [=====>.] - ETA: 0s
149 25000/25000 [=====] - 8s 303us/
    step
150 [1.6985481432724, 0.49768]
151
152 Process finished with exit code 0
153
```