



Snehadeep Gayen | CS21B078







B. Tech Computer Science and Engineering

Minor in Mathematics





Indian Institute of Technology, Madras



EDUCATION

- B. Tech CSE** | CGPA 9.94/10  Jul '21 - Present
Indian Institute of Technology Madras  Chennai, TN
- HSC Class 12th** | 98.17%  Apr '20 - Apr '21
Pace Junior Science College  Mumbai, MH
- ICSE Class 10th** | 98.80%  Apr '18 - Apr '19
Lilavatibai Podar High School  Mumbai, MH

EXPERIENCE

- Software Internship at Optiver Amsterdam**  May '24 - Jul '24
- Worked in the Quant Research & Data Team of Optiver Delta1
 - Added functionality to create TCP/IP filters from session configuration files for packet parsers and optimised them for performance.
 - Added functionality to convert timestamps across timezones, accounting for Daylight Saving Time changes
 - Analysed SQL queries and designed a new OneTick database with Schema to replace a saturated PostGres time series database.
- Team Avishkar Hyperloop, CFI**  Oct '22 - Jul '23
- Part of Embedded Software Team of the **Main Control Unit** and **Navigation Unit** of our Hyperloop Pod.
 - Used **RTOS** to collect and store data from over 20 sensors using various communication protocols at **low latency**.
 - Demonstrated our Hyperloop Pod in the prestigious **European Hyperloop Week - Scotland 2023** among over 25 teams globally, and were runner-ups in the Best Sense and Control System award.
- Teaching Assistant, CSE Dept, IIT Madras**  Aug '24 - Present
- Worked as a Teaching Assistant and designed Verilog labs for Computer System Design course under Dr. Sutanu Chakraborty
- Tutor & Contributor, NPTEL**  March '23 - Present
- Created **YouTube tutorials** for previous years' GATE CS questions
 - These tutorials aim to support applicants who may have limited access to resources

CODING ACHIEVEMENTS

- Rating 1806 **Expert** on Codeforces
- ICPC 2022 - AIR 151** and **Institute Rank 7** in Kanpur Qualifiers
- AIR 3** in Shaastra CP Potpourri (Mixed-bag coding contest)
[Shaastra is Asia's largest student-run Techfest]
- Global Rank 9** in CodeChef Starters 96
- Global Rank 231** in Codeforces Round 881
- 1st place in Inter-School Java Competition in Mumbai

KEY COURSES & LABS

- | | |
|--|---|
| • Computer System Design | • Router Architecture & Algorithms † |
| • Data Structures | • Principles of Economics |
| • Computer Architecture | • Microeconomics |
| • Theory of Computation | • Macroeconomics |
| • Object Oriented Programming | • Introduction to Game Theory |
| • Compiler Design | • Probability Theory † |
| • Operating Systems | • Combinatorics † |
| • Functional & Logical Programming | • Linear Programming and Combinatorial Optimisation † |
| • Computer Networks | • Linear Algebra † |
| • Pattern Recognition & Machine Learning | |

SOFTWARE SKILLS

- Languages:** C++, C, HDL (Verilog), OCaml, Python, Java, Prolog, SQL, x86, MIPS and 8085 ASM, HTML & CSS, R
- Tools:** TI CCS, Git, ~~LaTeX~~, AutoCAD, GDB
- Libraries:** TI RTOS, NumPy, PyLops, Matplotlib

EXTRACURRICULAR ACTIVITIES

- Sports:** Awarded 13 medals in Track & Field and Best Athlete U14 in High School, Taekwondo Red Dan II Belt, NSO Athlete at IITM
- Mentored incoming freshmen under **Saathi, IIT Madras**

SCHOLASTIC ACHIEVEMENTS

- Awarded Sri V Ramachandran Prize for **Highest CGPA** in Semesters 3 & 4 of B.Tech and Dual Degree in Computer Science
- Secured **AIR 5** in JEE Mains '19 out of 1 million students
- Secured **AIR 161** in JEE Advanced '19
- Secured **AIR 10** in Indian Statistical Institute Exam
- Secured **AIR 21** in INChO and attended Orientation Camp for International Chemistry Olympiad
- Awarded KVPY Fellowship '21 with **AIR 338**
- Winner of Mimamsa '22 at IISER Pune | 4th place in Chemenigma '22 at IISC Bangalore | Won Silver Medal in Homi Bhabha Science Competition (conducted in Maharashtra)

PROJECTS


WiFi Sensing for IoT using Machine Learning *C, Python*
Undergraduate Research Project under Prof. Ayon Chakraborty

- Created an end-to-end IoT pipeline for real-time Human Activity Recognition using WiFi CSI (Channel State Information) Sensing and Machine Learning on the Server
- Analysed the effect of compression on CSI data and its tradeoffs on the Network Bandwidth, Energy Consumption & Sensing Accuracy.
- Submitted the work in AIoT workshop organised in Athens, Greece.

Custom Protocol Headers for Network Support *P4, Python*
Undergraduate Research Project under Prof. Krishna Moorthy Sivalingam


- Ideation of a custom protocol header to improve network telemetry or security using P4 switch data plane programming language.
- Deployment & Testing on Intel Tofino switches

Java Compiler Design  *Java, C*


CS3300 Course Project - Prof. Krishna Nandivada  Jan-May '23


- Designed a MIPS compiler for a subset of Java with Lexical Analyser, Parsing, Type Checking, IR Generation, Register Allocation, Stack Handling, and MIPS code generation

OS Scheduler and Memory Management Unit Design  *Java*

CS3500 Course Project - Prof. Prashant LA  Jan-May '23


- Implemented a Memory Management Unit with LRU Page replacement Policy
- Implemented a Multi-Level Feedback Queue Scheduler for processes

LAN Chat Server & Music Streaming  *C*

CS3205 Course Project - Prof. Ayon Chakraborty  Jan-May '23

- Implemented a LAN Chat Server from scratch using C Socket API
- Implemented a Music Streaming Server and Client using C Socket API and Unix ALSA Library


Prolog & Functional Programming Interpreter  *OCaml, Prolog*

CS3100 Course Project - Prof. Kartik Nagar  Jan-May '23


- Implemented a Prolog interpreter in OCaml
- Implemented a basic functional programming language using Logic Programming in Prolog


CPU Design  *Verilog*

CS2610 Course Project - Prof. C. Chandra Sekhar  Jan-May '23

CS2310 Course Project - Prof. Ayon Chakraborty  Jul-Nov '22

- Implemented a CPU with **Register file** and **ALU** with instructions to perform Arithmetic and Logical operations on both 8-bit integers and 12-bit floating-point numbers
- Built a combinational 8-bit CPU with structural gate-level Verilog

Closeness Centrality Algorithm  *C++*

Project under Prof. Manikandan Narayanan  May-Jun '23

- Implemented the CENDY algorithm, an on-line algorithm for updating Average Path Length and Closeness Centrality in a Dynamic Graph, based on this paper. 