



# Snehadeep Gayen | CS21B078

B. Tech Computer Science and Engineering with Honors

Minor in Mathematics

Indian Institute of Technology, Madras



## EDUCATION

<b>B. Tech CSE</b>   CGPA 9.94/10 <i>Indian Institute of Technology Madras</i>	📅 Jul '21 - Present 📍 Chennai, TN
<b>HSC Class 12<sup>th</sup></b>   98.17% <i>Pace Junior Science College</i>	📅 Apr '20 - Apr '21 📍 Mumbai, MH
<b>ICSE Class 10<sup>th</sup></b>   98.80% <i>Lilavatibai Podar High School</i>	📅 Apr '18 - Apr '19 📍 Mumbai, MH

## EXPERIENCE

<b>Software Internship at Optiver Amsterdam</b> 📅 May'24 - Jul'24
<ul style="list-style-type: none"><li>Worked in the Quant Research &amp; Data Team of Optiver Delta1</li><li>Added functionality to create TCP/IP filters from session configuration files for packet parsers and optimised them for performance.</li><li>Added functionality to convert timestamps across timezones, accounting for Daylight Saving Time changes</li><li>Analysed SQL queries and designed a new OneTick database with Schema to replace a saturated PostGres time series database.</li></ul>
<b>Team Avishkar Hyperloop, CFI</b> 📅 Oct '22 - Jul'23
<ul style="list-style-type: none"><li>Part of Embedded Software Team of the <b>Main Control Unit and Navigation Unit</b> of our Hyperloop Pod.</li><li>Used <b>RTOS</b> to collect and store data from over 20 sensors using various communication protocols at <b>low latency</b>.</li><li>Demonstrated our Hyperloop Pod in the prestigious <b>European Hyperloop Week - Scotland 2023</b> among over 25 teams globally, and were runner-ups in the Best Sense and Control System award.</li></ul>
<b>Teaching Assistant, CSE Dept, IIT Madras</b> 📅 Aug '24 - Present
<ul style="list-style-type: none"><li>Worked as a Teaching Assistant and designed Verilog labs for Computer System Design course under Dr. Sutanu Chakraborty</li></ul>
<b>Tutor &amp; Contributor, NPTEL</b> 📅 March '23 - Present
<ul style="list-style-type: none"><li>Created <b>YouTube tutorials</b> for previous years' GATE CS questions</li><li>These tutorials aim to support applicants who may have limited access to resources</li></ul>

## CODING ACHIEVEMENTS

- Rating 1806 [Expert](#) on Codeforces
- ICPC 2022 - AIR 151** and **Institute Rank 7** in Kanpur Qualifiers
- AIR 3** in Shaastra CP Potpourri (Mixed-bag coding contest)  
*[Shaastra is Asia's largest student-run Techfest]*
- Global Rank 9** in CodeChef Starters 96
- Global Rank 231** in Codeforces Round 881
- 1st place in Inter-School Java Competition in Mumbai

## KEY COURSES & LABS

<ul style="list-style-type: none"><li>Computer System Design</li><li>Data Structures</li><li>Computer Architecture</li><li>Theory of Computation</li><li>Object Oriented Programming</li><li>Compiler Design</li><li>Operating Systems</li><li>Functional &amp; Logical Programming</li><li>Computer Networks</li><li>Pattern Recognition &amp; Machine Learning</li><li>Router Architecture &amp; Algorithms †*</li></ul>	<ul style="list-style-type: none"><li>Principles of Economics</li><li>Microeconomics *</li><li>Macroeconomics *</li><li>Introduction to Game Theory</li><li>Probability Theory †</li><li>Combinatorics †</li><li>Linear Programming and Combinatorial Optimisation †</li><li>Linear Algebra †</li><li>† - Graduate Level Course</li><li>* - Ongoing</li></ul>
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## SOFTWARE SKILLS

- Languages:** C++, C, HDL (Verilog), OCaml, Python, Java, Prolog, SQL, x86, MIPS and 8085 ASM,  $\LaTeX$ , HTML & CSS, R,
- Tools:** CMake, Make, git, gdb/lddb, TI CCS, AutoCAD, GTKWave
- Libraries:** TI RTOS, NumPy, PyLops, Matplotlib

## SCHOLASTIC ACHIEVEMENTS

- Awarded Sri V Ramachandran Prize for **Highest CGPA** in Semesters 3 & 4 of B.Tech and Dual Degree in Computer Science
- Secured **AIR 5** in JEE Mains '19 out of 1 million students
- Secured **AIR 161** in JEE Advanced '19
- Secured **AIR 10** in Indian Statistical Institute Exam
- Secured **AIR 21** in INChO and attended Orientation Camp for International Chemistry Olympiad
- Awarded KVPY Fellowship '21 with **AIR 338**
- Winner of Mimamsa '22 at IISER Pune | 4<sup>th</sup> place in Chemenigma '22 at IISC Bangalore | Won Silver Medal in Homi Bhabha Science Competition (conducted in Maharashtra)

## PROJECTS

<b>WiFi Sensing for IoT using Machine Learning</b> <i>C, Python</i> <i>Undergraduate Research Project under Prof. Ayon Chakraborty</i>
<ul style="list-style-type: none"><li>Created an end-to-end IoT pipeline for real-time Human Activity Recognition using WiFi CSI (Channel State Information) Sensing and Machine Learning on the Server</li><li>Analysed the effect of compression on CSI data and its tradeoffs on the Network Bandwidth, Energy Consumption &amp; Sensing Accuracy.</li><li>Submitted part of the work in <b>AIoT workshop organised in Athens, Greece</b>.</li></ul>
<b>Custom Protocol Headers for Network Support</b> *Ongoing <i>P4, Python</i> <i>Undergraduate Research Project under Prof. Krishna Moorthy Sivalingam</i>
<ul style="list-style-type: none"><li>Ideation of a custom protocol header to improve network telemetry or security using P4 switch data plane programming language.</li><li>Deployment &amp; Testing on Intel Tofino switches</li></ul>
<b>Java Compiler Design</b> <i>Java, C</i> <i>CS3300 Course Project - Prof. Krishna Nandivada</i> 📅 Jan-May '23
<ul style="list-style-type: none"><li>Designed a MIPS compiler for a subset of Java with Lexical Analyser, Parsing, Type Checking, IR Generation, Register Allocation, Stack Handling, and MIPS code generation</li></ul>
<b>OS Scheduler and Memory Management Unit Design</b> <i>Java</i> <i>CS3500 Course Project - Prof. Prashant LA</i> 📅 Jan-May '23
<ul style="list-style-type: none"><li>Developed a Multi-Level Feedback Queue for OS process scheduling.</li><li>Designed and implemented a Memory Management Unit with an LRU page replacement policy.</li></ul>
<b>LAN Chat Server &amp; Music Streaming</b> <i>C</i> <i>CS3205 Course Project - Prof. Ayon Chakraborty</i> 📅 Jan-May '23
<ul style="list-style-type: none"><li>Built a LAN Chat Server from scratch using only C Socket API</li><li>Developed a Music Streaming Server and Client using C Socket API and Unix ALSA Library</li></ul>
<b>CPU Design</b> <i>Verilog</i> <i>CS2610 Course Project - Prof. C. Chandra Sekhar</i> 📅 Jan-May '23 <i>CS2310 Course Project - Prof. Ayon Chakraborty</i> 📅 Jul-Nov '22
<ul style="list-style-type: none"><li>Implemented a CPU with <b>Register file</b> and <b>ALU</b> with instructions to perform Arithmetic and Logical operations on both 8-bit integers and 12-bit floating-point numbers</li><li>Built a combinational 8-bit CPU with structural gate-level Verilog</li></ul>
<b>Closeness Centrality Algorithm</b> <i>C++</i> <i>Project under Prof. Manikandan Narayanan</i> 📅 May-Jun '23
<ul style="list-style-type: none"><li>Implemented the CENDY algorithm, an on-line algorithm for updating Average Path Length and Closeness Centrality in a Dynamic Graph, based on this paper. </li></ul>

## EXTRACURRICULAR ACTIVITIES

- Sports:** Awarded 13 medals in Track & Field and Best Athlete U14 in High School, Taekwondo Red Dan II Belt, NSO Athlete at IITM
- Mentored incoming freshmen under **Saathi, IIT Madras**