



AD口电压高于1.1V, PD0输出高, LED亮。
AD口电压低于1V, PD0输出低, LED灭

S2、S1、S0 开启内部上拉电阻

PD5、PD7 输出PWM。每0.5s改变一次PWM占空比
(0% -> 100% -> 50% -> 25% 循环)

S2_S1_S0 = 000 PWM_output_100KHz

S2_S1_S0 = 001 PWM_output_40kHz

S2_S1_S0 = 010 PWM_output_20kHz

S2_S1_S0 = 011 PWM_output_10KHz

S2_S1_S0 = 100 PWM_output_5KHz

S2_S1_S0 = 101 PWM_output_2KHz

S2_S1_S0 = 110 PWM_output_1KHz

S2_S1_S0 = 111 PWM_output_500Hz

Title		
Size	Number	Revision
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Date:	16-Mar-2018	Sheet of
File:	电路设计文件 原理图文件 MyDesign.ddb	Drawn By: