CpE 213 - Chapter 1 HW Chi: 1-17

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- 1. Intel 8080, 1971, developed by Intel
- 2 MOS Technology 6502 Zilog - Z80
- 3 8051 introduced 1980 Predecessor - 8748 Introduced - 1976
- 4. RAM
 ROM contains contents when powered-off, nonvolatile
- 45 program counter; memory address of next instruction
 - 6 on address bus! contents of program counter on data bus! data (the instruction aposale) is read from RAM goes from CPU to address bus; to RAM; from RAM to databus to CPU 7, 218 = 262144 bits = 32768 bytes
 - 8 16-bit means there are 16 lines on the data bus
 - 9 Online available to CPV N/O human intervention upon requed of program archival hold duty rough needed to require manual Coording onto system
 - 10 optical disks such as CD-ROMS + WORM tedinology

- Il doign peripheral devices w/ humans in mind; the goal being the Safe, consortable, & efficient making of characterists of people w/ muchines they use
- 12 Input ' joyotick, mouse, mucrophone Output: light pen, landspeaken
- 13. Input DO + put Subrostnes directly manipulate the hardware of the system
- 14 actuator outputs (motors, relays)
 sensors monitoring devices for inputs (photo transmitors,
 thermostors)
- 15. The control program stored permuently in ROM furnished to used more in mucrocontroller Bystems because it is too costly to have the IC interface whother storage, devices
- 16 microcontrollers hours instruction sets catering to control of inputs trostputs
- 17. PC speakers

 Computer mouse

 CD-ROM drive

 Stereo

 cordless telephone

 VCR

 digital coffee maker