

10

GrE III : HW #10
 10.1, 10.2, 10.3
 3 4 3

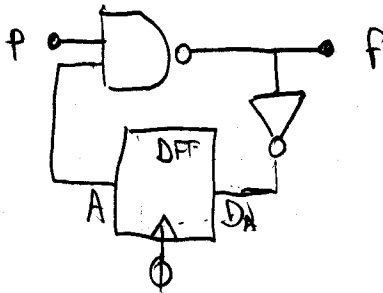
Jesse Ahn
 12/1/99

10. G: circuit

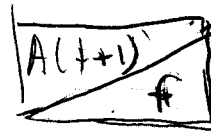
F: a) $f(t)$ and $D_A(t)$

b) state table c) state diagram

S:

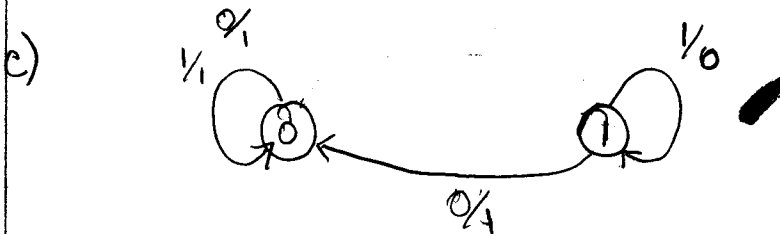


a) $f(t) = \overline{p \cdot A}$
 $D_A(t) = p \cdot A$

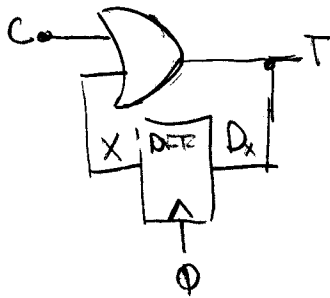


b)

	P	
	0	1
A		
0	0	0
1	1	1

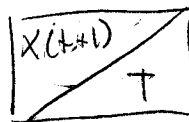


1026: diagram XOR2 replaced w/ OR2
 F: a) state table b) state diagram
 S:

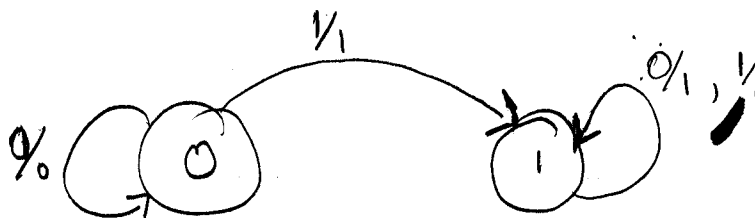


a)

		C	
		0	1
X	0	0	0
	1	1	1



b)



103 G: table
F: state diagram
S:

