

CpE 213 - Digital Systems Design
Tentative Schedule (as of 8/26/2003)

Lecture	Date	Topic
1	8/26	Syllabus and motivational, Digital logic review (some Ch 1)
2	8/28	Digital logic review, Addr Decoding (some Ch 1)
3	9/2	Wimp51 – intro to computer organization (handout)
4	9/4	Wimp51 – intro to computer organization
5	9/9	The 8051 – Hardware summary (Ch 2)
6	9/11	The 8051 – Hardware summary (Ch 2)
7	9/16	The 8051 – Hardware summary (Ch 2)
8	9/18	8051 Instruction set (Ch 3)
9	9/23	8051 Instruction set (Ch 3)
10	9/25	ASM Programming (Ch 7) and Keil Debugger (handout)
11	9/30	Exam 1
12	10/2	Review of C programming (handout)
13	10/7	Review of C programming (handout)
14	10/9	C for the 8051 (handout)
15	10/14	Applications in C and ASM (handouts, Ch 10?)
16	10/16	Applications in C and ASM (handouts, Ch 10?)
17	10/21	Applications in C and ASM (handouts, Ch 10?)
18	10/23	Mixing C and ASM (handout)
19	10/28	Interrupts (Ch 6)
20	10/30	Interrupts (Ch 6)
21	11/4	Interrupts (Ch 6)
22	11/6	Exam 2
23	11/11	Interrupts (Ch 6)
24	11/13	Timers/Counters (Ch 4)
25	11/18	Timers/Counters (Ch 4)
26	11/20	Serial Communication (Ch 5)
27	11/25	Serial Communication (Ch 5)
	11/27	Thanksgiving Holiday – no class
28	12/2	Design Process (Ch 9)
29	12/4	Multi-Tasking/ real-time systems (handouts)
30	12/9	Multi-Tasking/ real-time systems (handouts)
31	12/11	Catch-up and review; Evaluations and surveys.
	12/19	FINAL EXAM (8:00 to 10:00 am)