Appendix B. Directive Summary

Directive	Format	Description
BIT	*symbol* BIT *bit_address*	Define a bit address in bit data space.
BSEG	BSEG [AT *absolute address*]	Define an absolute segment within the bit address space.
CODE	*symbol* CODE *code_address*	Assign a symbol name to a specific address in the code space.
CSEG	CSEG [AT *absolute address*]	Define an absolute segment within the code address space.
DATA	*symbol* DATA *data_address*	Assign a symbol name to a specific on-chip data address.
DB	[*label*:] DB *expression* [*, expression* ...]	Generate a list of byte values.
DBIT	[*label*:] DBIT *expression*	Reserve a space in bit units.
DD	[*label*:] DD *expression* [*, expression* ...]	Generate a list of double word values.
DS	[*label*:] DS *expression*	Reserve space in byte units.
DSB †	[*label*:] DSB *expression*	Reserve space in byte units.
DSD †	[*label*:] DSD *expression*	Reserve space in double word units.
DSEG	DSEG [AT *absolute address*]	Define an absolute segment within the indirect internal data space.
DSW †	[*label*:] DSW *expression*	Reserve space in word units; advances the location counter of the current segment.
DW	[*label*:] DW *expression* [, *expression* ...]	Generate a list of word values.
END	END	Indicate end of program.
EQU	EQU *expression*	Set symbol value permanently.
EVEN †	EVEN	Ensure word alignment for variables.
EXTRN **EXTERN** †	EXTRN *class* [:*type*] (*symbol* [, *symbol* ...]) EXTERN *class* [:*type*] (*symbol* [,*symbol* ...])	Defines symbols referenced in the current module that are defined in other modules.
IDATA	*symbol* IDATA *idata_address*	Assign a symbol name to a specific indirect internal address.
ISEG	ISEG [AT *absolute address*]	Define an absolute segment within the internal data space.
LABEL †	*name*[:] LABEL [*type*]	Assign a symbol name to a address location within a segment.
LIT †	*symbol* LIT *'literal string'*	Assign a symbol name to a string.
NAME	NAME *modulname*	Specify the name of the current module.
ORG	ORG *expression*	Set the location counter of the current segment.
PROC † **ENDP** †	*name* PROC [*type*] *name* ENDP	Define a function start and end.

† New features in the A251 assembler and the MCS 251 architecture

Directive	Format	Description
PUBLIC	PUBLIC *symbol* [, *symbol* ...]	Identify symbols which can be used outside the current module.
RSEG	RSEG *seg*	Select a relocatable segment.
SEGMENT	*seg* SEGMENT *class* [*reloctype*] [*alloctype*]	Define a relocatable segment.
SET	SET *expression*	Set symbol value temporarily.
USING	USING *expression*	Set the predefined symbolic register address and reserve space for the specified register bank.
XDATA	*symbol* XDATA *xdata_address*	Assign a symbol name to a specific off-chip data address.
XSEG	XSEG [AT *absolute address*]	Define an absolute segment within the external data address space.