

Keystone (Group 6)
dlofran2, mwdanie2, gentil2
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ECE 411 – MP3.3
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Roadmap

For the fourth checkpoint, we will have to implement a static branch predictor, as well as the eviction write buffer and performance counters. The main paper diagram will show the integration of those components to the rest of the processor.

For the static branch predictor, we decided to opt for a static taken branch predictor design, as we feel that it will be more fit for the implementation of the rest of design. As for the eviction write buffer, we all agreed that having it between the L2 cache and the main memory will be the best option. As usual, we might change our minds during the process of implementing all of those new components.

We decided to split the work for this checkpoint the same way we did for the three previous checkpoints.