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# Simulating the Evolution of Software Variants with VEVOS

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Gothenburg, Sweden

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BERN**



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# Clone-and-own and variability

$V_0$  1 int x = foo();

# Clone-and-own in growing projects

$V_3$

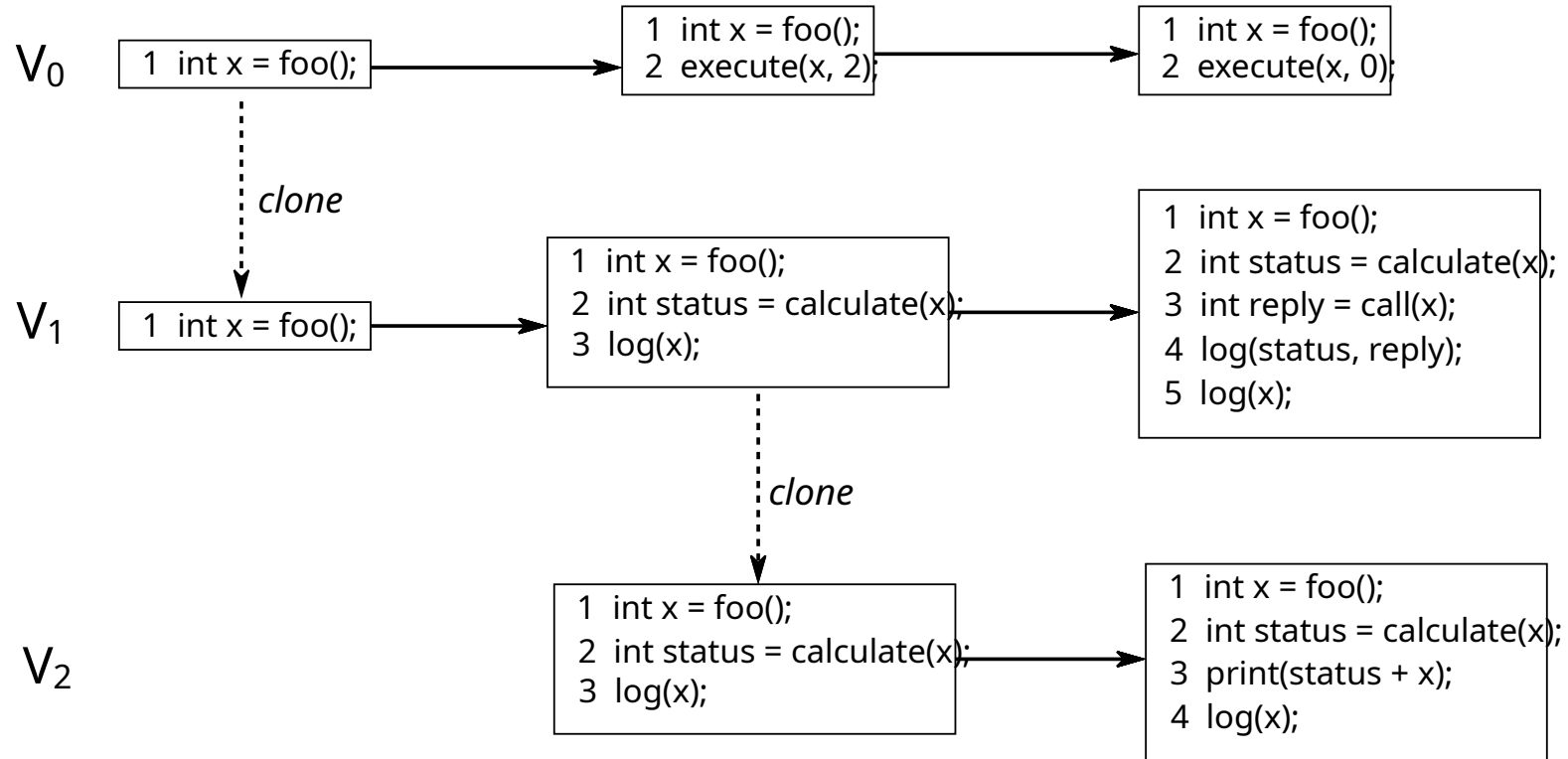
$V_1$

$V_0$  —————

$V_2$

$V_4$

# Problem: No explicit knowledge about features



What items are on our data wishlist?

# 1. Source code of variants

$V_0$

```
1 int x = foo();  
2 execute(x, 0);
```

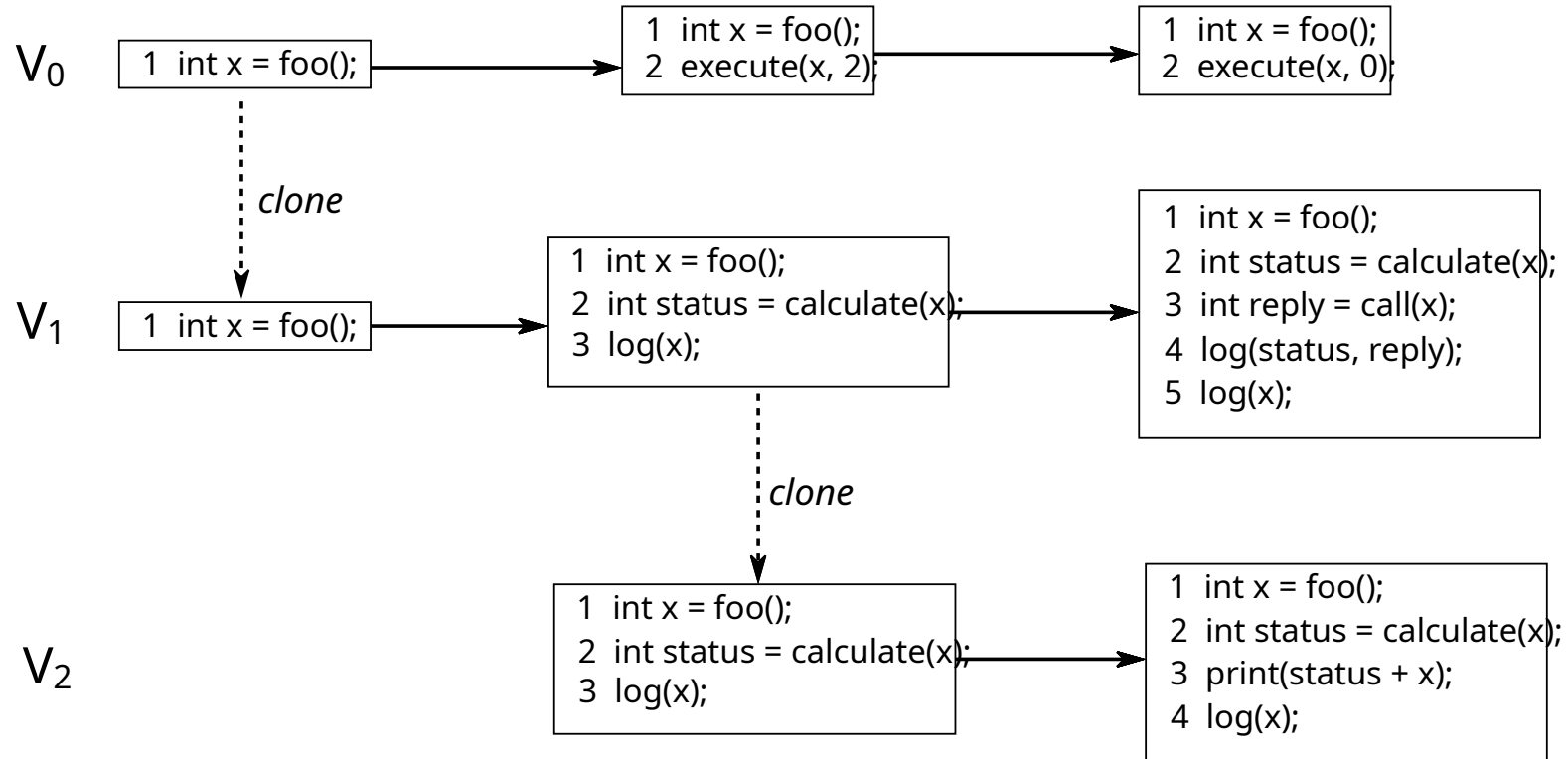
$V_1$

```
1 int x = foo();  
2 int status = calculate(x);  
3 int reply = call(x);  
4 log(status, reply);  
5 log(x);
```

$V_2$

```
1 int x = foo();  
2 int status = calculate(x);  
3 print(status + x);  
4 log(x);
```

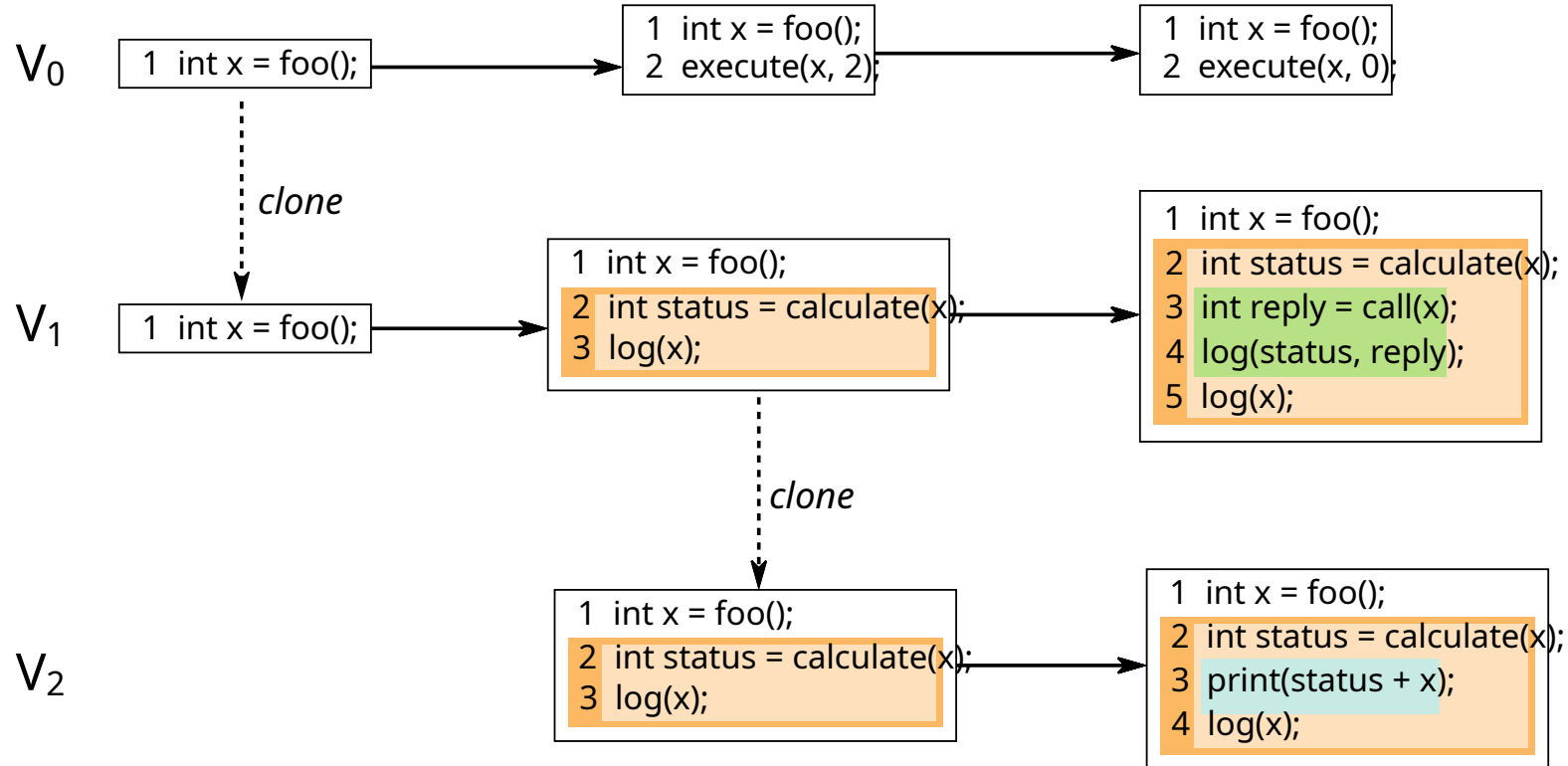
## 2. Evolution of variants



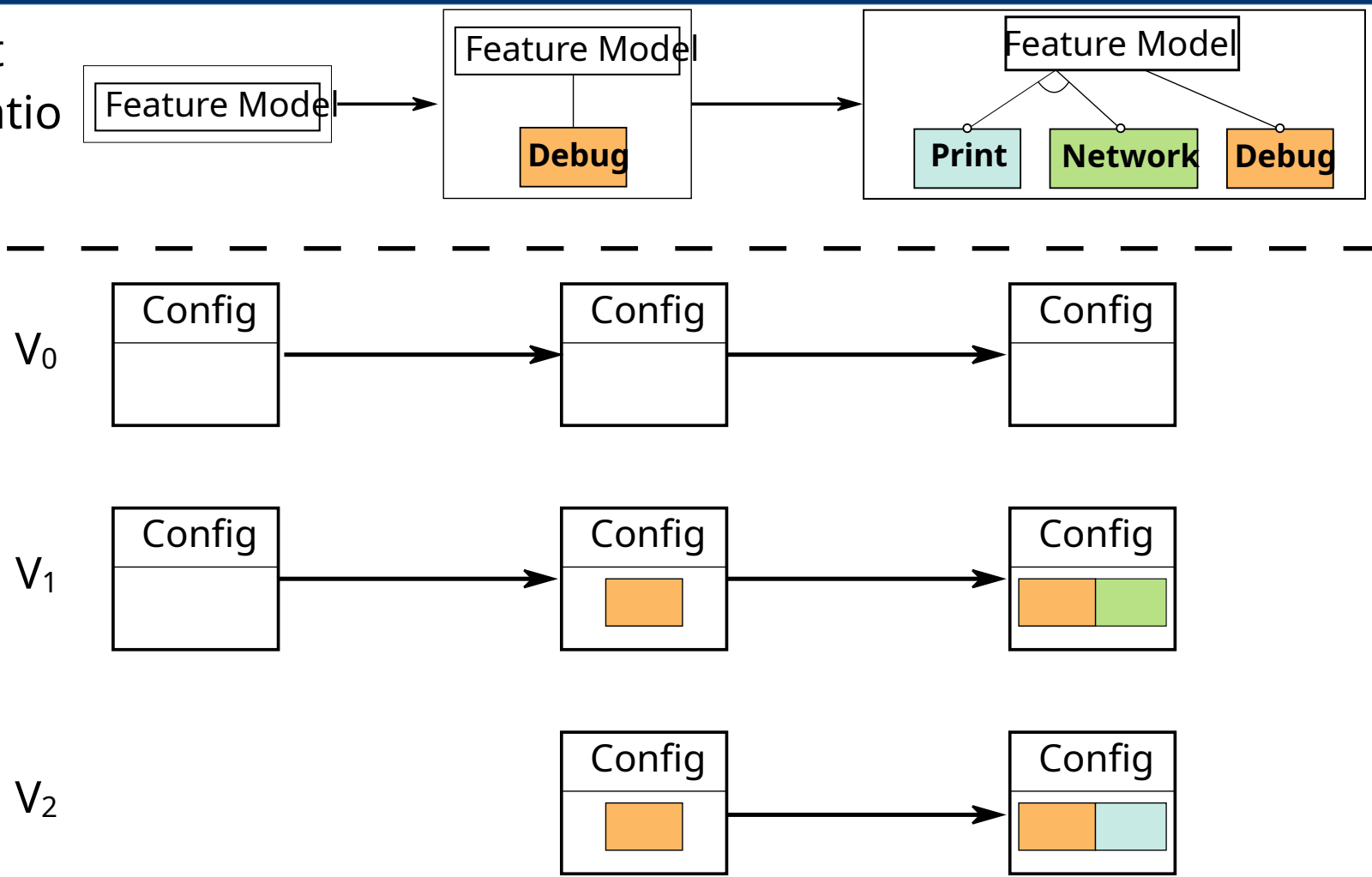


#### 4. Evolution of the feature model

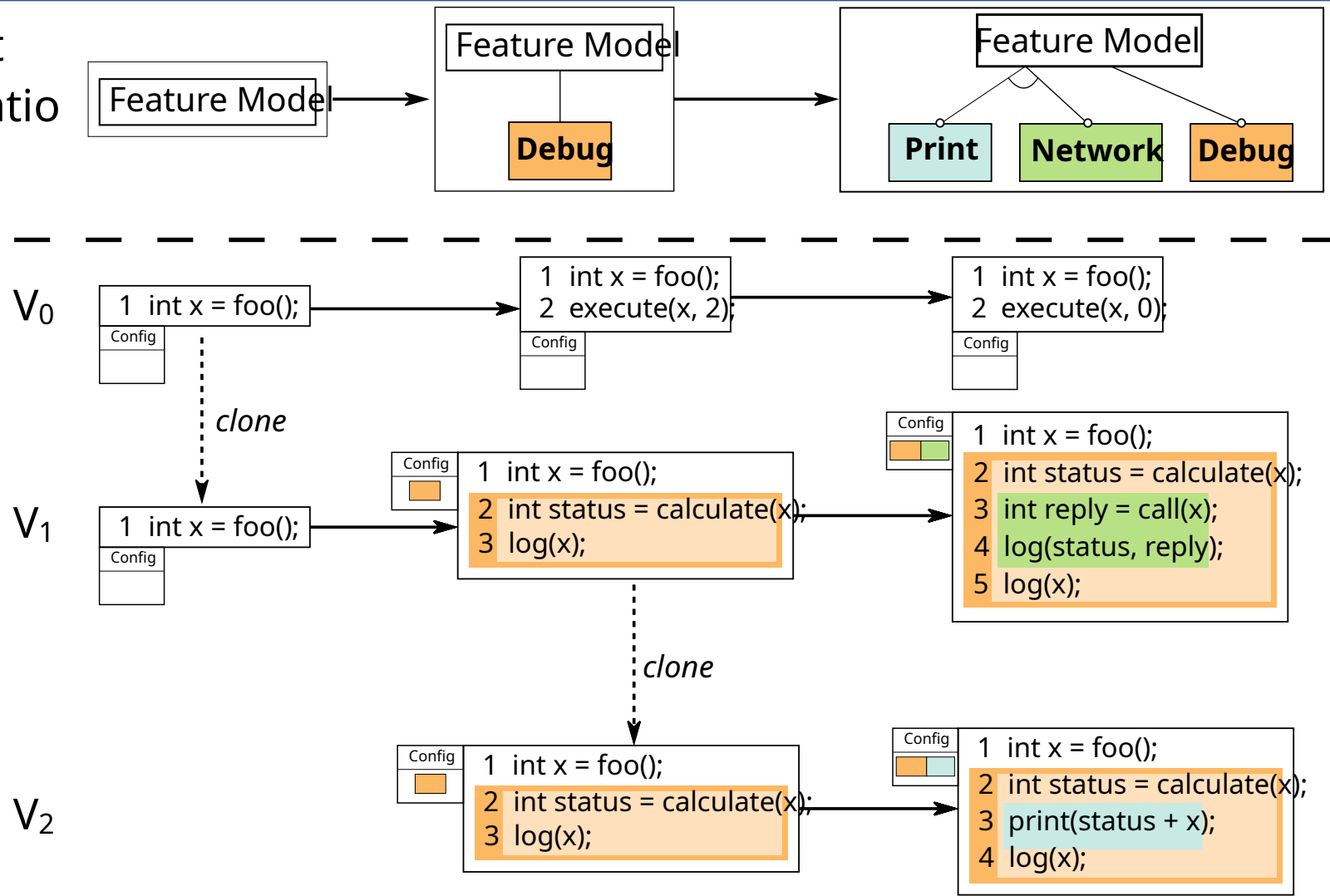
#### 3. Feature mappings of source code



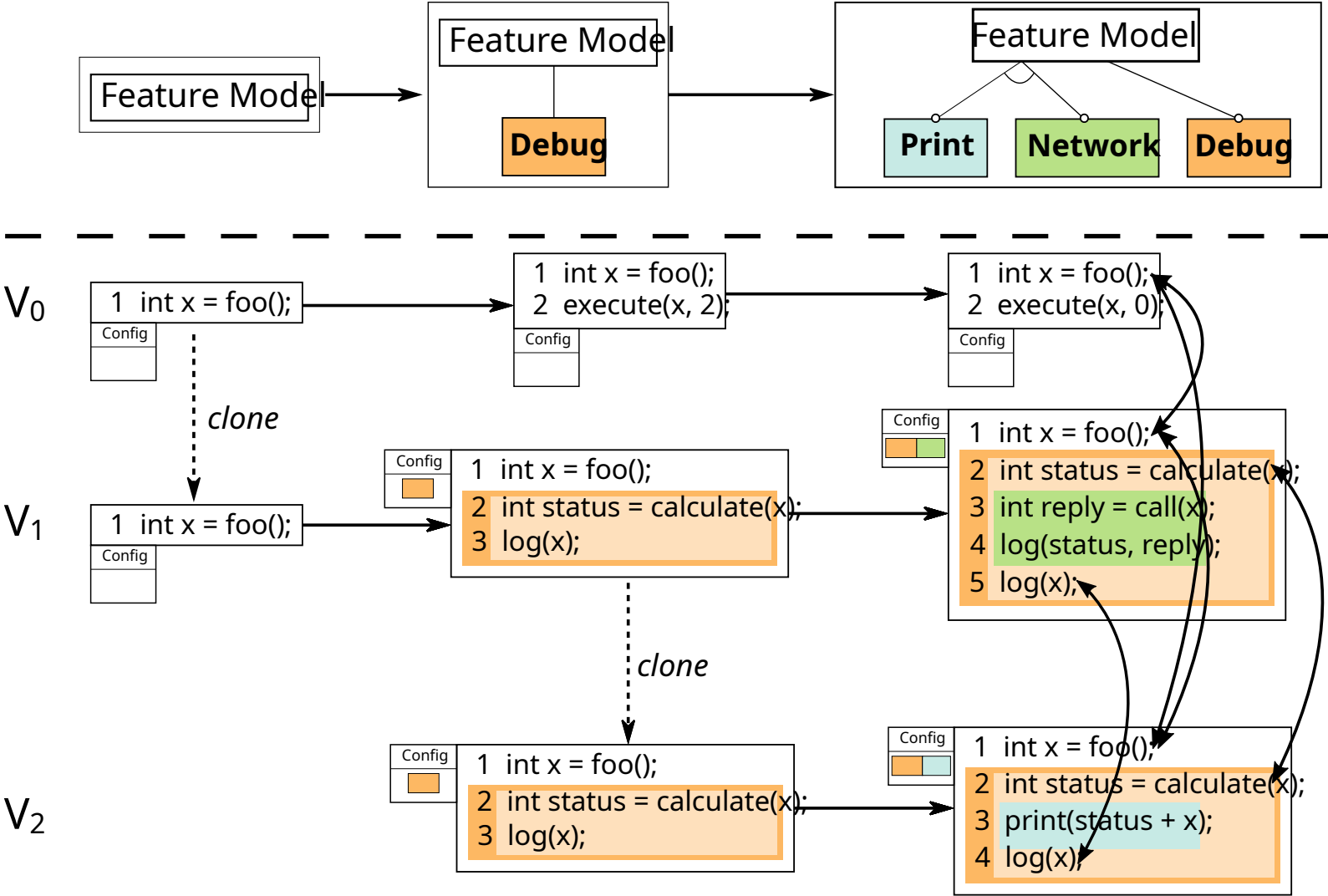
## 5. Variant configurations



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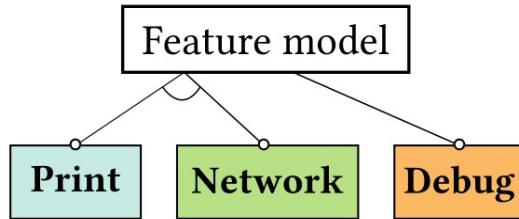


6. Code matching



Where can we get this data?

# Software product lines



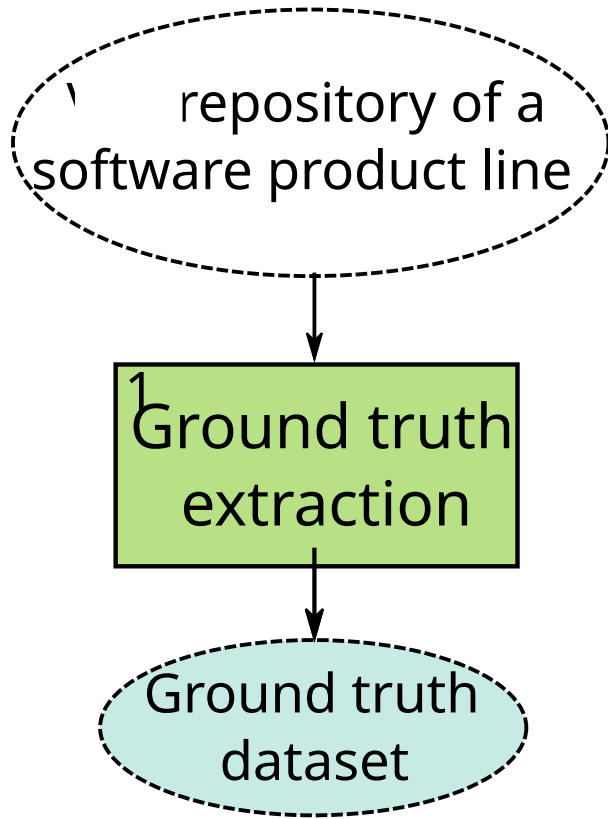
# VEVOS

# VEVOS

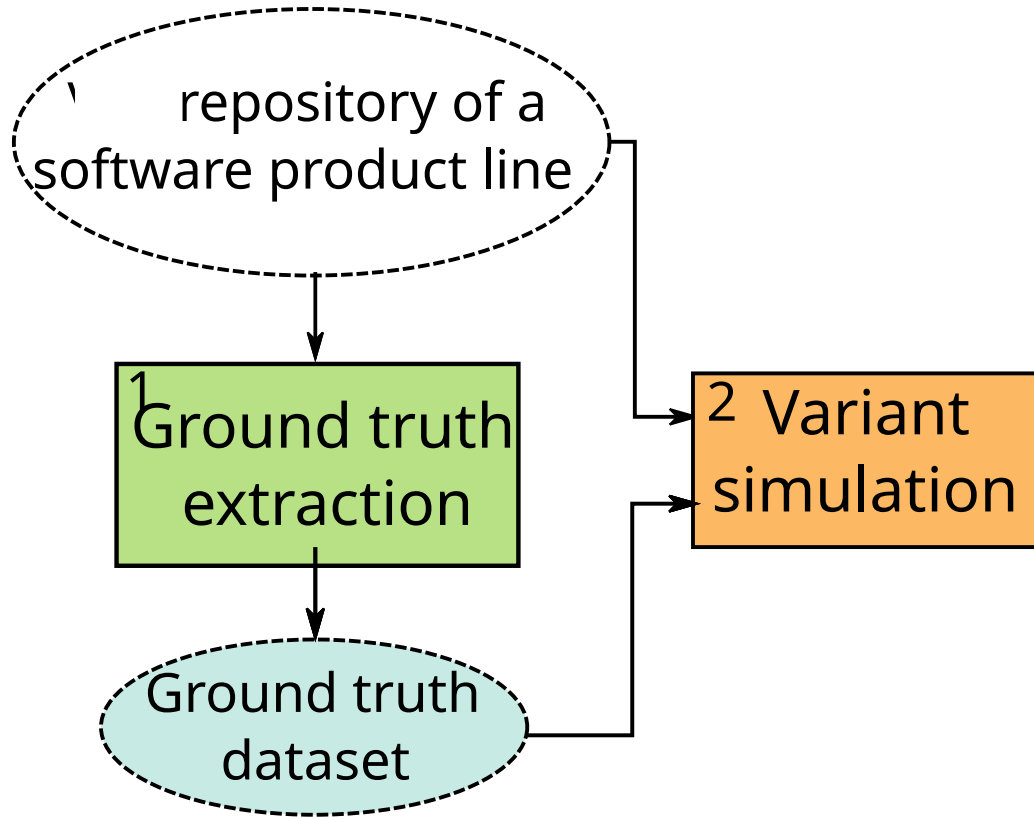




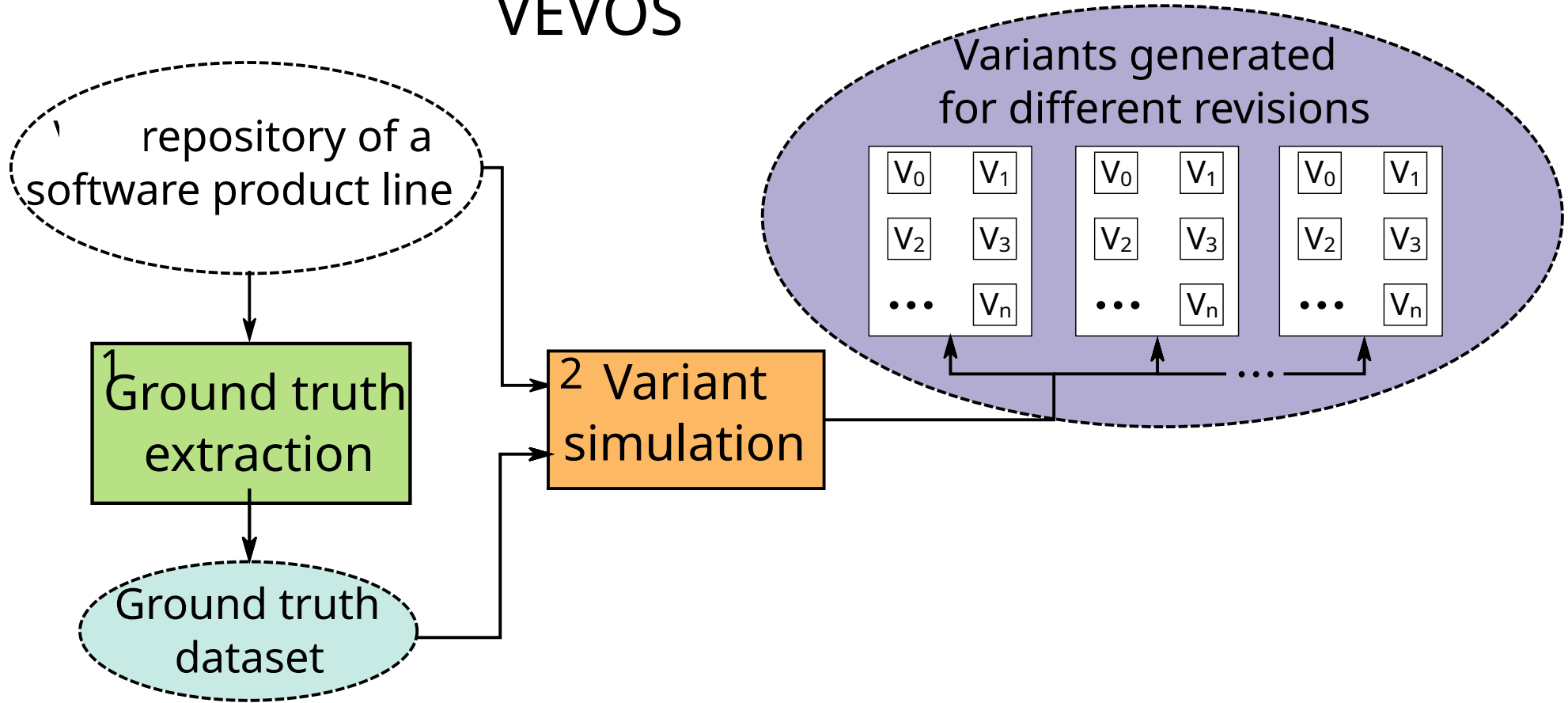
# VEVOS



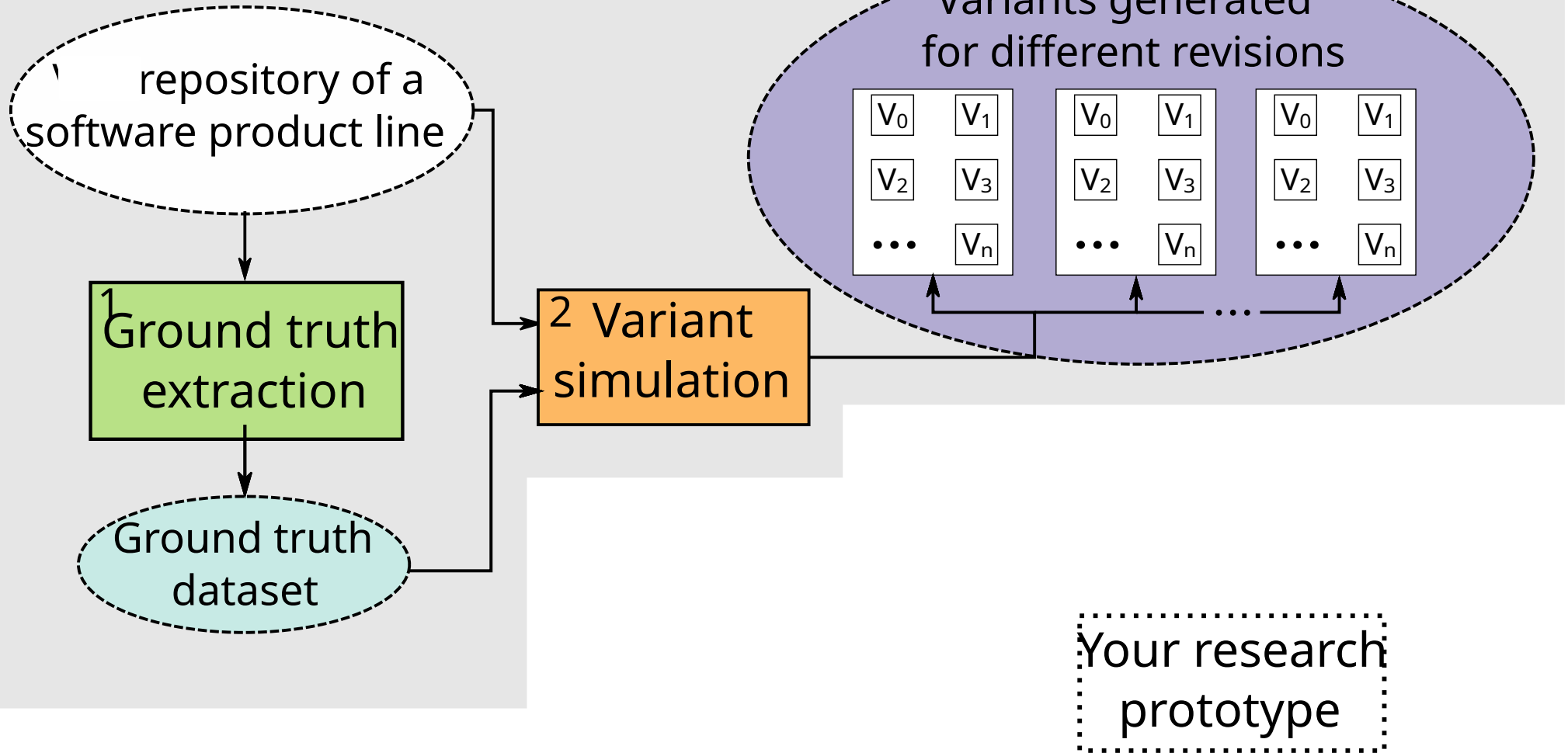
# VEVOS



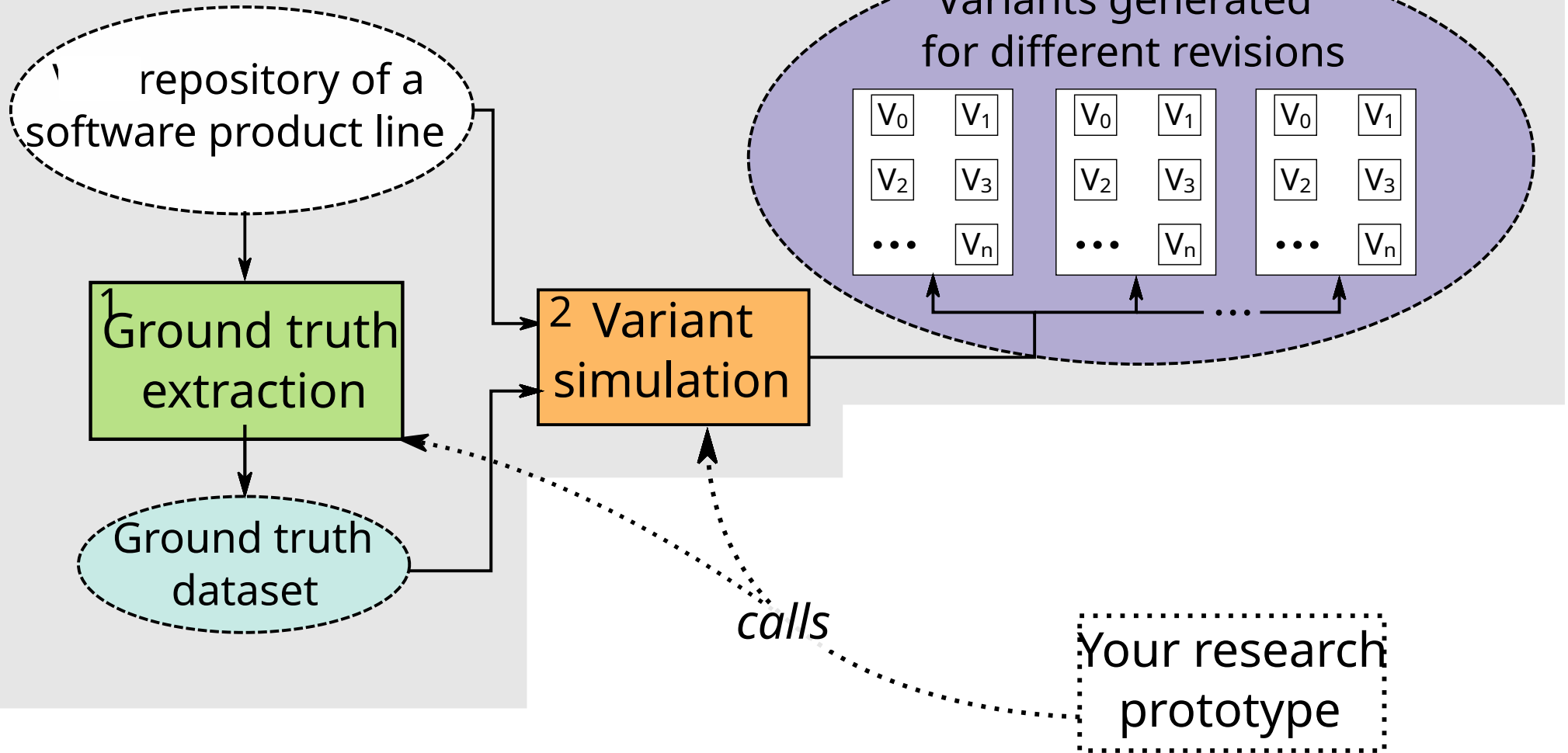
# VEVOS



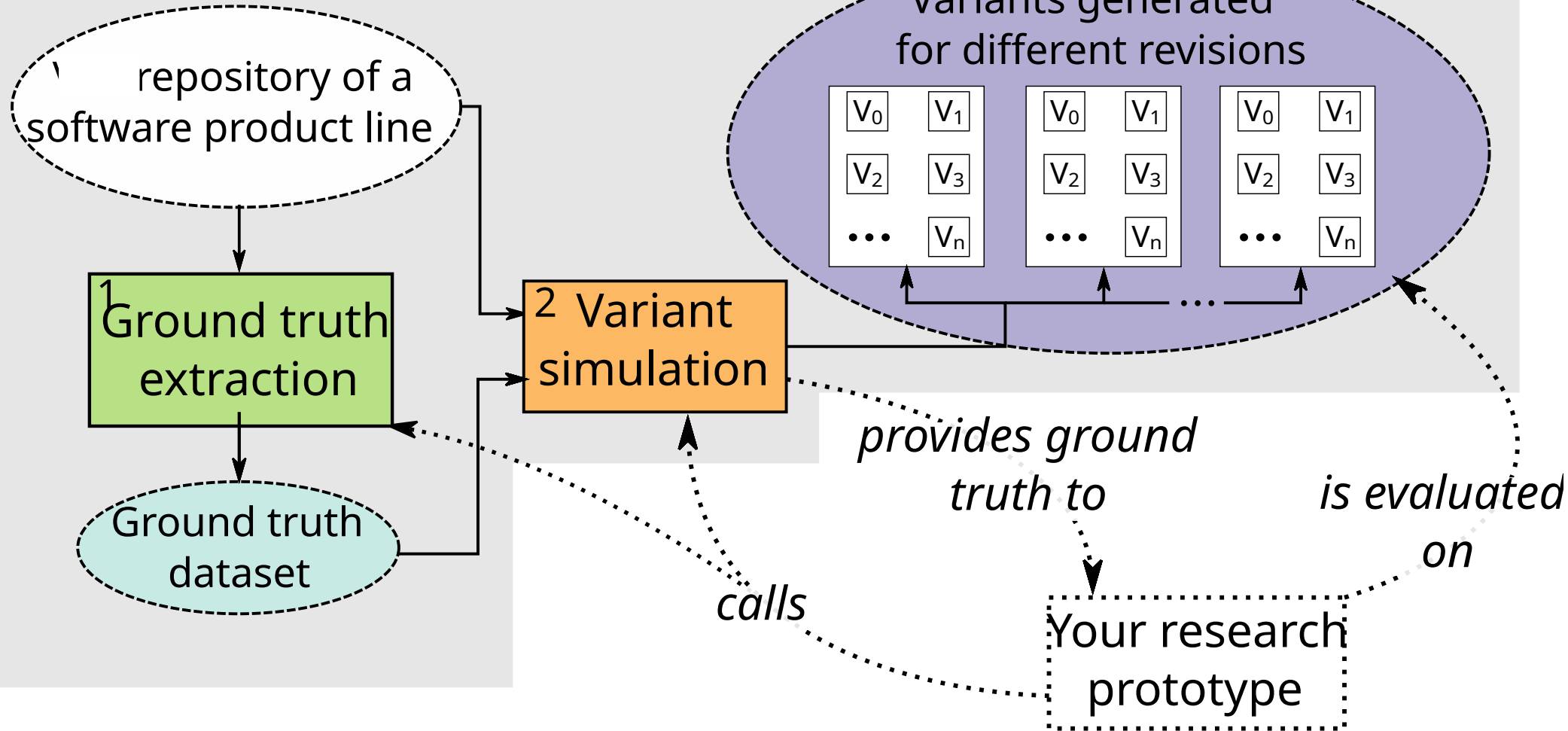
# VEVOS



# VEVOS



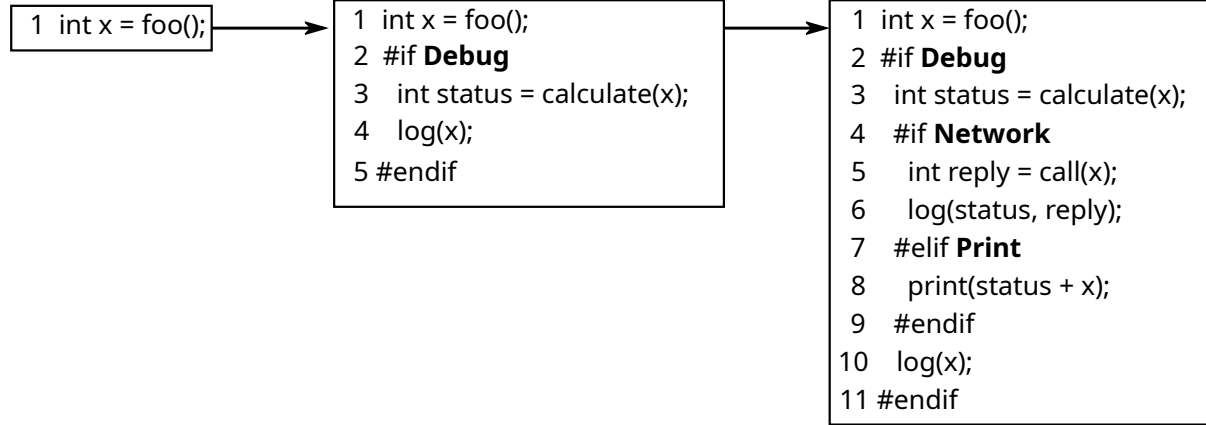
# VEVOS



# Ground truth extraction

# Extracting a ground truth

SPL  
history



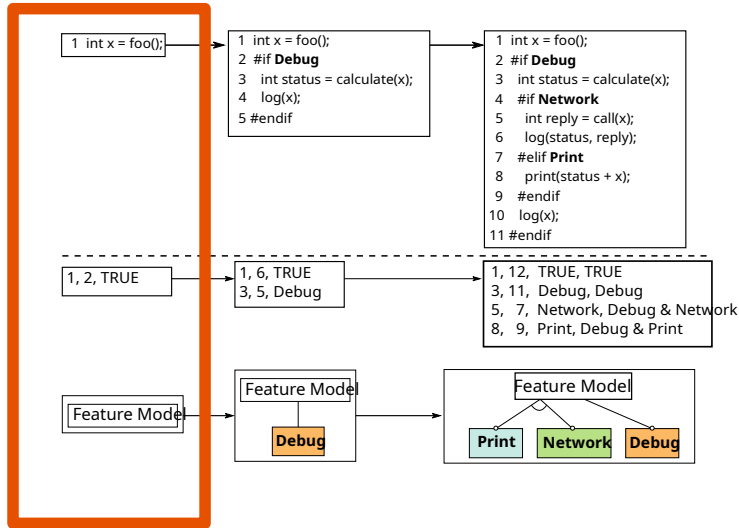


# Variant simulation

# Simulation of variants

# Simulation of variants

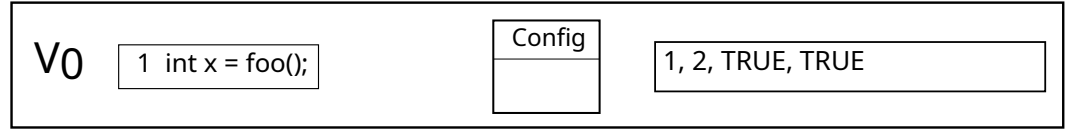
## Ground truth dataset



Source code

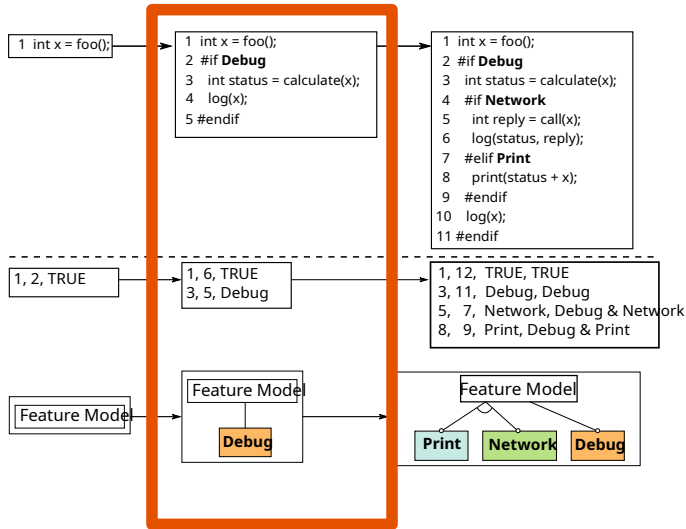
Configuration

Feature mapping  
and presence condition



# Simulation of variants

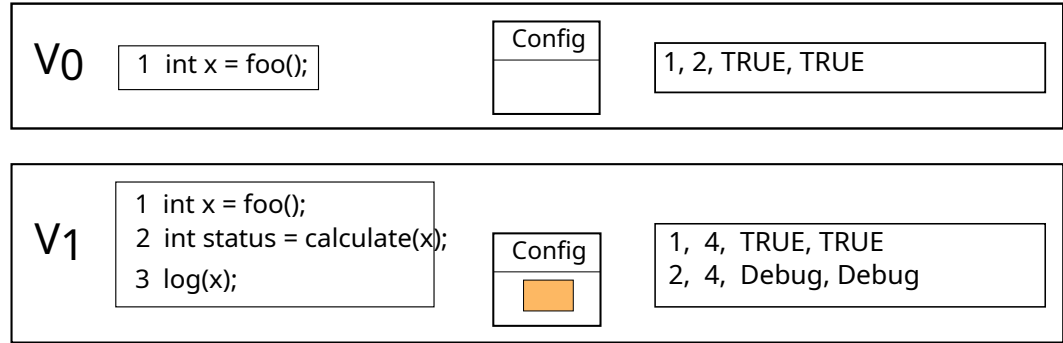
## Ground truth dataset



Source code

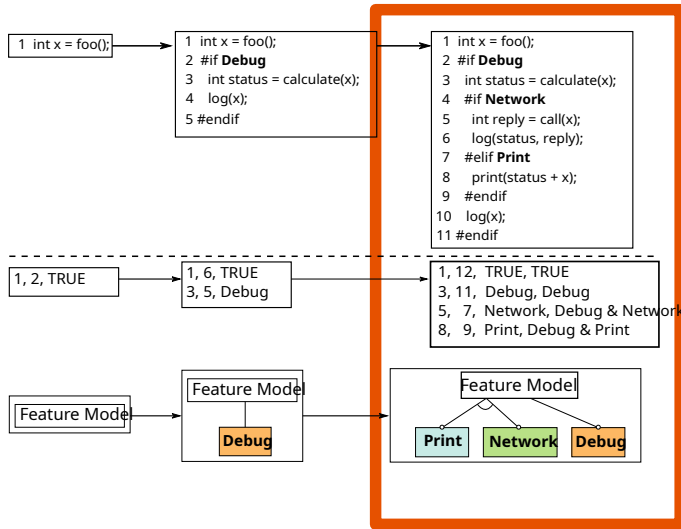
Configuration

Feature mapping  
and presence condition



# Simulation of variants

## Ground truth dataset

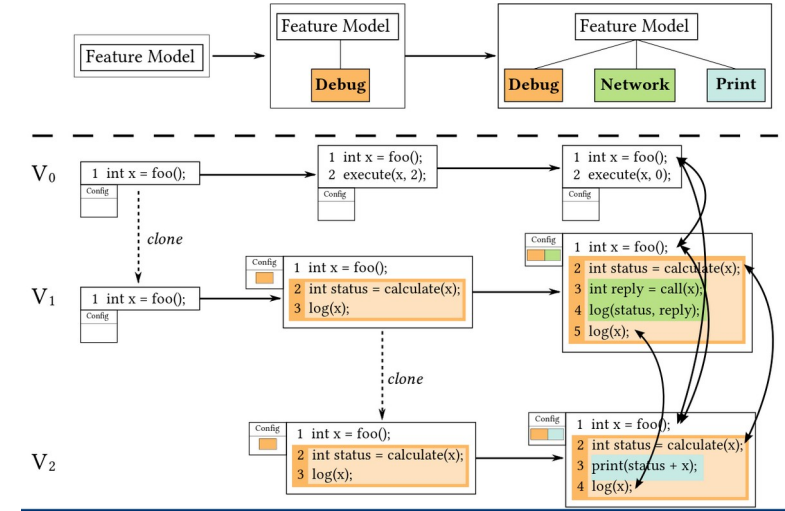
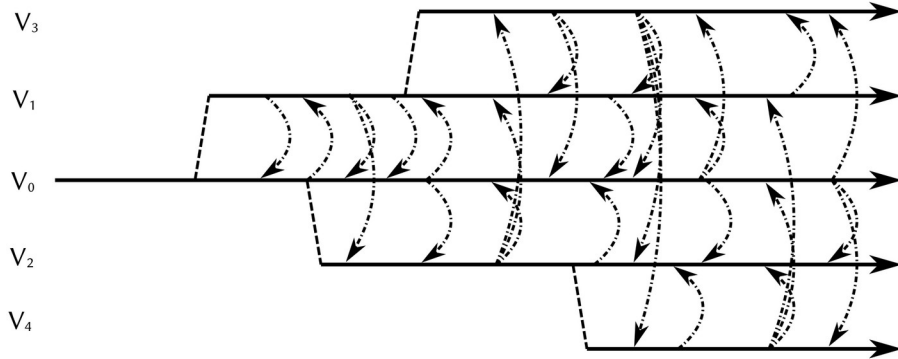


Source code

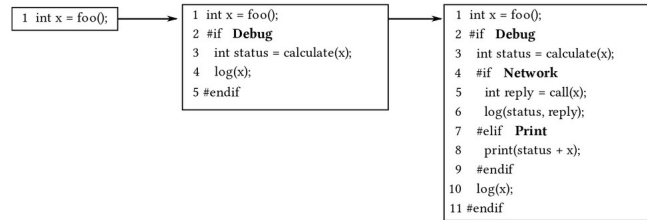
Configuration

Feature mapping  
and presence condition

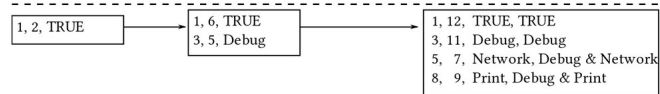
V0	1 int x = foo();	Config	1, 2, TRUE, TRUE
V1	1 int x = foo(); 2 int status = calculate(x); 3 log(x);	Config 	1, 4, TRUE, TRUE 2, 4, Debug, Debug
V2	1 int x = foo(); 2 int status = calculate(x); 3 int reply = call(x); 4 log(status, reply); 5 log(x);	Config 	1, 6, TRUE, TRUE 2, 6, Debug, Debug 3, 5, Network, Debug & Network
V3	1 int x = foo(); 2 int status = calculate(x); 3 print(status + x); 4 log(x);	Config 	1, 5, TRUE, TRUE 2, 5, Debug, Debug 3, 4, Print, Debug & Print



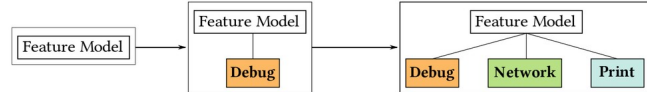
## SPL history



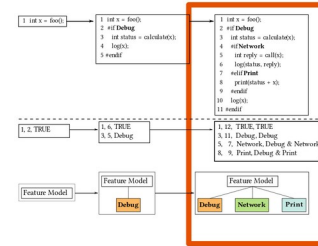
## Feature Mapping



## Feature Model



## Ground truth dataset



	Source code	Configuration	Feature mapping and presence condition
V <sub>0</sub>	1 int x = foo();	Config	1, 2, TRUE, TRUE
V <sub>1</sub>	1 int x = foo(); 2 int status = calculate(x); 3 log(x);	Config	1, 4, TRUE, TRUE 2, 4, Debug, Debug
V <sub>2</sub>	1 int x = foo(); 2 int status = calculate(x); 3 int reply = call(x); 4 log(status, reply); 5 log(x);	Config	1, 6, TRUE, TRUE 2, 6, Debug, Debug 3, 5, Network, Debug & Network
V <sub>3</sub>	1 int x = foo(); 2 int status = calculate(x); 3 print(status + x); 4 log(x);	Config	1, 5, TRUE, TRUE 2, 5, Debug, Debug 3, 4, Print, Debug & Print

# Appendix

# What are VEVOS' current extraction capabilities?

- Linux
  - Successful for ~140,000 of ~1,076,000 commits
  - V4.0 through v4.10
  - Development slice: 2015 - 2017
- BusyBox
  - Successful for ~5,600 of ~17,000 commits (most recent commits)
  - Development slice: 2010 - 2022



# What are VEVOS' current simulation capabilities?

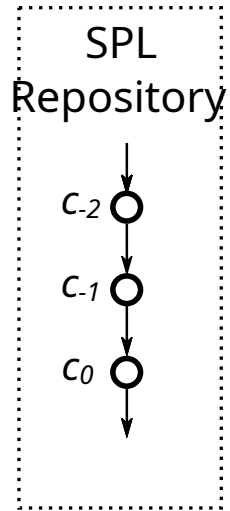
For each version of a software product line

- Random sampling of configurations
- Generation of feature-aware variants
  - Feature mapping
  - Configuration
  - Source code
  - Code matching

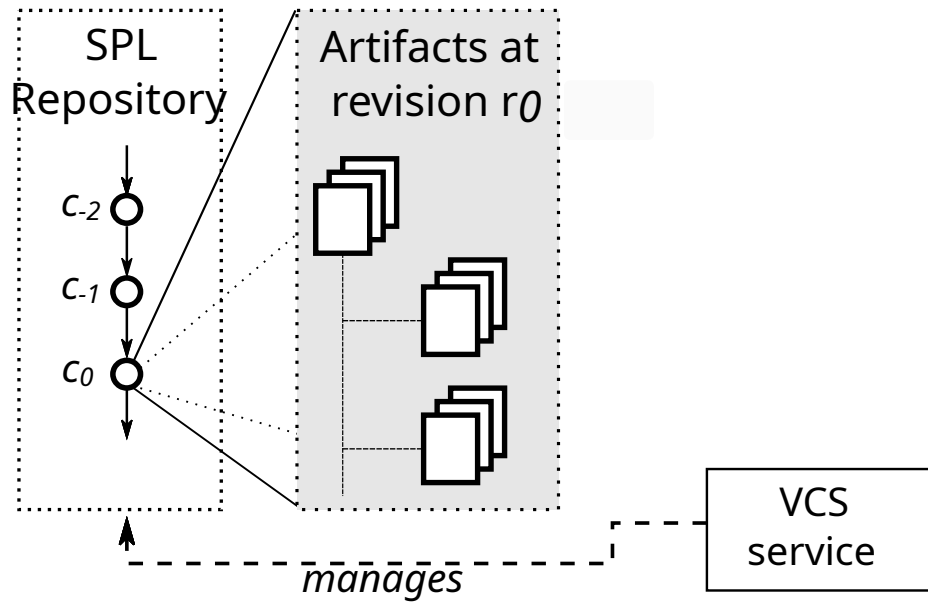
# Unresolved Challenges

- Build system analysis is not robust
- Generated variants expose no unintentional divergences
  - These can occur in clone-and-own (e.g., refactoring in one variant)
  - Leads to evaluation bias

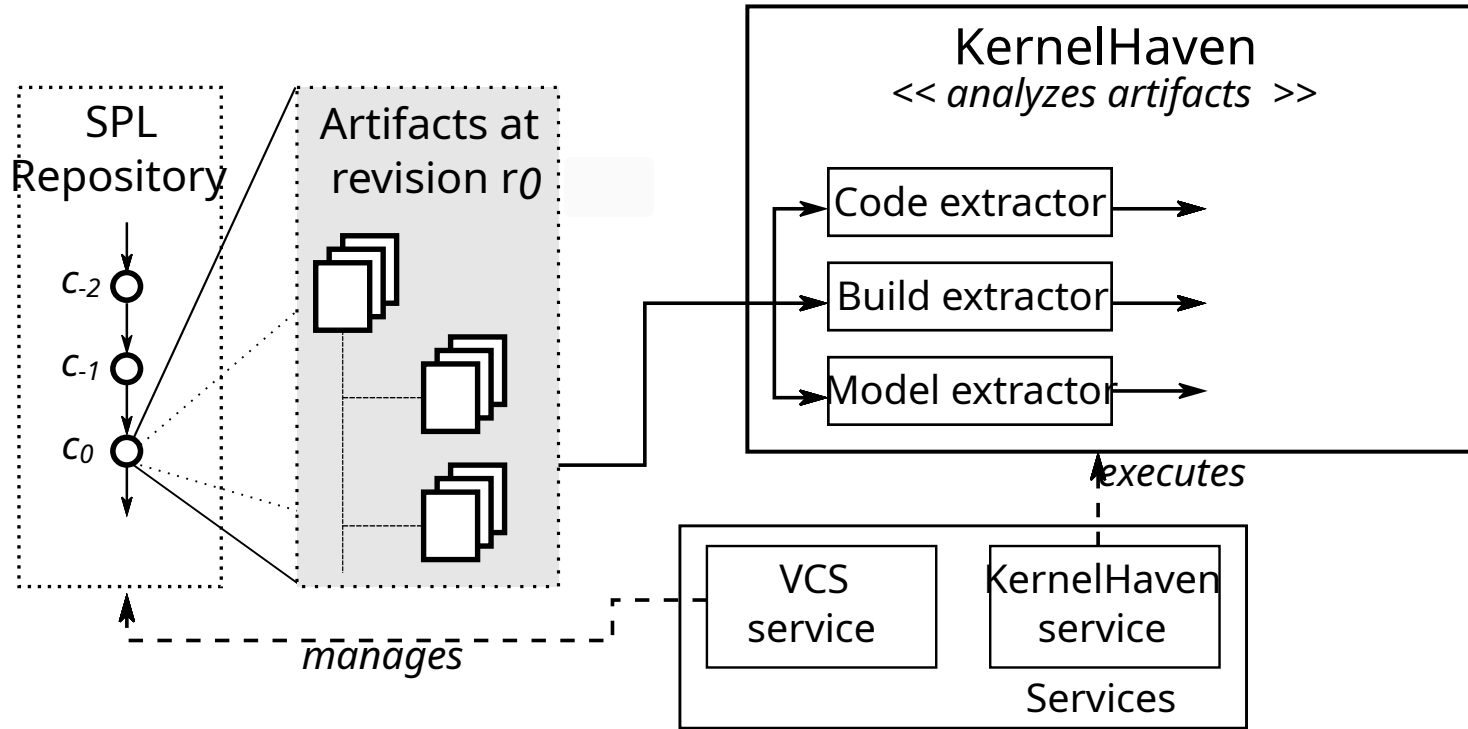
# The Ground Truth Extraction



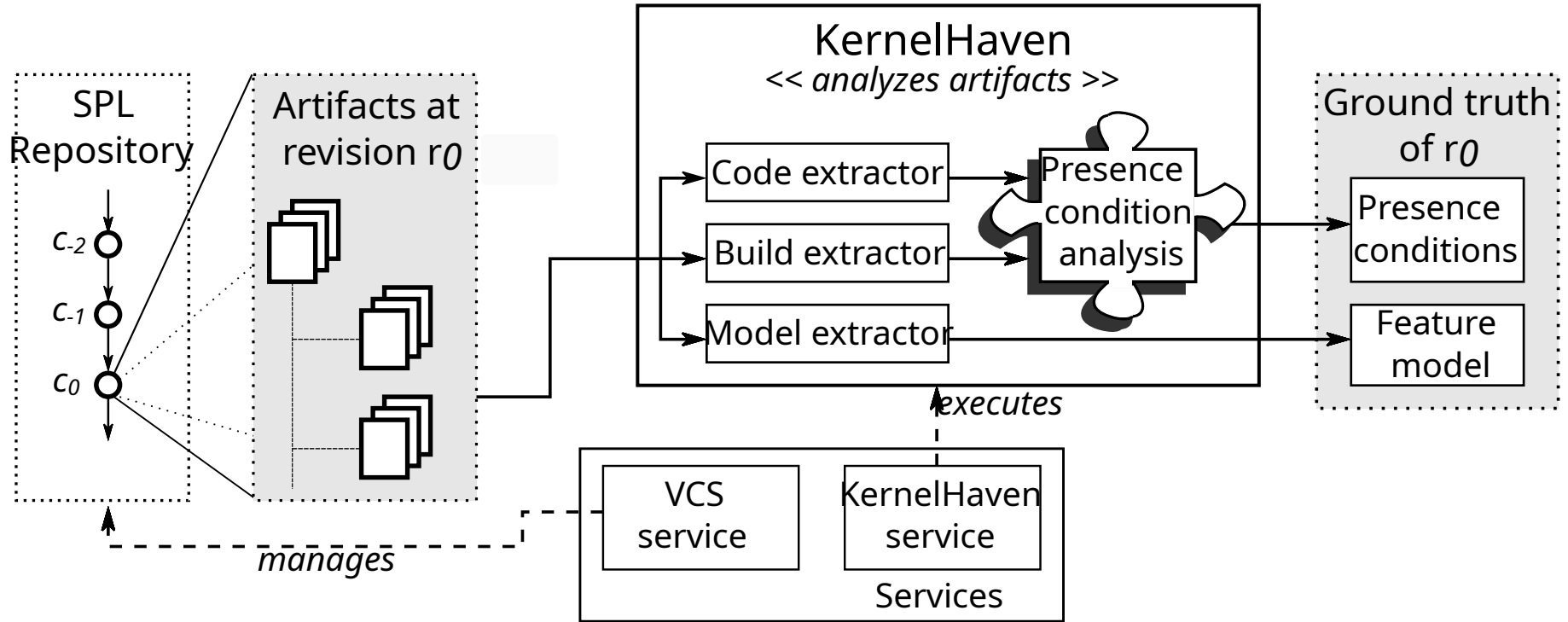
# The Ground Truth Extraction



# The Ground Truth Extraction



# The Ground Truth Extraction



# The Ground Truth Extraction

