Date	Task	Who
9/28	Met up on teams	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo
9/29-10/6	Worked on Verilog	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu
10/7	Meeting on teams	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu
10/9	Completed Multiplier	Evan McCauley
10/10	Completed Verilog code	Matthew, Even, Rafael
10/11	Completed system diagram	Rafael Raymos
10/11	Everyone was able to run the code	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu
10/11	Completed the rest of the turn in's	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu