Date	Task	Group members participation
10/22	Teams meeting	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu
10/24	Worked on the DFF, Gate logic code for all gates Xnor, Nand, NOT gate and all others.	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu
10/25	Teams meeting, Finite State Machine completed.	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu
10/28	Fixed and Worked on the DFF, Gate logic code for all gates Xnor, Nand, NOT gate and all others.	Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu
10/29	Modified the DFF, Gate logic code for all gates Xnor, Nand, NOT gate and all others.

System Diagram completed. | Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu |
| 10/30 | Made Accumulator and changed output. Also changed the code to better run for displaying the exact and correct output. | Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu |
| 10/31 | Log completed, SystemDesign completed, Output completed. | Matthew Standeven, Rafael Raymos, Eric Cutherell, Evan McCauley, Jonas Salcedo, Paul Lupeituu |