

Low Power Mid-crack Code for High Performance Image Transmission

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Abstract

Contour representation of binary object is increasingly used in image processing and pattern recognition. Chain code and crack code are efficient and widely used methods for representing rasterised binary objects and contours. However, by using these methods, an accurate estimate of geometric features like area and perimeter of objects are difficult to obtain. Mid-crack code, a third contour encoding method, has higher accuracy in estimating the geometric features of objects. Each element in the mid-crack code represents the relative angle difference between the mid-point of the edges of two adjacent pixels along the boundary of an object with a 3-bit code. Therefore, the frequencies of moves in different directions are not considered and binary bits that represent the codes are treated equally. A power efficient encoding of mid-crack code is possible by considering and applying a variable-length encoding technique that treats the binary bits differently considering their energy consumption requirements. In this paper, a low power mid-crack code is proposed based on a variation of the Huffman code. Experiments performed on different images yield that the proposed representation reduces the overall transmission cost of encoded objects considerably with compared to classical mid-crack code.

Keywords: Mid-crack code, Pattern Recognition, Huffman Code, Image Compression, Image Transmission

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1. Introduction

2. Background Study

2.1. Mid-Crack Code

2.2. Huffman Code

5 2.3. Source of Power Dissipation and its Effects on Information Encoding

In the recent years, application of battery-powered portable devices, e.g. laptop computers and mobile phones has increased rapidly. Power dissipation has become a primary concern for digital community because it affects the performance, reliability, and the cost of computation in both portable and non-portable devices. CMOS technologies were developed in order to reduce the power consumption in devices. Digital CMOS circuits have three major sources of power dissipation and are summarised in the following equation [1]:

$$\begin{aligned} P_{avg} &= P_{dynamic} + P_{short-circuit} + P_{leakage} \\ &= \alpha_{0 \rightarrow 1} \cdot C_L \cdot V_{dd}^2 \cdot f_{clk} + I_{sc} \cdot V_{dd} + I_{leakage} \cdot V_{dd} \end{aligned} \quad (1)$$

2.4. Unequal Bit Cost Encoding

3. Proposed Scheme

3.1. Power Efficient Huffman Code

3.2. Low Power Mid-crack Code

10 4. Results and Discussion

5. Conclusion

References

- [1] N. H. E. Weste, K. Eshraghian, Principles of CMOS VLSI design: a systems perspective, Addison-Wesley, 1988.