

Arithmetic

This section describes the available arithmetic opcodes/memonics and their corresponding operations.

All the arithmetic instructions accept only a single operand.

The other operand(also the destination) is taken from the Link register L0, L1, L2, L3

SEE {REFERENCE}

REFERENCE NEEDED [registers-Link_registers](#)

Addition

The addition instructions are

- ADDI (ADD Integer)
- ADDU (ADD Unsigned)
- ADDF (ADD Floating point values)

1. ADDI:

Opcode: 13

Operand Accepted: 64-bit signed integers

Adds the passed 64-bit signed int value to the value stored in the register L2(also considered to be an 64-bit signed integer value)

The result is stored in the L2 Register

!!! Example

```
ADDI 1 ;Will add 1 to whatever value is in register L2
```

Opcode	Code	Operand Count	Opernads	Description
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SUBI	
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MULI	
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DIVI	
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MODI	
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ADDU	
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SUBU	
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MULU	
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DIVU	
------	--

MODU	
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ADDF	
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SUBF	
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MULF	
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DIVF	
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