

Instruction	Description	Opcode(4)	Addressing Mode (3)	R1 (3)	R2 (3)	Unused (3)	Offset / Immediate value (16)
addi	Rdst <- Rdst + #immediate	000 0	0XX	XXX	Rdst	XXX	#immediate
addr	Rdst <- Rdst + Rsrc	000 0	1XX	Rsrc	Rdst	XXX	X
subi	Rdst <- Rdst - #immediate	0 001	0XX	XXX	Rdst	XXX	#immediate
subr	Rdst <- Rdst - Rsrc	0 001	1XX	Rsrc	Rdst	XXX	X
andi	Rdst <- Rdst & #immediate	0 010	0XX	XXX	Rdst	XXX	#immediate
andr	Rdst <- Rdst & Rsrc	0 010	1XX	Rsrc	Rdst	XXX	X
ori	Rdst <- Rdst   #immediate	0 011	0XX	XXX	Rdst	XXX	#immediate
orr	Rdst <- Rdst   Rsrc	0 011	1XX	Rsrc	Rdst	XXX	X
mnsi	z <- Rdst - #immediate	0 100	0XX	XXX	Rdst	XXX	#immediate
mnsr	z <- Rdst - Rsrc	0 100	1XX	Rsrc	Rdst	XXX	X
cmp	Rdst <- !Rdst	0 101	XXX (000)	XXX	Rdst	XXX	X
li Rdst, #imme	Rdst <- #imme	100X	00X	XXX	Rdst	XXX	#immediate
lr Rdst, Rsrc	Rdst <- Rsrc	100X	01X	Rsrc	Rdst	XXX	X
la Rdst, d(rb)	Rdst <- M[rb+ #d]	100X	10X	rb	Rdst	XXX	#d
sta d(rb), Rsrc	M[rb + #d] <- Rsrc	101X	XXX (10X)	rb	Rsrc	XXX	#d
j <i>addr</i>	j <i>addr</i>	1100	XXX	XXX	XXX	XXX	<i>addr</i>
jz <i>addr</i>	jz <i>addr</i>	1101	0	XXX	XXX	XXX	<i>addr</i>
jnz <i>addr</i>	jnz <i>addr</i>	1101	1	XXX	XXX	XXX	<i>addr</i>
jc <i>addr</i>	jc <i>addr</i>	1101	2	XXX	XXX	XXX	<i>addr</i>
jnc <i>addr</i>	jnc <i>addr</i>	1101	3	XXX	XXX	XXX	<i>addr</i>
jv <i>addr</i>	jv <i>addr</i>	1101	4	XXX	XXX	XXX	<i>addr</i>
jnv <i>addr</i>	jnv <i>addr</i>	1101	5	XXX	XXX	XXX	<i>addr</i>
jm <i>addr</i>	jm <i>addr</i>	1101	6	XXX	XXX	XXX	<i>addr</i>
jnm <i>addr</i>	jnm <i>addr</i>	1101	7	XXX	XXX	XXX	<i>addr</i>
jal Rlink, <i>addr</i>	Rlink <- PC + 1; PC <- PC + M[PC]	1110	XXX	XXX	Rlink	XXX	<i>addr</i>
jr Rlink	PC <- Rlink	1111	XXX	XXX	Rlink	XXX	X