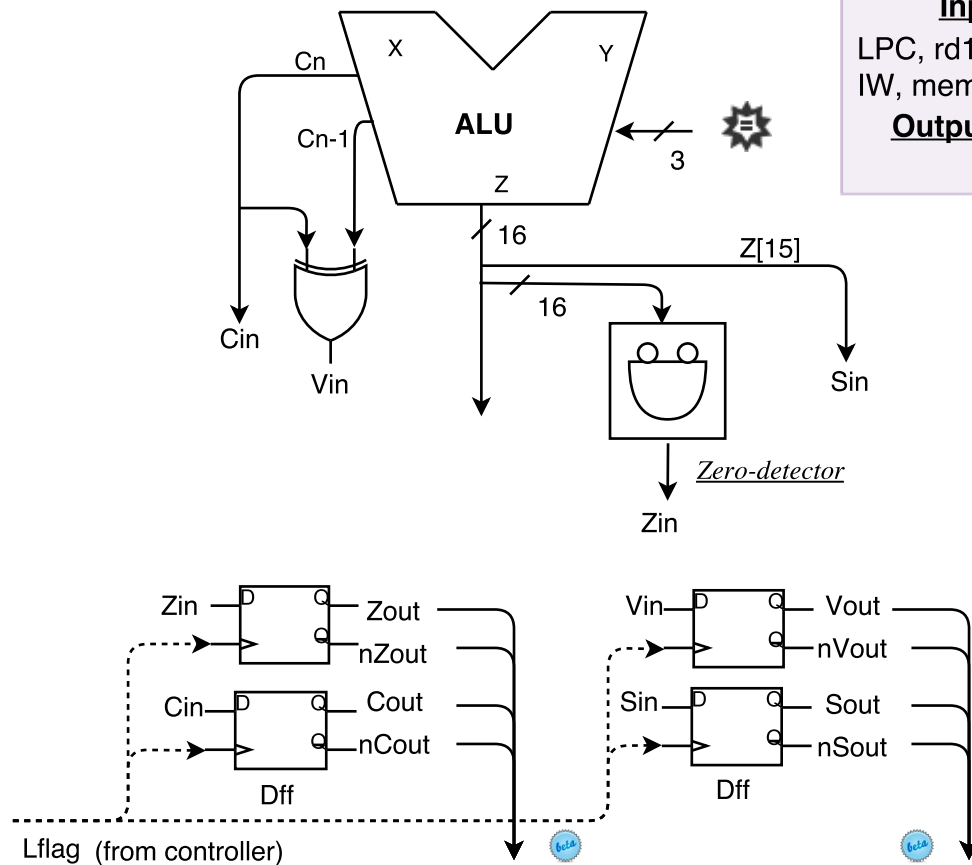


Condition Detection, Condition Flags and D-condition



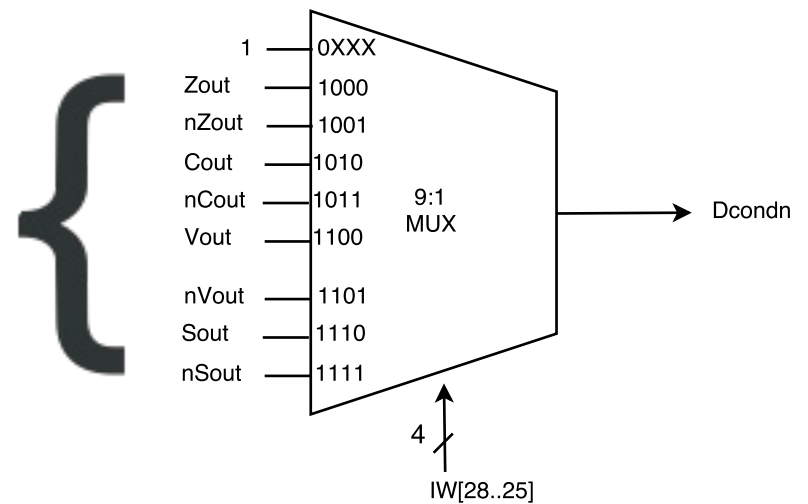
Input to Datapath

LPC, rd1, rd2, wr_contr, selM1, selM2, fnsel[1:0], selM3, isJumpInstr, isCallInstr, Lflag_contr; IW, memOut

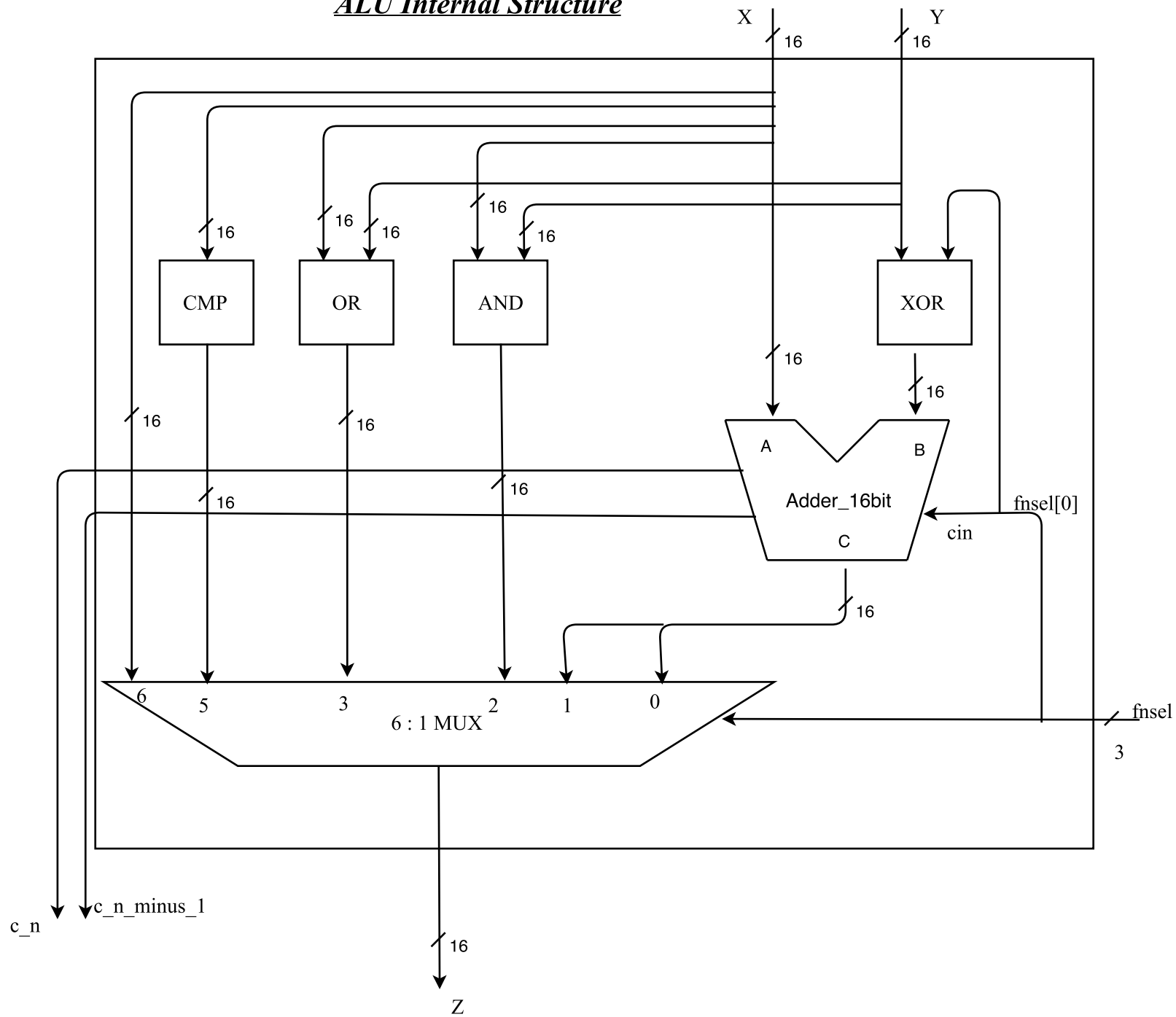
Output from Datapath

IW[31...25]; pcOut, Z, p2

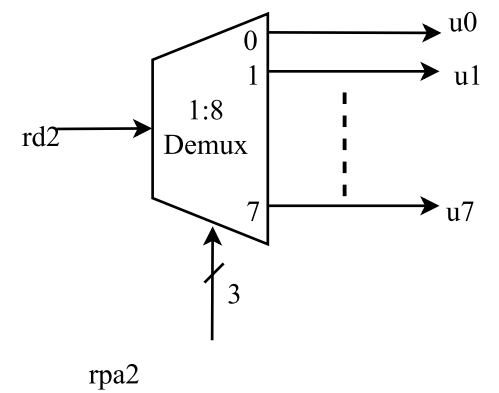
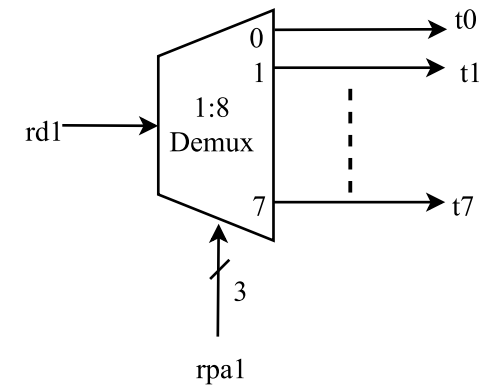
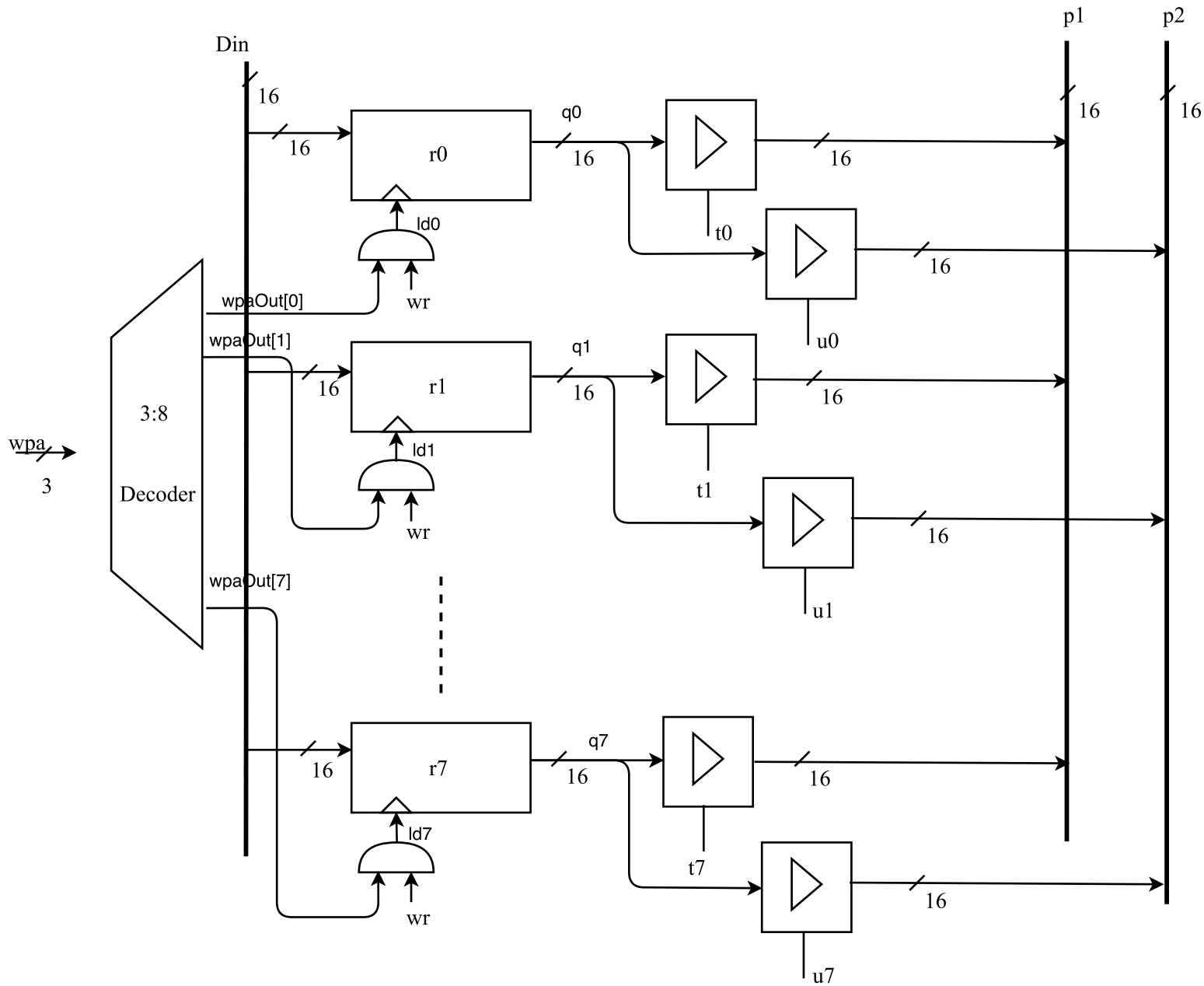
beta



ALU Internal Structure



Register Bank Internal Structure



[Rough] Register Bank Internal Structure

