addi Rdst <- Rdst + #immediate	Instruction	Description	Opcode(4)	Addressing Mode (3)	R1 (3)	R2 (3)	Unused (3)	Offset / Immediate value (16)
subi Rdst <- Rdst - #immediate 0 001 0XX XXX Rdst XXX #immediate subr Rdst <- Rdst - Rsrc	addi	Rdst <- Rdst + #immediate	0000	OXX			XXX	
subr Rdst <- Rdst & Himmediate 0 001 1XX Rsrc Rdst XXX X andi Rdst <- Rdst & Himmediate	addr	Rdst <- Rdst + Rsrc	000 0	1XX	Rsrc	Rdst	XXX	X
andi Rdst <- Rdst & #immediate 0 010 0XX XXX Rdst XXX #immediate andr Rdst <- Rdst & Rsrc	subi	Rdst <- Rdst - #immediate	0 001	0XX	XXX	Rdst	XXX	#immediate
andr Rdst <- Rdst & Rsrc 0 010 1XX Rsrc Rdst XXX XXX ori Rdst <- Rdst #immediate	subr	Rdst <- Rdst - Rsrc	0 001	1XX	Rsrc	Rdst	XXX	Χ
ori Rdst <- Rdst #immediate 0 011 0XX XXX Rdst XXX #immediate orr Rdst <- Rdst Rsrc	andi	Rdst <- Rdst & #immediate	0 010	0XX	XXX	Rdst	XXX	#immediate
orr Rdst <- Rdst Rsrc 0 011 1 XX Rsrc Rdst XXX X mnsi z <- Rdst - #immediate	andr	Rdst <- Rdst & Rsrc	0 010	1XX	Rsrc	Rdst	XXX	Χ
mnsi z <- Rdst - #immediate 0 100 0XX XXX Rdst XXX #immediate mnsr z <- Rdst - Rsrc	ori	Rdst <- Rdst #immediate	0 011	0XX	XXX	Rdst	XXX	#immediate
mnsr z <- Rdst - Rsrc 0 100 1XX Rsrc Rdst XXX X cmp Rdst <- !Rdst	orr	Rdst <- Rdst Rsrc	0 011	1XX	Rsrc	Rdst	XXX	Χ
cmp Rdst <- !Rdst 0 101 XXX (000) XXX Rdst XXX X li Rdst, #imme Rdst <- #imme	mnsi	z <- Rdst - #immediate	0 100	0XX	XXX	Rdst	XXX	#immediate
I Rdst, #imme	mnsr	z <- Rdst - Rsrc	0 100	1XX	Rsrc	Rdst	XXX	Χ
Ir Rdst, Rsrc Rdst <- Rsrc 100X 01X Rsrc Rdst XXX X la Rdst, d(rb) Rdst <- M[rb + #d]	cmp	Rdst <- !Rdst	0 101	XXX (000)	XXX	Rdst	XXX	Χ
la Rdst, d(rb) Rdst <- M[rb+#d] 100X 10X rb Rdst XXX #d sta d(rb), Rsrc M[rb+#d] <- Rsrc	li Rdst, #imme	Rdst <- #imme	100X	00X	XXX	Rdst	XXX	#immediate
sta d(rb), Rsrc M[rb + #d] <- Rsrc 101X XXX (10X) rb Rsrc XXX #d j addr j addr 1100 XXX XXX XXX XXX addr jz addr jz addr 1101 0 XXX XXX XXX addr jnz addr jnz addr 1101 1 XXX XXX XXX addr jnc addr jnc addr 1101 2 XXX XXX XXX addr jv addr jv addr 1101 3 XXX XXX XXX addr jnv addr jnv addr 1101 4 XXX XXX XXX addr jm addr jm addr 1101 5 XXX XXX XXX addr jm addr jm addr 1101 6 XXX XXX XXX addr jal Rlink, addr Rlink <- PC + 1; PC<-PC+M[PC]	lr Rdst, Rsrc	Rdst <- Rsrc	100X	01X	Rsrc	Rdst	XXX	X
j addr j addr 1100 XXXX XXXX XXXX XXXX AXXX	la Rdst, d(rb)	Rdst <- M[rb+ #d]	100X	10X	rb	Rdst	XXX	#d
jz addr jz addr 1101 0 XXX XXX XXX addr jnz addr jnz addr 1101 1 XXX XXX XXX addr jc addr jc addr 1101 2 XXX XXX XXX addr jnc addr jnc addr 1101 3 XXX XXX XXX addr jnv addr jnv addr 1101 4 XXX XXX XXX addr jm addr jm addr 1101 6 XXX XXX XXX addr jal Rlink, addr Rlink <- PC + 1; PC<-PC+M[PC]	sta d(rb), Rsrc	M[rb + #d] < -Rsrc	101X	XXX (10X)	rb	Rsrc	XXX	#d
jnz addr jnz addr 1101 1 XXX XXX XXX addr jc addr jc addr 1101 2 XXX XXX XXX addr jnc addr jnc addr 1101 3 XXX XXX XXX addr jv addr jv addr 1101 4 XXX XXX XXX addr jnv addr jnv addr 1101 5 XXX XXX XXX addr jm addr jm addr 1101 6 XXX XXX XXX addr jal Rlink, addr Rlink <- PC + 1; PC<-PC+M[PC]	j addr	j addr	1100	XXX	XXX	XXX	XXX	addr
jc addr jc addr 1101 2 XXX XXX XXX XXX addr jnc addr jnc addr 1101 3 XXX XXX XXX XXX addr jv addr jv addr 1101 4 XXX XXX XXX addr jnv addr jnv addr 1101 5 XXX XXX XXX addr jnm addr jnm addr 1101 7 XXX XXX XXX addr jal Rlink, addr Rlink <- PC + 1; PC<-PC+M[PC]	jz addr	jz addr	1101	0	XXX	XXX	XXX	addr
jnc addr jnc addr 1101 3 XXX XXX XXX XXX addr jv addr jv addr 1101 4 XXX XXX XXX addr jnv addr jnv addr 1101 5 XXX XXX XXX addr jnm addr jnm addr 1101 6 XXX XXX XXX addr jal Rlink, addr Rlink <- PC + 1; PC<-PC+M[PC]	jnz <i>addr</i>	jnz <i>addr</i>	1101	1	XXX	XXX	XXX	addr
jv addr jv addr 1101 4 XXX XXX XXX XXX addr jnv addr jnv addr 1101 5 XXX XXX XXX addr jm addr jm addr 1101 6 XXX XXX XXX addr jnm addr jnm addr 1101 7 XXX XXX XXX addr jal Rlink, addr Rlink <- PC + 1; PC<-PC+M[PC]	jc addr	jc <i>addr</i>	1101	2	XXX	XXX	XXX	addr
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	jnc <i>addr</i>	jnc <i>addr</i>	1101	3	XXX	XXX	XXX	addr
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	jv <i>addr</i>	jv addr	1101	4	XXX	XXX	XXX	addr
jnm $addr$ jnm $addr$ 1101 7 XXX XXX XXX $addr$ jal Rlink, $addr$ Rlink <- PC + 1; PC<-PC+M[PC] 1110 XXX XXX Rlink XXX $addr$	jnv <i>addr</i>	jnv <i>addr</i>	1101	5	XXX	XXX	XXX	addr
jal Rlink, addr Rlink <- PC + 1; PC<-PC+M[PC] 1110 XXX XXX Rlink XXX addr	jm <i>addr</i>	jm <i>addr</i>	1101	6	XXX	XXX	XXX	addr
	jnm <i>addr</i>	jnm <i>addr</i>	1101	7	XXX	XXX	XXX	addr
jr Rlink PC <- Rlink 1111 XXX XXX Rlink XXX X	jal Rlink <i>, addr</i>	Rlink <- PC + 1; PC<-PC+M[PC]	1110	XXX	XXX	Rlink	XXX	addr
	jr Rlink	PC <- Rlink	1111	XXX	XXX	Rlink	XXX	Χ