

(1) (2) Content of workshop Programming/Software development Circuit Theory/ Electronics Math Practical Demonstrations Few proofs/theory My approach: Fondational Knowledge > simple = Complex > general example = example solution Goal:

>[HW] > [SW] >

Analog

Electricity

Code Many different ways in research & industry workshop is one way 1) Blackbox: inner aspects of HW unknown How does input ->output (Acoustica) 2) Whitebox: Components are modeled "Physically Informed" (UA) 3) Graybox: The Art of VA File Design

Andro Bignals.
Analog Approaches for component modeling (3) Digital 1) Not in workshop: Wave Digital Filter
Different Paradigm
Kurt Werner, DAFX 171111 Continuous A(t) Acoustic Sound Pressure Discrete 2) Traditional Circuit Analysis Techniques X[n] n= sample number Nodal Analysis -> Modified Nodal Analysis
State-Space -> Discrete Kirchhoff (DK)
Presonus "branded" Electricity. 0,1,2,3,... Signal Gain Change in sout in sout Type of effect (system) determine in you Matlab = loop

XENI -> 8 -> YENI Matlab = loop Modeling technique. EQ, Reverb Black-box Sine sweep Sine wave synthesis. Linear, Time-Invariant (LTI) DC Sweep [-1:.001:1] Non-linear: Distortion VoHerra-Series Characteristic Curve Hammer Stein - Weiner Mode Schenatic > Math > Prototype > Plug-in]

Mathab

Python

SucE/C++ Know the end goal, Remaintroduce electronics Start with Digital as reference.

Circuit: a system of interconnected components
Circuit: a system of interconnected components that carry electricity.
that carry electricity. Electricity: electrons that move La negative charge, jump from one atom to next.
La negative charge, jump trom one atom
Current: Flow of electrons. to next.
Lourits amps/ampere (A)
Voltage: force that causes current to flow. unit Volt(V)
1 / Clare sices
Analogy. water flowing, pipes pump pushes water.
pump pushes water.
Relationship between voltage & current.
Circuit: we need a splace to start.
Circuit: we need a splace to start. What is the impat.
Sources: voltage & current
Battery - Terminals.
Not a complete circuit on its own.
Not a complete circuit on its own. No current flowing, supplying voltage.
Symbol V 5

Use wire to connect paths for current to flow. V (2) li open circuit Voltage is identical when measured across wires. Short circuit: Current is the same at points in wire not separated by component. Other types of Voltage Sources microphone, electric quiter. AC US. DC. Kettery DC. direct current (no frequency) AC- alternating current (has frequency Hz) Other times in schematic. Current Sources

0.5A D There has to be exactly this current at this point in circuit.

Useful circuits need other components. B. What is voltage across individual resisted Current is same all the way around.

Vi=i.Ri i=4 Resistors: restrict current flow. units Ohm's Sl (onga). Symbol _____ Vz=i.Rz First full circuit. V1=4.1=4 volts. V2=4.2=8 volts. Relationship between Volts, Amps, Ohms. Input 12 volts divided between R, #Rz "Voltage divider" circuit. Ohm's Law V= I·B v=i·R Transfer Function Form. Examples: 10 V = = R=592 What is current? Vin (2) Re Vout Interested in writing

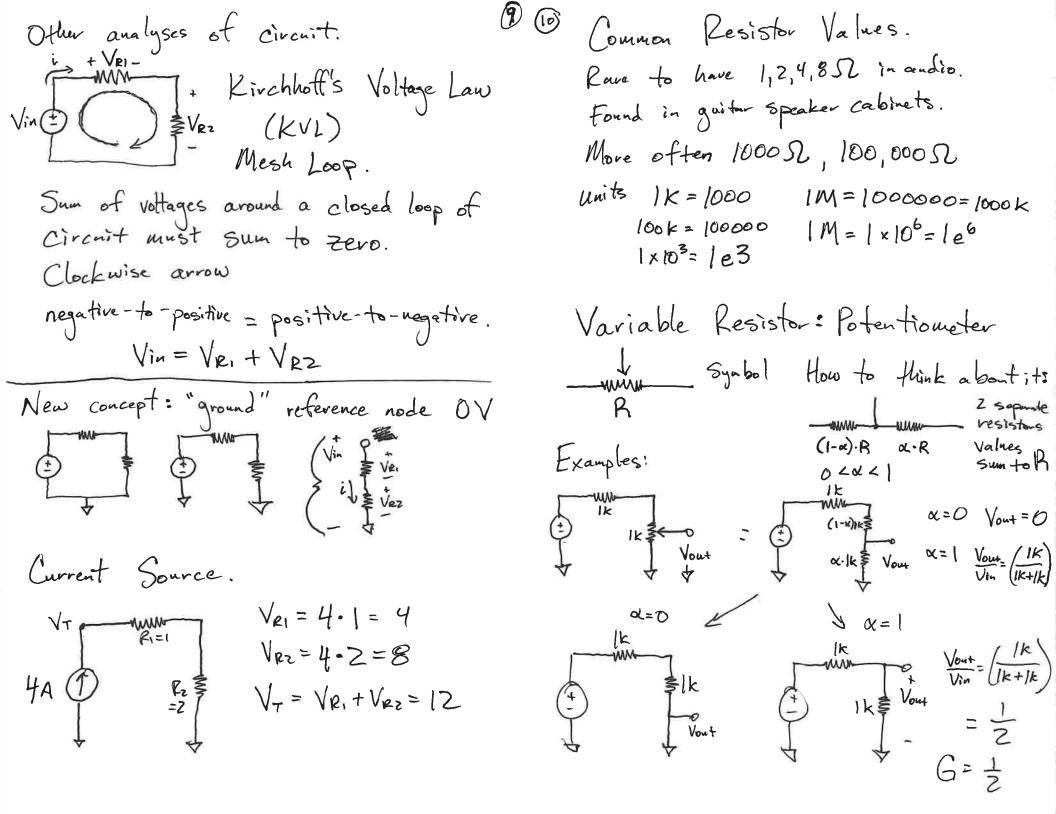
Vout as a function of 10=i.R 10=i.5 i=10=2 i=1 Note: this amount of current is at all points in this circuit. i= Vin.

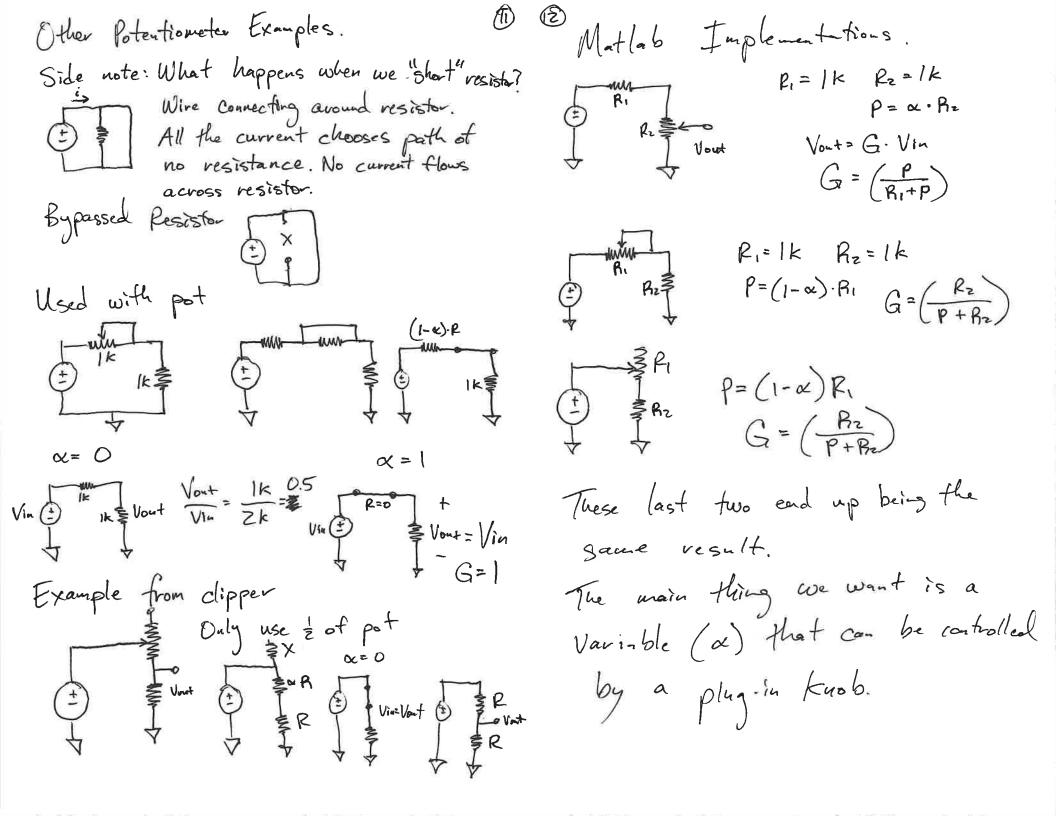
Ri+Rz Start by finding current through
all elements. Voltage can be measured across source or resister same value. Vout=iRz Plng in current to find voltage across Rzonly. Combined Resistance (sevies) R=R1+R2 equivalent resistance

R=150

R=250

What is current? Vont = Vin Rz Vont = Rz = G $y [n] = G \cdot x [n]$ $R_1 = R_2 | R_1 > R_2 | R_1 < R_2$ G = 0.5 | 0 < G < 0.5 | 0.5 < G < 1i= 12 = 12 = 4 A





Next example

is the state of t

Things we know: KVL Vin= &Vi+Vz Vz=Vout Vin=Vi+Vou+ blum's Law Vi=i,·Ri What about current on other es?

New Rule: Kirchhoff's Current Law (KCL)

* Water in pipes.

Current flow into node = Current flowing out of node. $L_1 = L_2 + L_3$

Go back to Ohm's Law

Vz=iz·Rz Vout=iz·Rz iz= Vz = Vont
Rz = Vont
Rz

We know several relationships between elements of circuit. Let's combine them together to reach our goal. This is what circuit analysis is all about.

Many different ways to do this.

Vin= V, + Von+ Vin = i, P, + Vont

Vin = (iz+iz)·R, + Vou+

Vin = (Vont + Vout) - Rit Vont

Vin = R1 Vont + R1 Vont + Vont

Vin = (Ri+Ri+1). Vont

Option 1: Vin-G = Von +

$$G = \frac{1}{\left(\frac{R_1}{R_2} + \frac{R_1}{R_3} + 1\right)}$$

Option 2 Expand. Want Rz. Rz in denominator

Nodal Analysis: different way to define relationships in circuit. We will use thing moving forward. Node a is b Pick nodes. What's a node?

Vin (1) Pick nodes. What's a node?

Vin (1) Pick nodes. What's a node?

Vin (1) Pick nodes. What's a node? Va = Vin Vb = Vont Currents into node = currents out of node $i_1 = i_2$ Normally we'd write i, = VRI also use Vin = VRI + Vont Notice Upi = Vin - Vont We will now write voltages as the dop from node "a" to node "b" $\frac{C_1 = Va - Vb}{R_1} = \frac{Vin - Vout}{R_1}$ $\frac{C_2 = Vb - O}{R_2} = \frac{Vb}{R_2} = \frac{Vout}{R_1}$ When the to ground $\frac{R_1}{R_2} = \frac{Vb}{R_2} = \frac{Vout}{R_1}$ Node B: KCL i=iz Vin-Vout = Vout Vin = Vout (1 + 1) ...

R1 = Vout (1 + 1) ...

Note: What happens if we label current directions differently? Viu (1)

Res Vout

Res Vout $\hat{c}_i = \frac{V_b - V_{ex}}{R_i} = \frac{V_{ont} - V_{in}}{R_i}$ 0 = Vout - Vin + Vout
R1 - R2 Vin = Von+ (Pi + 1) = Von+ (Pi Rz + Ri) Pi Rz Vin Rike = Vont Vin / Rz / = Vout G = RZ Same result. Take-away: with resistors main thing

start-finish Pay attention KCL

resistor Side of equation.

Redo example with parallel resistans (7) using modal analysis. Use Node (b)

Ref Voit Write KCL

O = i, + iz + iz

O = Vout - Vin + Vout + Vout

Reg Vout

Reg Vout

O = Vout - Vin + Vout

Reg Vout

R Vin = Vout + Vout + Vout P3 Vin = Vont (PI+ PE+ PB) Vin - Vout / RZR3 + RIR3 + RIR2 RIRZR3 PIRZR3 Vin = Vant (RzRz+ RiRz+ RiRz)
RiRzRz Vin Rz Rz

Ri Rz + Ri Rz + Rz Rz) = Von + Up to this point, we have seen passive circuits Resistors degreese amp, never increase 06G < I

(B) Active circuits, amplifiers 6>7 New Component: operational amplifier op-amp. Symbol

Active Device"

Need to supply

terminals output terminal power to it. Inverting imput Non-inventing input There is a circuit inside the op-amp non-trivial, we will ignore it Instead, we will tocas on conceptual behavior how if performs input - output. "Black box" Also assume ideal behavie bokay usually. We can make our circuit models even more realistic, if we model circuit inside op-amp. laper reference:

Using resistors with op-amp.

Inverting amplifier. Ideal Op-amp behavior. Take the difference between +/- termials and amplify it. Re Label Vx node

Note: Vx = 0

Positive terminal

The state of the positive terminal

The state of the terminal of the state of the sta In this configuration, amplifes more than is practically useful. No current flows into op-amp, all current must Almost never see this "open-loop op-amp. flow throng h R, -> Rz Instead: Feedback. Label i,, iz i,= iz Nodal Analysis. If there is any difference between +/- it will be fed back to input and removed.

instantaneously. $i_1 = \frac{V_{in} - V_X}{P_1}$ $i_2 = \frac{V_X - V_{out}}{P_2}$ Vin-O = O-Vout

Pr Vin = -Vout

Pr Pz Therefore, assumption #7 Vin - $\frac{Rz}{R_1} = -Vont$ or $Vout = -\frac{Rz}{R_1}$. Vin $G = \frac{-Rz}{R_1}$ if $R_2 > R_1$ increase in amplit positive and negative ferminals have exact Same voltage. Label as single node. Example

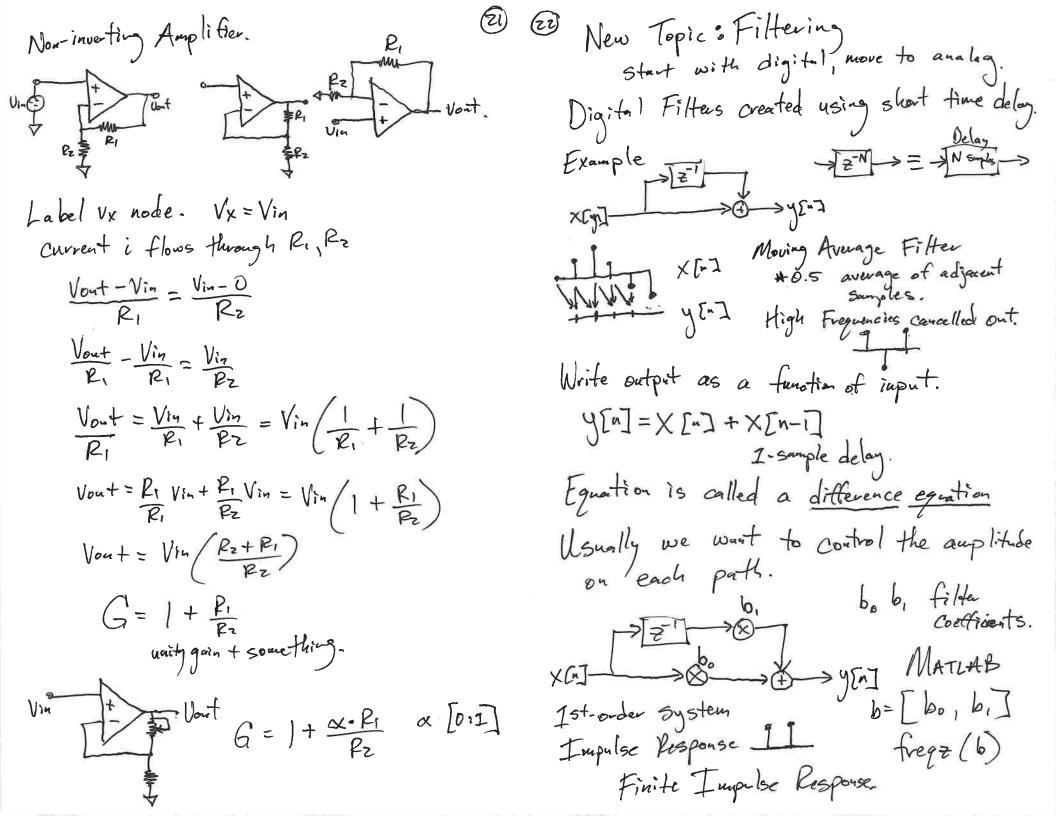
Vin Vont Va= Vin

Vont = Va

Vont = Va

Vont = Vin

What is the use of that? if Rz > R, increase in amplitude Rz = R, unity gain 101 Rz < Ri decrease in amplitude. Vin Vin Vin Vant Assumption #2 internal resistance between the is infinite (open circuit) no current flows into op-aug. Can be used to butter sections of circuit.



Digital Filters with feedback. $\times [n] \longrightarrow \bigotimes_{\alpha_{1}} \xrightarrow{Q_{0}} \underbrace{}_{\alpha_{1}} \xrightarrow{Q_{0}} \underbrace$ Infinite Impulse Response (11B) La Analog Filters. MATLAB Loop Example y, - state variable. Combining FF & FB General Form XM SET & YEAR YEAR Y[n]= b. x[n] + b, x[n-1] + a, g[n-1] Transfer Function Form: Y OUT = [Effect]

used for going analog > digital

We need a way to separate delay from X, Y

Z-transform (topic outside of workshop, notation) Y[n] = b. ×[n] + b. ×[n-1] + a. y[n-1] $\times [n-1] \rightarrow \times [\pm 1] \quad \text{y[n]} \rightarrow \times [\pm 1] \\ \times [n-1] \rightarrow \times [\pm 1] \cdot \pm 1 \quad \text{y[n-1]} = \times [\pm 1] = 1$ Y[=]= b, X[=]+b, X[=]="+a, Y[=]=" Move YEZ] to LHS

YET-a, YET=z'=b, XET+b, XET=z'Factor out Y, X

YET(1-a, z')=xET=(b0+b1z') $\frac{Y[\overline{z}]}{X[\overline{z}]} = \frac{(b_0 + b_1 \overline{z}')}{(1 - a_1 \overline{z}')} \qquad b = [b_0, b_1] \text{ freqz}$ $\alpha = [1, -a_1] \text{ freqz}$ Example: convert higher-order TF to difference to. EXTET = bo + b, Z + bz Z Second order biquad. Y[z](1+a,z'+az=z)=X[z](b+b,z'+bz=z) y[n] + a, y[n-1] + azy[n-z] = bo x[n] + b, x[n-1] + bzx[x] y[n]= b, x[n]+b, x[n-1]+b, x[n-2]-a,y[n-1]-azy[n-2] Side note: 2nd-order bignond filter can be used to make LPF, HPF, Shelf, BPF, notch Andio EQ Cookbook

f, amp, Q -> b. b, bz a, az

Analog Filters. Symbols (25) (26) Ohm's Law for resistance V=I.R New component: Capacitor II I whele polarity.

Two metal plates close together labeled polarity.

Think of static electricity.

Charge builds up on plate, eventually jumps (flows) Resistance is simple type of complex impedance general Written V= I.Z where Z= impedance for resistor Z=R for capacitor slightly different relationship. This component also impedes the flow of electricity Equation involves calculus. We don't actually Frequency Dependent " Trequency Dependent" need to use calculus to use this relationship. It is important to understand a comple of Concepts from calculus. Notation Low Frequencies: open circuit or infinite resistor dv = i. E

dv = i - L

elastance E= L capacitance

the implied.

dv : derivative of voltage over time equation

at rate of change in voltage

Slope of voltage High Frequencies: short circuit or no resistar In the middle: some resistance. Examples: Basic Filters. De Vout De Vi De Vi Vo= O For a function/signal we can determine its HPF

Low

High

Single node Slope at some point in time Simplified example
with out calculus notation

slope Notation f'(x) $\frac{3+1}{2} = \frac{2}{3}$ Now we have concept, let's study equation. This is how we calculate derivative for digital signal

Audio signal X[n]

X[s] 9×[6] X[6] Slope = X[6]-X[5]

Ts Another important calculus concept: integral Notation $\int_{1}^{\infty} x(t) dt \implies \sum_{1}^{\infty} x[t]$ Sum underneath curve of function $\int_{1}^{10} f(x) dx = \int_{1}^{10} f(x$ Sav dt = V Therefore: Sav dt = Si-L dt 事量V=Si-Ldt Voltage equals sum of current over time Voltage accumulates current over time If all this calculus is new to you, don't worry. We can avoid a bot of it, if we use a tranform. Simplied Notation, math steps casier.

(2) (28) Laplace Transform (Similar to 7-transform) $V(t) = \frac{1}{C} \int i(t) dt \implies V(s) = \frac{1}{sC} \cdot I(s)$ $V = \frac{I}{SC} = \frac{V}{I} = \frac{1}{SC} = \frac{1}{SC} = \frac{1}{SC}$ impedance Apply to circuit example: Vin (*) CT Unit Viss ZeT Vo(s) Voltage Francistor V=I.ZA V=I.R voltage across capaction V= I.Zc V=I·I Solve circuit using "2"s & Chin's Law Substitute into transfer function. Vi(s)-Vo(s) = Vo(s)-0 Vi(s) = Vo(s) + Vo(s) = ZR ZR Vi(5) - Vo(5) (1+1) Vi(5) = Vo(5) (ZR+ZE)

ZR ZR ZR Villa) . Ze Ze Vola) = Vola) Vo(6) = Zc TF Vi(6) = Zr+Zc form

Substitute into transfer function. Ze = sc Vo(5) = Zc Ze = R Vi(s) ZR+Ze Vo(s) = $\frac{1}{sc}$ Multiply all $\frac{1}{sc}$ sc $\frac{1}{sc}$ $\frac{1}{sc}$ $\frac{1}{sc}$ $\frac{1}{sc}$ $\frac{1}{sc}$ $\frac{1}{sc}$ $\frac{1}{sc}$ Vols) = 1 Standard form for LPF Vo(s) = RC Alternative.

Vi(s) = s+ L This form allows us to do several things Domain Filters 5=jw look at frequency

Spectrum

Spectrum

Spectrum

We=1 rad

Fe sec fe = 2π fe Hz Now we know the transfer function of analog files Next we convert & analog to digital. Use bilinear transform map continuous to discrete "Trapezordal Rule" Z=est T-sampling period approximation $S = \frac{Z}{T} \frac{(z-1)}{(z+1)}$ let $k = \frac{Z}{T}$ sometimes T different. $S \rightarrow \frac{k(z-1)}{(k+1)}$ any where we have "s", plug in this to get Z-domain TF

(29) (30) HIZ]= WC = $\frac{(30)}{[2+1]} + [2] = \frac{Wc}{(2+1) + wc(2+1)} = \frac{Wc}{(2+1) + wc(2+1)}$ $H[z] = \frac{\omega_c(z+1)}{k(z-1) + \omega_c(z+1)} = \frac{\omega_c z + \omega_c}{kz - k + \omega_c z + \omega_c}$ H[Z] = WcZ+wc Multiply all terms
(We+k)Z+(Wc-k) by Z-1 $H[z] = \frac{w_c + w_c z^{-1}}{(w_c + k) + (w_c - k) z^{-1}} \quad b_o = w_c \quad b_i = w_c$ $(w_c + k) + (w_c - k) z^{-1} \quad a_o = w_c + k \quad a_i = w_c - k$ Want do = I divide all turns by do bo = we bi = we+k do=1 di = we-k we+k $y[n] = b_0 \times [n] + b_1 \times [n-1] - a_1 y[n-1]$ Costo $k = \frac{Z}{Ts} = 2.Fs$ we get frequency warping. We can pick one frequency in the spectrum and make sure amp |H(s) = |H[z] if we let $k = Z\pi f$ Typically we pick most important frequency to be fcK= ZH fc = ZHZHRC = RC tan (H fc) ten (T zhec) tan (Tec)

(31) (52) Turn H(s) -> H[7] S->K(2-1) $H[z] = \frac{K(z-1)}{z+1} = \frac{K(z-1)}{z+1} = \frac{K(z-1)}{K(z-1)+w_c(z+1)}$ $H[z] = \frac{K(z-1)}{k(z-1)+w_c(z+1)} = \frac{Kz-k}{kz-k+w_cz+w_c}$ H[7] = KZ-K . Multiply by 2-1 (K+Wc)Z+(Wc-K) $H[z] = \frac{k - k z'}{(w_c + k) + (w_c - k) z'} \quad b_o = k \quad b_i = -k$ $(w_c + k) + (w_c - k) z' \quad a_o = w_c + k \quad a_i = w_c - k$ we want do=1 so divide by watk $b_0 = \frac{K}{w_c + k} \quad b_i = \frac{-K}{w_c + k} \quad d_o = 1 \quad a_i = \frac{w_c - k}{w_c + k}$ y[m] = bo X[m] + b, X[m-i] - a, y[m-i] We can apply this approach to more complicated circuits involving R&C's. Examples: Baxandall Bennett AES Bassman Tonestack Yeh Smith Dafx

Schenatic -> Laplace Transform -> Bilinan Transform -> Inverse Z When circuit is complicated, that's a lot GDifference Eq of steps to do by hand. Tools like Mathematica Wolfram Alpha can help. Solution still messy.

Alternative: Discretize Schenatic from stat. substitute components with discrete approximate, specifically capacitor (memory storage) using trapezoidal rule.

Discrete Schenatic -> Difference Equation.

Equation to capacitar: apply trapezoidal rule duli = i(t) 1

Current
Current
Suple
Suple dv(x) change in voltage -> V[u] -V[n-i]

 $V[n] - V[n-1] = \frac{T_s}{2C} \left(i[n] + i(n-1]\right)$ relationship between current & voltage based entirely on discrete time

When we apply KCL to circuit we want to know current i[n] = ~~

current stuff from past voltage Define a "state" for stuff from past $X[n-i] = \underbrace{ZC}_{Ta} V[n-i] + i[n-i]$

Side note: from KCL if we have a current $i[n] = i_1[n] - i_2[n]$

Current
$$[[n]]$$

$$|[i[n]]| = [i_1[n] - [i_2[n]]$$

$$|[i[n]]| = [i_1[n]] - [i_1[n]]$$

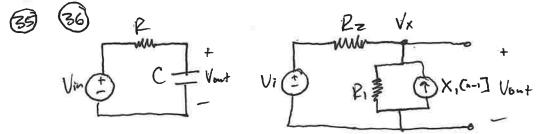
$$|[i[n]]| = [i_1[n]]$$

$$|[i[n]]| = [i_1[n]] - [i_1[$$

Nice form
$$i, [i,] = V[i,]$$
 R
 $let R = Ts$
 R
 $l[u] = V[u]$
 R
 R

How to handle state X[11-1]? Initialize state x[0]=0 How does X[n-i] change over time? We defined $\times [u-i] = \frac{V[u-i]}{P} + i[u-i]$ Therefore X[n] = V[n] + i[n] We already found ital [[n] = V[n] - X[n-1] Plug in X[n] = V[n] + V[n] - ×[n-1] $\times [n] = \frac{2}{R} \times [n] - \times [n-1]$

Every sample we will calculate X[u] and use for subsequent sample as X[u-i] Now let's apply this to some circuits to see how it works.



Node V_X Current in = current out $\frac{V_i - V_o}{R_z} = \frac{V_o}{R_1} - X_1[u-1]$ $\frac{V_i}{R_z} + X_1[u-1] = \frac{V_o}{R_1} + \frac{V_o}{R_z}$ $\frac{V_i}{R_z} + X_1[u-1] = V_o\left(\frac{1}{R_1} + \frac{1}{R_2}\right) = V_o\left(\frac{R_1 + R_2}{R_1R_2}\right)$ $V_o = V_i\left(\frac{R_1}{R_1 + R_2}\right) + \left(\frac{R_1R_2}{R_1 + R_2}\right) \times [(u-1)]$

 $X_1[n] = \frac{2}{P_1} \cdot V_0 - X_1[n-1]$ (3) DKreFiller

we calculate this first for each sample so we can use it to update state.

In just a few steps we converted this circuit to a difference equation that can easily be implemented as code.

More examples build up to Tube Screme.

$$\frac{\sqrt{i-V_0}-X_1[n-i]}{P_1}=\frac{V_0}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-X_1[n-i]}=\frac{V_0}{P_1}+\frac{V_0}{P_2}=\frac{V_0}{P_2}=\frac{1}{P_1}+\frac{1}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-X_1[n-i]}=\frac{V_0}{P_1}+\frac{P_2}{P_2}=\frac{V_0}{P_1}+\frac{1}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-\frac{X_1[n-i]}{P_1}=\frac{V_0}{P_1}+\frac{P_2}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-\frac{X_1[n-i]}{P_1}=\frac{V_0}{P_1}+\frac{P_2}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-\frac{P_1}{P_2}-\frac{P_1}{P_1}+\frac{P_2}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-\frac{P_1}{P_2}-\frac{P_1}{P_2}+\frac{P_2}{P_1}$$

$$\frac{\sqrt{i}}{P_1}-\frac{P_1}{P_2}-\frac{P_1}{P_2}+\frac{P_2}{P_2}$$

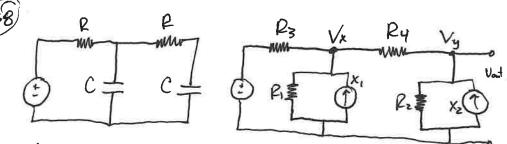
$$\frac{\sqrt{i}}{P_1}-\frac{P_1}{P_2}-\frac{P_1}{P_2}+\frac{P_2}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-\frac{P_1}{P_2}-\frac{P_1}{P_2}+\frac{P_2}{P_2}$$

$$\frac{\sqrt{i}}{P_1}-\frac{P_1}{P_2}-\frac{P_1}{P_2}+\frac{P_2}{P_2}$$

Both are known to current sample before updrating state.

Next Example: 2nd - Order RC Filter 2 states, parallel components



Node VxCurrent in Current ont $\frac{Vi - Vx}{R_3} = \frac{Vx}{R_1} - X_1[m_1] + \frac{Vx - Vont}{R_4}$

We will need to replace Vx in equation use node Vy and then come back.

Node Vy:

$$\frac{\sqrt{x-V_0} = \frac{V_0}{R_z} - X_z}{R_4} = \frac{V_x}{R_4} = \frac{V_0}{R_z} + \frac{V_0}{R_4} - X_z$$

solve for

Vo diff- EQ

Solve Node Vx

Vo (Ry+Pz)-RyXz = Vi + Vo + X1 RyGx GX

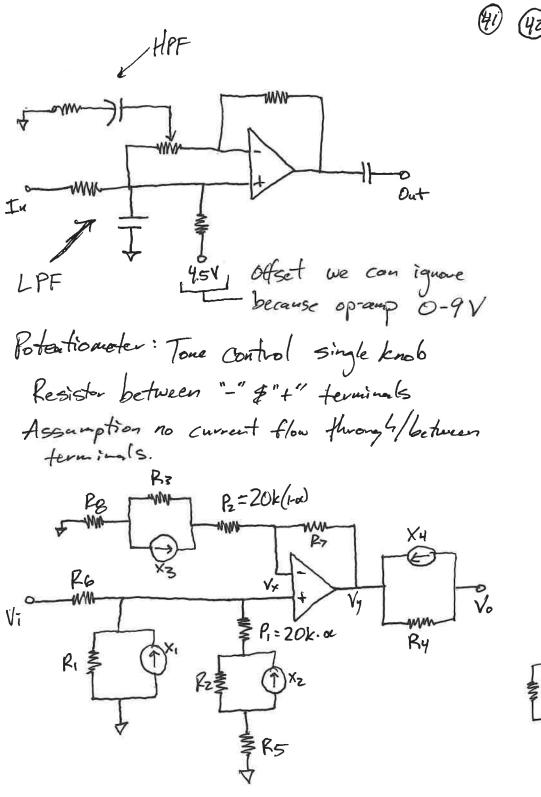
$$V_0 \left[\frac{R_4 + R_2}{R_2} - \frac{1}{R_4 G \times} \right] = \frac{V_i}{R_3 G \times} + \frac{X_1}{G \times} + \frac{X_2 \cdot R_4}{G \times}$$

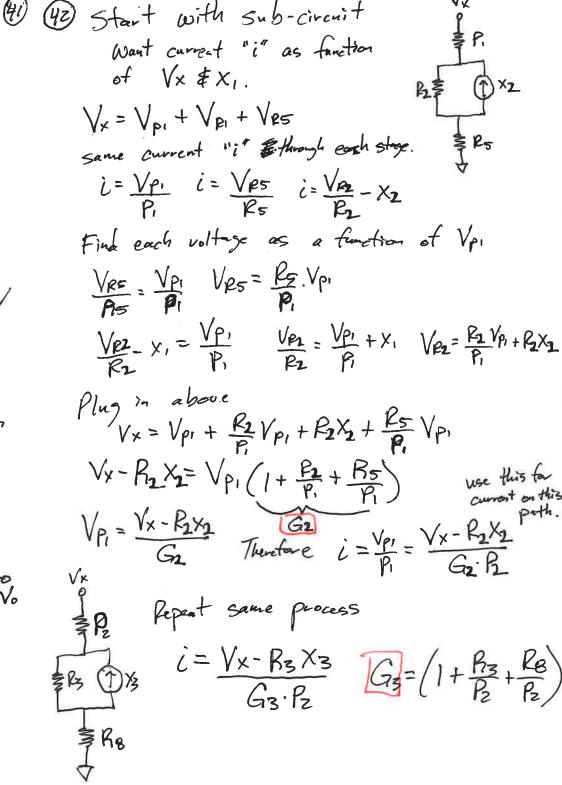
$$b_0 \quad b_1 \quad b_2$$

State update equations X2[n]= = Vo - X2[n-1] XI[n] = Z Vx - XI[n-1] We will need to find Vx in terms of in/ont & states Vx = Vo (Ru+Rz) - Ruxz we already found this, other option has more computations. Make sure to calculate Vx before Xz (4) DKrc2nd Order Sallen-Key 2nd Order Filter (w/ resonance a)
Active Filter includes op-amp Vo by Vx Vx by Vy Vy by ViŧVo

(39) (40) Start with current from Vor+ - ground. Vout - Vx = Vx
RB Vout = VX (PA+RB) = VX (RA+RB) = VX (RA+RB) VX = Vous (RB) Save for substitution Node Ux $\frac{V_{y}-V_{x}}{P_{y}}=\frac{V_{x}}{P_{t}}-X_{t}$ $\frac{V_{y}}{P_{y}}=V_{x}\left(\frac{1}{P_{t}}+\frac{1}{P_{y}}\right)-X_{t}$ $V_y = V_x \cdot R_4 \left(\frac{1}{R_1} + \frac{1}{R_4} \right) - R_4 \times_1 = V_x \left(\frac{R_4}{R_1} + 1 \right) - R_4 \times_1$ Vy=Vo(RB)(R1+1)-R4X1 Node vy Vi-Vy = Vy-Vx + Vy-Vo-Xz isolate Vi + Vx + Vo + Xz = Vo · Gy · (RB) (Ry+1) - Ry Gy·X, Vi + Vo (RB) + Vo + X2 = Vo Gy (RB) (Ry +1) - RGX Vo (Gy (RB) (RH) - 1 - RB + RB (RH) = Vi + R4 Gy X, + XZ

a. b. b. b. X[n]= = Vx - X,[n-1] Xz[n]= Z (1y-Vo) - Xz[n-i]





Output Branch Vy Ra Vio Current going through each component is equal. Write everything as a function of Vo for difference equation. Vb-Vo = Vo RIO Vb = Vo(1+ RIO) Vb = Vo(1+ RIO) $\frac{V_{a}-V_{b}}{R_{q}}=\frac{V_{o}}{R_{II}} \qquad V_{a}-V_{b}=\frac{R_{q}}{R_{I}}V_{o} \qquad V_{a}-\frac{R_{q}}{R_{II}}V_{o}=V_{b}$ $V_{a} - \frac{R_{a}}{R_{II}} V_{o} = V_{o} \left(1 + \frac{R_{Io}}{R_{II}} \right) \quad V_{a} = V_{o} \left(1 + \frac{R_{io}}{R_{II}} + \frac{R_{9}}{R_{II}} \right)$ $\frac{V_{y}-V_{4}}{R_{4}}-X_{4}=\frac{V_{0}}{R_{11}}\frac{V_{y}-V_{a}}{R_{4}}=\frac{V_{0}}{R_{11}}+X_{4}$ Vy-Va = Ry Vo + Ruxu Vy - Ry Vo - Ry Xy = Va Vy - Ky Vo - Ry Xy = Vo (1 + R10 + R9) Vy = Vo (1+ R10 + R9 + R4 + R11) + R4 X4 Save for later. Now we need to connect branches together.

Node Vx "-" Terminal current flows Vy -> Vx Vy-Vx = Vx - R3 X3 (From previous page) Isolate Vy to plug in Vy-Vx = R7 Vx - R7 R3 X3 Vy = Vx + R7 Vx - R7 R3 X3 G3 R2 a plug in Vy Vo(Go) + Ry Xy = Vx (1+ R7)- R3 R7 X3 Isolate VX Vo. G. + Ry Xu + R3 R7 X3 = Vx (1+ R7) VX = Go. Vo + R3R7 + R4 X4

GXG3P2 + GX X4 now we need to connect input soutput "+" terminal to "-"

Node
$$V_X$$
 "+" terminal

 $\frac{Vi - Vx}{Rb} = \frac{Vx}{Ri} - X_1 + \frac{Vx}{Gz} \frac{R_2 X_z}{Gz}$ (from previous)

Tsolate V_X
 $\frac{Vi}{Rb} + X_1 + \frac{R_2 X_z}{Gz} = \frac{V_x}{R_1} + \frac{1}{G_z} + \frac{1}{R_b}$
 $V_X = \frac{Vi}{R_b Gz} + \frac{R_2 X_z}{Gz} = \frac{V_x}{Gz} + \frac{R_2 X_z}{Gz}$ set equal to other V_X
 $\frac{Vi}{R_b Gz} + \frac{X_1}{Gz} + \frac{R_2}{Gz} \times \frac{Z}{Gz} = \frac{V_0}{G_0} + \frac{R_3 R_7}{G_X} \times \frac{X_3}{G_X} + \frac{R_4}{G_X} \times \frac{X_4}{G_X}$

Solve for V_0 for difference equation

 $\frac{Vi}{R_b Gz} + \frac{X_1}{Gz} + \frac{R_2}{Gz} \times \frac{Z}{Gz} - \frac{R_3 R_7}{Gx} \times \frac{X_3}{Gx} - \frac{R_4}{Gx} \times \frac{V_1}{Gx} = \frac{V_0}{Gx} \times \frac{V_1}{Gx} + \frac{G_1}{Gx} \times \frac{V_1}{Gx} + \frac{G_2}{Gx} \times \frac{V_1}{Gx} + \frac{G_2}{Gx} \times \frac{V_1}{Gx} + \frac{G_2}{Gx} \times \frac{V_1}{Gx} + \frac{G_2}{Gx} \times \frac{X_1}{Gx} \times \frac{X_1}{Gx} + \frac{G_2}{Gx} \times \frac{X_1}{Gx} + \frac{G_2}{Gx} \times \frac{X_1}{Gx} + \frac{G_2}{Gx} \times \frac{X_1}{Gx} + \frac{G_2}{Gx} \times \frac{X_1}{Gx} + \frac{G_2}{Gx}$

(46) State update quations [X, [n] = Z. Vx - X, [n-1] Treed to calculate. Xz[n] = Z VRZ - Xz[n-1] more complicated. Let's find Vez as function of Vx Use Vx = Vp, + VR2 + VR5 write all terms with VRZ, use currents egeal VPI = VRZ - XZ VPI = PIVEZ PIXZ VR5 = VRZ - XZ VR5 = R5 VRZ - XZR5 now plug in VX = P1 VR2 P1 X2 + VR2 + R5 VR2 - X2R5 Solve for VRZ Vx+P1Xz+R5Xz=VRZ(P1+1+R5) Vez = $\frac{V \times}{G_R} + \left(\frac{R_1 + R_5}{G_R}\right) \times z$ Plug into original state equation Xz[n]= 2 (Vx + (P+R5) Xz[n] - Xz[n]

We can repeat exact samp process for X3. Here are the results.

Output Branch