

# MEEC/MIEEC

## ELECTRONICS FOR MICRO-SYSTEMS

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### Lab#1 P1 A Temperature Meter System with 3 Sensors, Relay and GUI

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# Contents

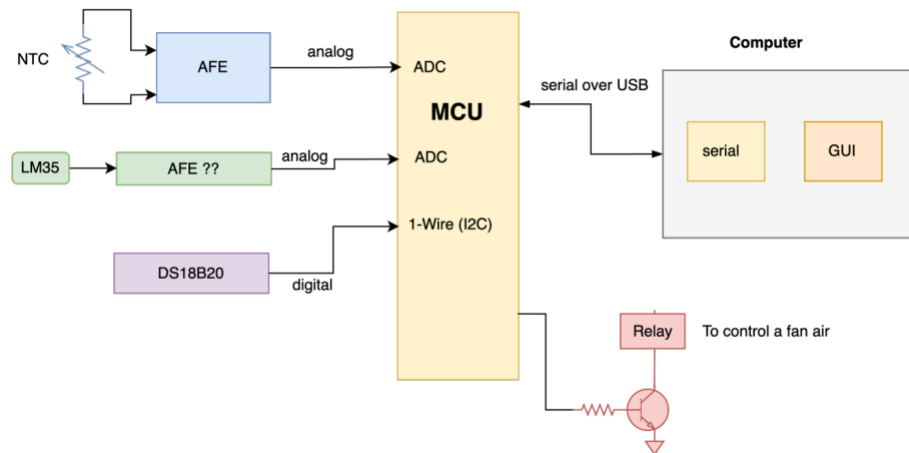
<b>1</b>	<b>Introduction</b>	<b>3</b>
<b>2</b>	<b>Temperature Sensors</b>	<b>4</b>
2.1	NTC - Negative Temperature Coefficient . . . . .	4
2.2	LM35 - Precision Centigrade Temperature Sensor . . . . .	4
2.3	DS18B20 - Digital Thermometer . . . . .	4
<b>3</b>	<b>System Design</b>	<b>4</b>
3.1	Analog FrontEnd (AFE) NTC . . . . .	4
3.2	LM35 . . . . .	6
3.3	Output Relay . . . . .	7
<b>4</b>	<b>Simulations</b>	<b>9</b>
4.1	NTC . . . . .	9
4.2	LM35 . . . . .	11
<b>5</b>	<b>Implementation and Experimental Tests</b>	<b>12</b>
<b>6</b>	<b>Results Analysis</b>	<b>12</b>
<b>7</b>	<b>Conclusion</b>	<b>12</b>

## List of Figures

1	Temperature sensing system with 3 three types of sensors. . . . .	3
2	NTC voltage divider. . . . .	4
3	Output Voltage vs Temperature . . . . .	5
4	NTC's AFE Differential Amplifier . . . . .	5
5	NTC's AFE topology . . . . .	6
6	LM35 Differential Amplifier. . . . .	7
7	AFE for the LM35 sensor. . . . .	7
8	Relay circuit. . . . .	8
9	Linear supply NTC test circuit. . . . .	9
10	Linear supply NTC test results. . . . .	9
11	Realistic NTC test circuit. . . . .	10
12	Realistic NTC test results. . . . .	10
13	NTC Linear and Realistic results comparison. . . . .	11
14	Relay circuit. . . . .	11
15	Relay circuit. . . . .	12

# 1 Introduction

explain the requirements and the main objectives of the project Se calhar dizer aqui quais sao as metricas por onde podemos avaliar a nossa solucao Linearidade de output para conseguir aproveitar melhor a resolucao do adc Consumo erro



**Figure 1:** Temperature sensing system with 3 three types of sensors.

## 2 Temperature Sensors

### 2.1 NTC - Negative Temperature Coefficient

### 2.2 LM35 - Precision Centigrade Temperature Sensor

### 2.3 DS18B20 - Digital Thermometer

## 3 System Design

### 3.1 Analog FrontEnd (AFE) NTC

A parte onde definimos o intervalo de temperatura nao devia estar aqui pq é para todos os circuitos

In order to design the *NTF AFE* first it's necessary to define the temperature interval in which this circuit will work, thus it was define as  $T \in [10^\circ; 40^\circ]$ . Through the *NTC*'s datasheet the interval of its resistance values is  $R_{NTC} \in [5,282k ; 19,98k]$

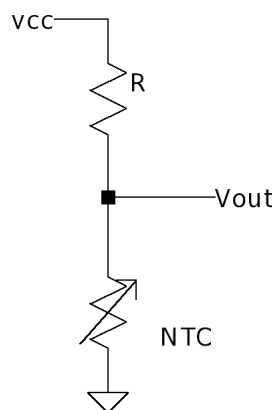
For an accurate reading of the temperature it was used the *Steinhart-Hart equation*.

$$\frac{1}{T} = A + B \cdot \ln(R_{NTC}) + C \cdot [\ln(R_{NTC})]^3 \quad (1)$$

In order to find the parameters  $A$ ,  $B$  and  $C$ , its necessary to use 3 points from the datasheet. The points chosen were the two extremes and the middle point.

$$\begin{cases} R(283, 15) = 1,998 \cdot 10^4 \Omega \\ R(298, 15) = 10^4 \Omega \\ R(313, 15) = 0,5282 \cdot 10^4 \Omega \end{cases} \Leftrightarrow \begin{cases} A = 1,2 \cdot 10^{-3} \\ B = 2,1 \cdot 10^{-4} \\ C = 1,3 \cdot 10^{-7} \end{cases} \quad (2)$$

The simplest way to convert the resistance to voltage, is to use a voltage divider circuit.



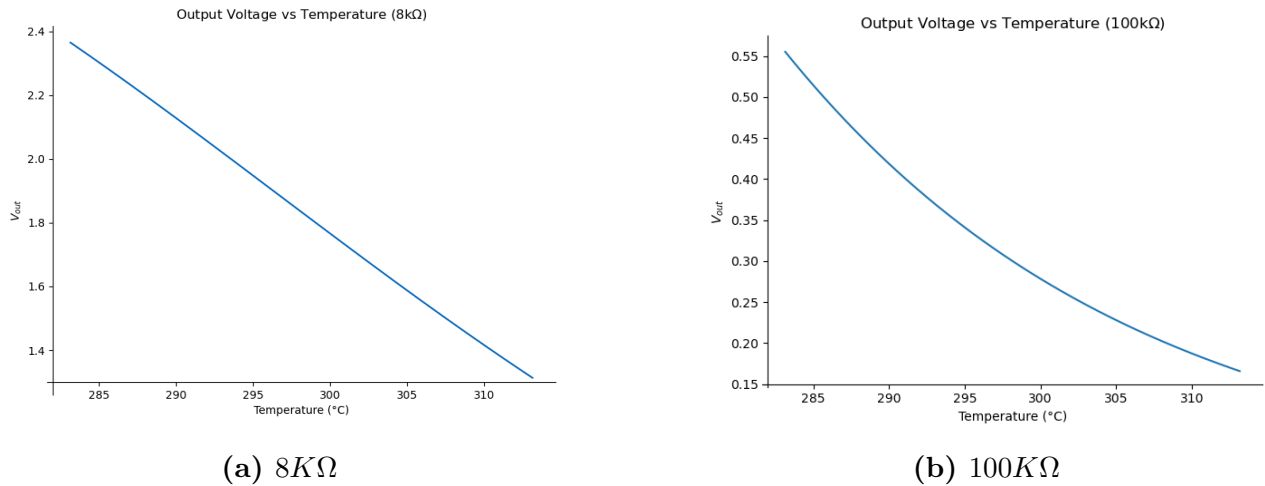
**Figure 2:** NTC voltage divider.

This approach has a few problems:

- The output resistance is really high  $R \parallel NTC$ ,

· The output voltage is highly non linear, which is a problem because this way some *ADC* resolution is lost.

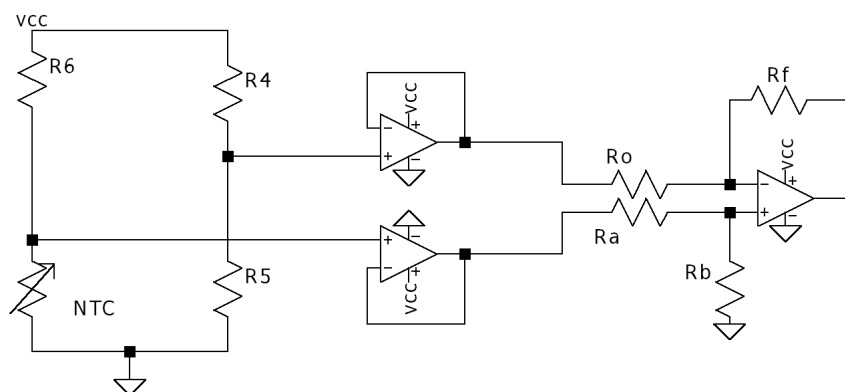
The first problem is solved through a buffer at the entrance of the *AFE*, and the second is somewhat mitigated by using a resistor value around  $8K\Omega$ . To achieve this value it was used the beta model for the *NTC* and through a python script, the resistor value was iterated until an almost linear output was achieved. See `NtcTempToVoltage()` function in the attached python script.



**Figure 3:** Output Voltage vs Temperature

With  $R = 8k$  the output signal is  $V_{out} \in [1.31V; 2.36V]$ . But the *ADC* as resolution of 12 bits and a voltage range of  $3.3V$ , meaning that to have the best resolution possible the signal needs an offset and a gain of 2.87 and  $-1.75$  respectively. It's important to note that a  $0.1V$  margin was added to  $V_{out}$  in order to ensure that the *AFE* works properly in the specified range.

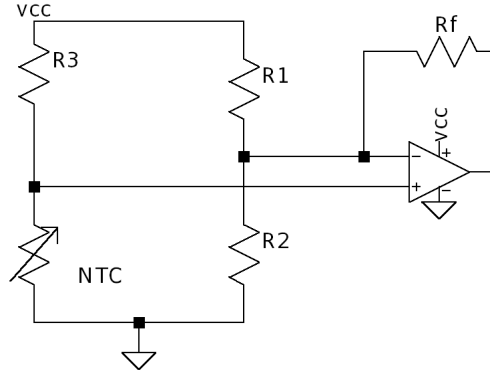
The most obvious way to achieve this values would be with a differential amplifier, as seen in the following circuit.



**Figure 4:** NTC's AFE Differential Amplifier

Although in the simulation this yields the expected result buy using the

This gain and offset was achieved through the following topology.



**Figure 5:** NTC's AFE topology

*Nao esquecer de fazer referencia ao codigo funcao AfeNtc()*

As already specified the  $R_3$  value is  $8K \Omega$  and for the purpose circuit dimensioning the circuit the positive node of the opamp will be treated as a variable  $V_p$ .

Using python the circuit function  $V_{out}(V_p)$ :

$$V_{out} = V_p \left[ 1 + R_f \cdot \left( \frac{1}{R_1} + \frac{1}{R_2} \right) \right] - V_{cc} \cdot \frac{R_f}{R_1} \quad (3)$$

Now it's clear to see what part of the equation is responsible for the circuit gain and offset.

$$\begin{cases} V_{offset} = -V_{cc} \cdot \frac{R_f}{R_1} \\ G = \left[ 1 + R_f \cdot \left( \frac{1}{R_1} + \frac{1}{R_2} \right) \right] \end{cases} \quad (4)$$

Setting  $R_f = 10K$  the values that satisfy the equation system are  $R_1 = 9348\Omega$  and  $R_2 = 11760\Omega$ . In the simulation this values are a bit too close to the limits so in order to have better margins and to use the available resistors the final values are  $R_1 = 8.2K\Omega$  and  $R_2 = 12K\Omega$ .

## 3.2 LM35

This integrated-circuit temperature sensor, generates an output voltage linearly proportional to the Centigrade temperature.

$$V_{out} = 10^{-2} \cdot T$$

Hence, in the specified conditions  $V_{out} \in [0.1; 0.4]$ . To increase resolution as done in [NTC's AFE subsection](#), it's needed to add gain and an offset. For this purpose a differential amplifier can be used.

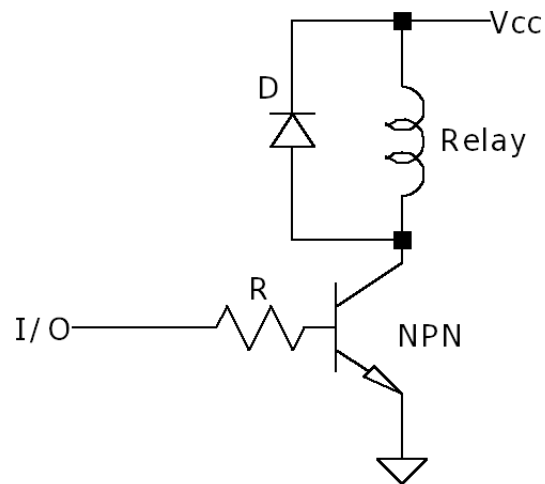


**Figure 7:** AFE for the LM35 sensor.

### 3.3 Output Relay

Lastly the *MCU* will turn on and off a fan. For this purpose a relay will open and close the fan circuit. Because the *MCU's* *I/O* can't drive the relay a *NPN* transistor is use to get enough current. Thus the following circuit also needs to be dimensioned. It's important to note that the diode is needed to protect the circuit.





**Figure 8:** Relay circuit.

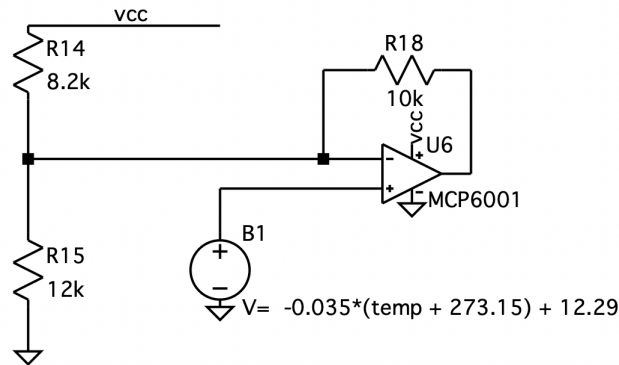
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$$R = \frac{I/O_{HIGH} - V_{BE}}{I_r} \cdot \beta \Leftrightarrow R = \quad (5)$$

## 4 Simulations

### 4.1 NTC

For the purpose of testing the following circuit was designed. At first with linear voltage supply since as seen in section, this voltage is almost linear.



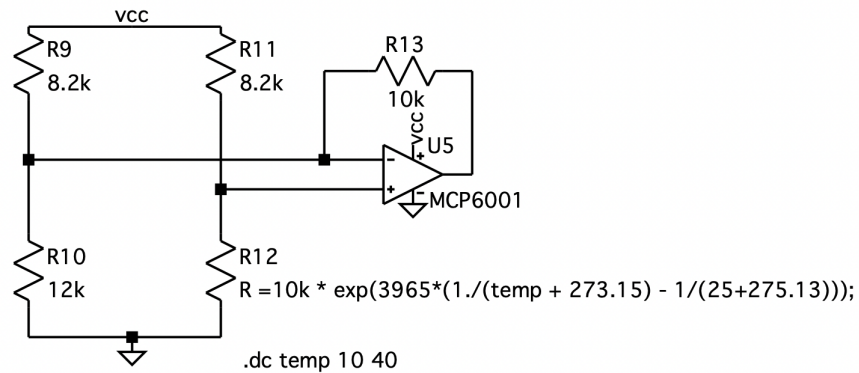
**Figure 9:** Linear supply NTC test circuit.

Producing the following results.



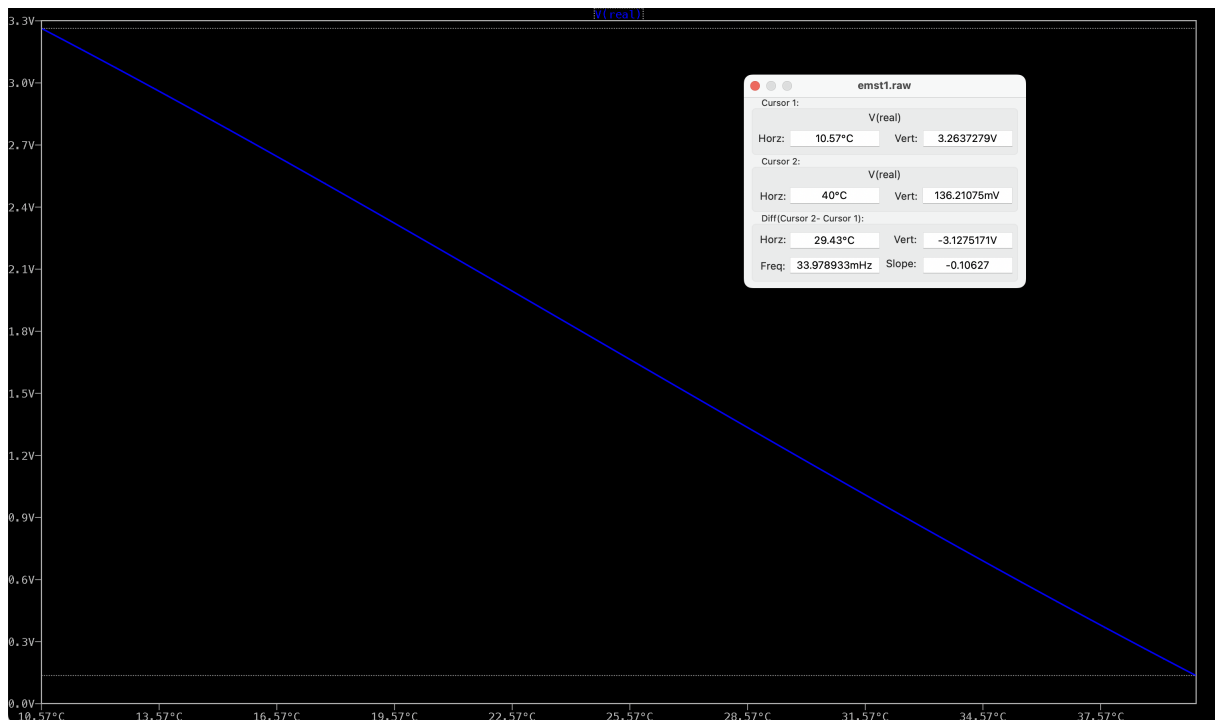
**Figure 10:** Linear supply NTC test results.

Confirming the effectiveness of the *AFE* circuit a more realistic circuit was then tested.



**Figure 11:** Realistic NTC test circuit.

With the following results.



**Figure 12:** Realistic NTC test results.

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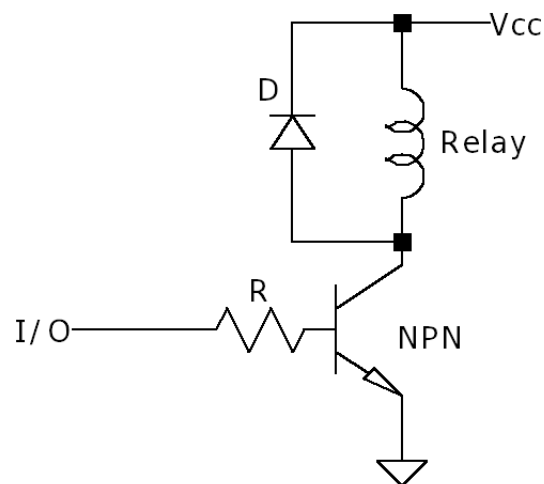
Comparing both results the difference is minimal. Confirming that the output is nearly linear in relation to the temperature variation.



**Figure 13:** NTC Linear and Realistic results comparison.

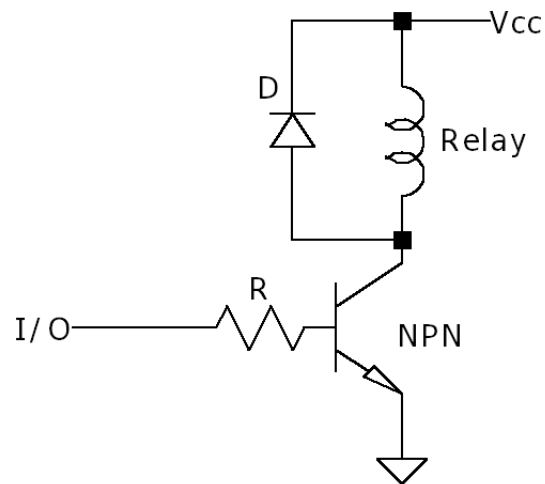
## 4.2 LM35

For this sensor the simulation is straight forward, since the output is linear and proportional to the temperature, it can be simulated with variable voltage supply.



**Figure 14:** Relay circuit.

Giving the following results.



**Figure 15:** Relay circuit.

## 5 Implementation and Experimental Tests

## 6 Results Analysis

## 7 Conclusion